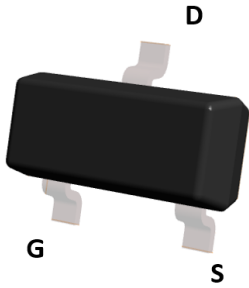
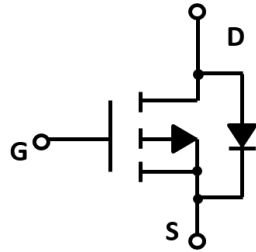
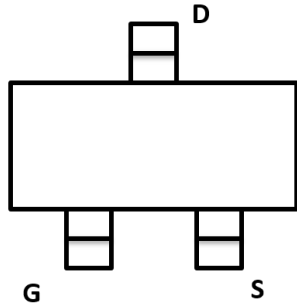


P-Channel Enhancement Mode Field Effect Transistor



Top View

SOT-23



Product Summary

- V_{DS} -30V
- I_D -4.4A
- $R_{DS(ON)}$ (at $V_{GS}=-10V$) <55 mohm
- $R_{DS(ON)}$ (at $V_{GS}=-4.5V$) <68 mohm
- $R_{DS(ON)}$ (at $V_{GS}=-2.5V$) <96 mohm

General Description

- Trench Power LV MOSFET technology
- High density cell design for Low $R_{DS(ON)}$
- High Speed switching

Applications

- Battery protection
- Load switch
- Power management

■ Absolute Maximum Ratings ($T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Maximum	Unit
Drain-source Voltage	V_{DS}	-30	V
Gate-source Voltage	V_{GS}	± 12	V
Drain Current	I_D	$T_A=25^\circ C$ @ Steady State	-4.4
		$T_A=70^\circ C$ @ Steady State	-3.5
Pulsed Drain Current ^A	I_{DM}	-27	A
Total Power Dissipation @ $T_A=25^\circ C$	P_D	1.2	W
Thermal Resistance Junction-to-Ambient @ Steady State ^B	$R_{\theta JA}$	105	$^\circ C/W$
Junction and Storage Temperature Range	T_J, T_{STG}	-55~+150	$^\circ C$

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	Marking	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
YJL3401A	F2	3401.	3000	30000	120000	7" reel



YJL3401A

■ Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-30V, V_{GS}=0V, T_C=25^\circ\text{C}$			-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 12V, V_{DS}=0V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-0.6	-0.9	-1.4	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=-10V, I_D=-4.4A$		45.5	55	m Ω
		$V_{GS}=-4.5V, I_D=-4A$		52	68	
		$V_{GS}=-2.5V, I_D=-2A$		64	96	
Diode Forward Voltage	V_{SD}	$I_S=-4.4A, V_{GS}=0V$		-0.8	-1.2	V
Maximum Body-Diode Continuous Current	I_S				-4.4	A
Dynamic Parameters						
Input Capacitance	C_{iss}	$V_{DS}=-15V, V_{GS}=0V, f=1\text{MHz}$		680		pF
Output Capacitance	C_{oss}			105		
Reverse Transfer Capacitance	C_{rss}			68		
Switching Parameters						
Total Gate Charge	Q_g	$V_{GS}=-10V, V_{DS}=-15V, I_D=-4.4A$		7.2		nC
Gate Source Charge	Q_{gs}			1.2		
Gate Drain Charge	Q_{gd}			1.6		
Turn-on Delay Time	$t_{D(on)}$	$V_{GS}=-10V, V_{DD}=-15V, R_L=15\Omega, I_D=-1A, R_{GEN}=2.5\Omega$		15		ns
Turn-on Rise Time	t_r			63		
Turn-off Delay Time	$t_{D(off)}$			21		
Turn-off Fall Time	t_f			12		

A. Pulse Test: Pulse Width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

B. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.



■ Typical Performance Characteristics

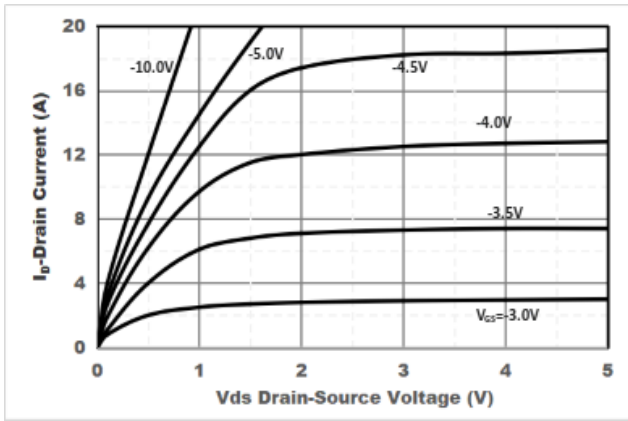


Figure1. Output Characteristics

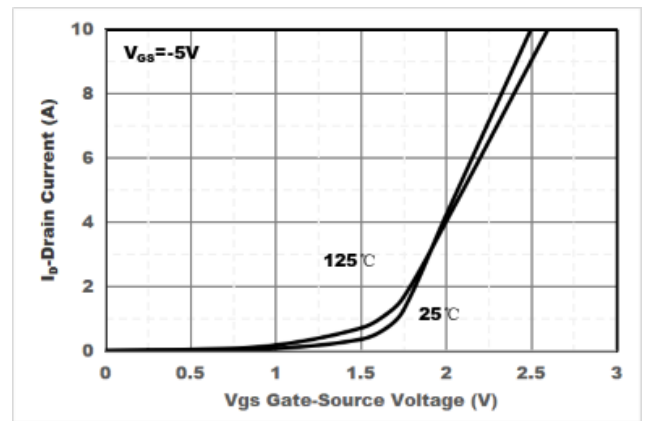


Figure2. Transfer Characteristics

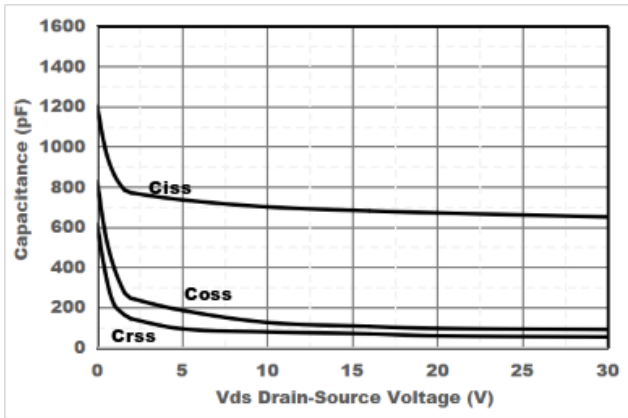


Figure3. Capacitance Characteristics

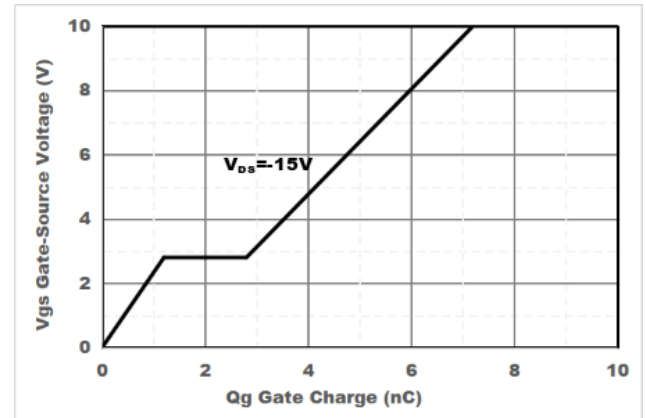


Figure4. Gate Charge

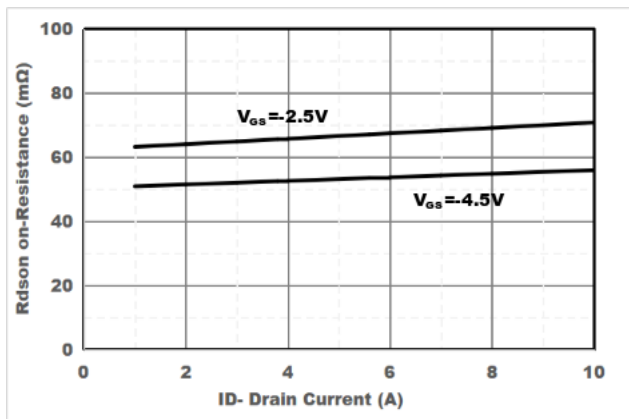


Figure5. Drain-Source on Resistance

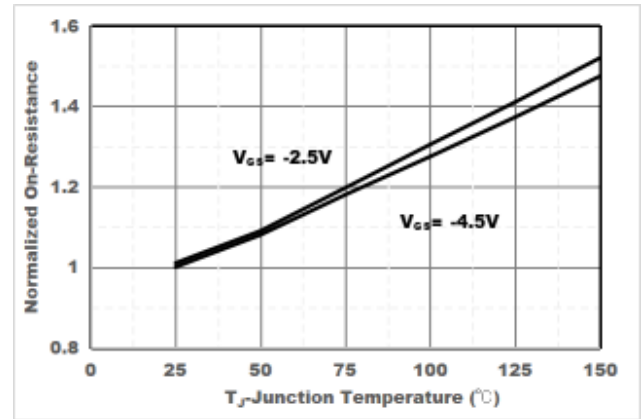


Figure6. Drain-Source on Resistance

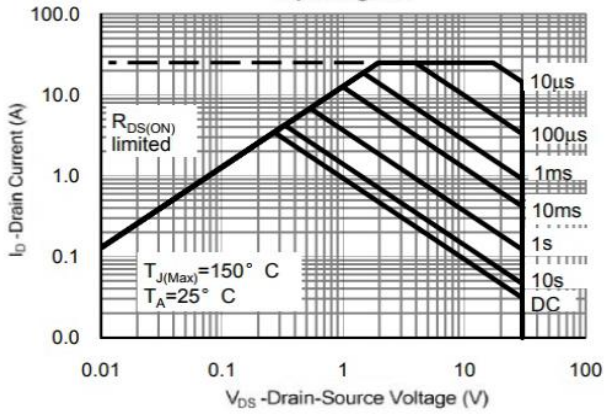


Figure7. Safe Operation Area

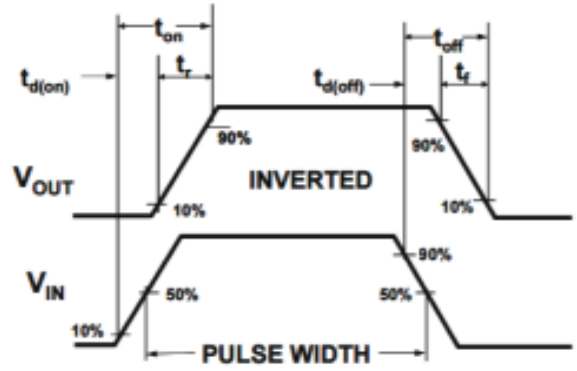
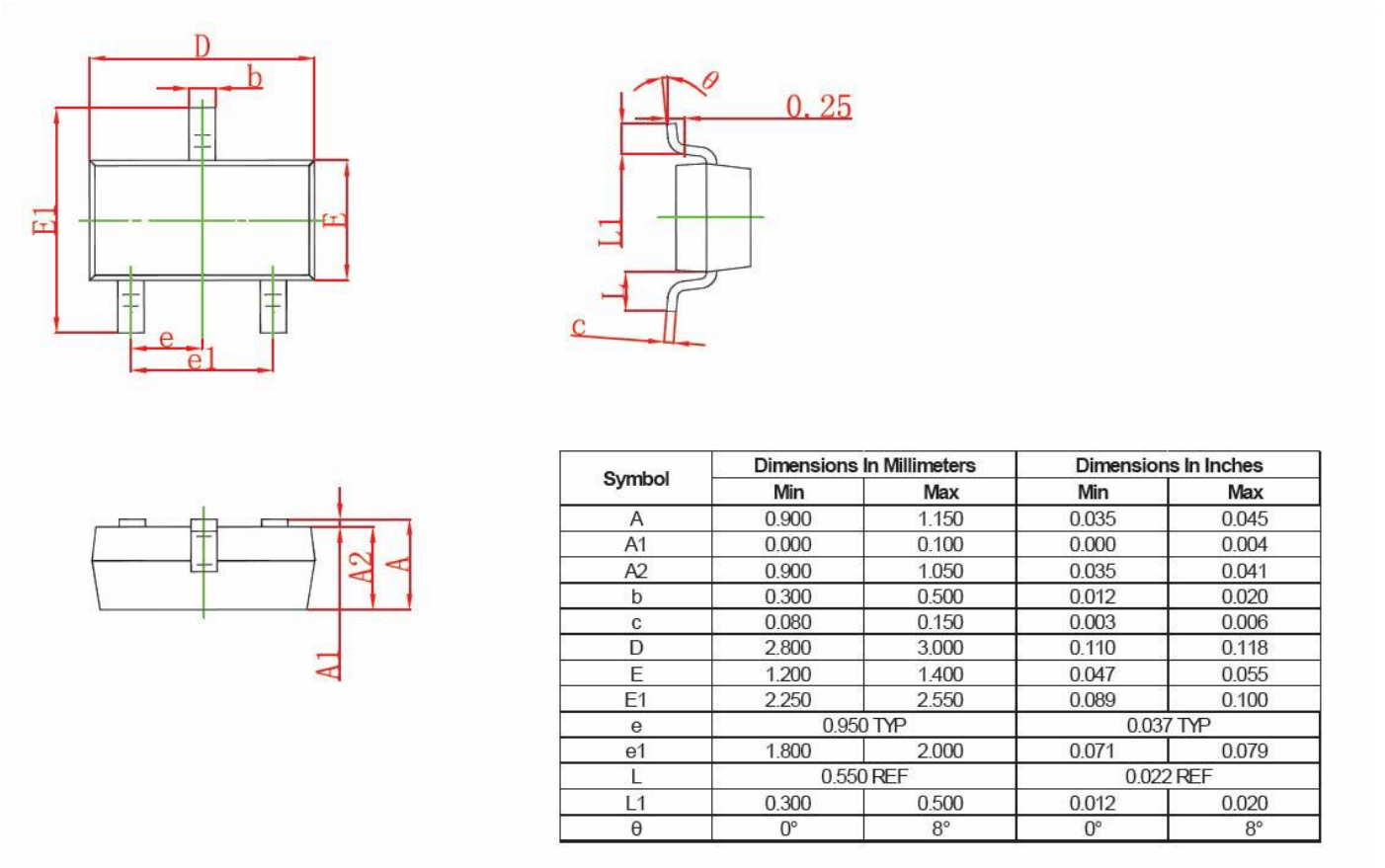
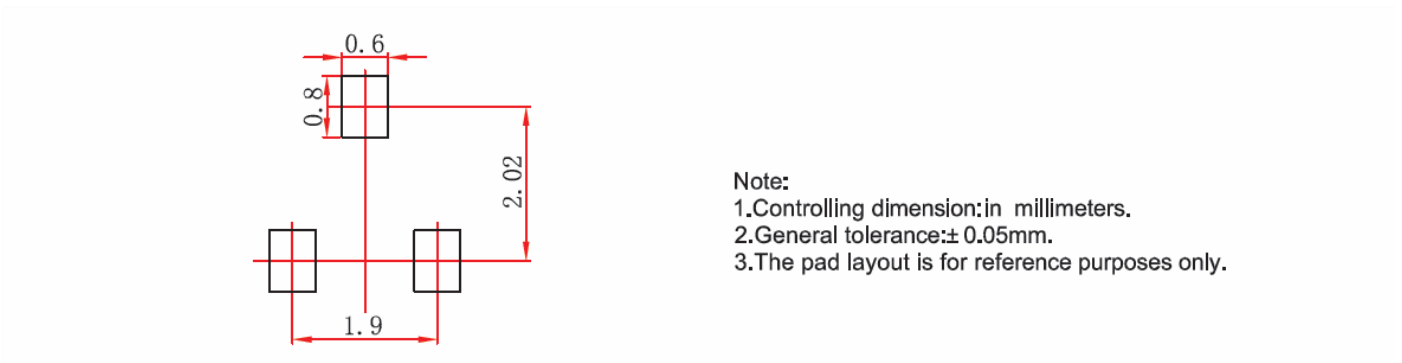


Figure8. Switching wave

■ SOT-23 Package information



■ SOT-23 Suggested Pad Layout





YJL3401A

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