

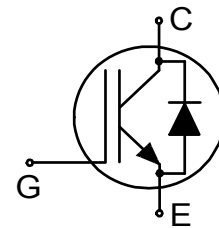
## TRENCHSTOP™ Advanced Isolation

High speed switching series third generation IGBT copacked with Rapid 1 fast and soft antiparallel diode in fully isolated package

### Features:

TRENCHSTOP™ technology offers :

- Short circuit withstand time 5 $\mu$ s at  $T_{vj} = 175^{\circ}\text{C}$
- Positive temperature coefficient in  $V_{CE(sat)}$
- Low EMI
- Very soft, fast recovery anti-parallel diode
- Maximum junction temperature 175 $^{\circ}\text{C}$
- 2500  $V_{RMS}$  electrical isolation, 50/60 Hz, t = 1 min
- 100 % tested isolated mounting surface
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt>



Fully isolated package TO-247

### Potential Applications:

- Air Conditioning PFC
- General Purpose Drives (GPD)
- Servo Drives

### Product Validation:

Qualified for industrial applications according to the relevant tests of JEDEC 47/20/22



### Key Performance and Package Parameters

Type	$V_{CE}$	$I_C$	$V_{CEsat}, T_{vj}=25^{\circ}\text{C}$	$T_{vjmax}$	Marking	Package
IKFW50N60DH3	600V	40A	1.85V	175 $^{\circ}\text{C}$	K50DDH3	PG-TO247-3-AI

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## TRENCHSTOP™ Advanced Isolation

## Maximum Ratings

For optimum lifetime and reliability, Infineon recommends operating conditions that do not exceed 80% of the maximum ratings stated in this datasheet.

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_{vj} \geq 25^{\circ}\text{C}$	$V_{CE}$	600	V
DC collector current, limited by $T_{vjmax}$ $T_h = 25^{\circ}\text{C}$ $T_h = 65^{\circ}\text{C}$ $T_h = 65^{\circ}\text{C}$	$I_C$	53.0 44.0 60.0 <sup>1)</sup>	A
Pulsed collector current, $t_p$ limited by $T_{vjmax}$	$I_{Cpuls}$	160.0	A
Turn off safe operating area $V_{CE} \leq 600\text{V}$ , $T_{vj} \leq 175^{\circ}\text{C}$ , $t_p = 1\mu\text{s}$	-	160.0	A
Diode forward current, limited by $T_{vjmax}$ $T_h = 25^{\circ}\text{C}$ value limited by bondwire $T_h = 65^{\circ}\text{C}$	$I_F$	40.0 32.0	A
Diode pulsed current, $t_p$ limited by $T_{vjmax}$	$I_{Fpuls}$	160.0	A
Gate-emitter voltage Transient Gate-emitter voltage ( $t_p \leq 10\mu\text{s}$ , $D < 0.010$ )	$V_{GE}$	$\pm 20$ $\pm 30$	V
Short circuit withstand time $V_{GE} = 15.0\text{V}$ , $V_{CC} \leq 400\text{V}$ Allowed number of short circuits < 1000 Time between short circuits: $\geq 1.0\text{s}$ $T_{vj} = 150^{\circ}\text{C}$	$t_{SC}$	5	$\mu\text{s}$
Power dissipation $T_h = 25^{\circ}\text{C}$ Power dissipation $T_h = 65^{\circ}\text{C}$	$P_{tot}$	145.0 106.0	W
Operating junction temperature	$T_{vj}$	-40...+175	$^{\circ}\text{C}$
Storage temperature	$T_{stg}$	-55...+150	$^{\circ}\text{C}$
Soldering temperature, wave soldering 1.6mm (0.063in.) from case for 10s		260	$^{\circ}\text{C}$
Mounting torque, M3 screw, PG-TO247-3-Al Maximum of mounting processes: 3	$M$	0.6	Nm
Isolation voltage RMS, $f = 50/60\text{Hz}$ , $t = 1\text{min}^{2)}$	$V_{isol}$	2500	V

## Thermal Resistance

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>R<sub>th</sub> Characteristics</b>						
IGBT thermal resistance, <sup>3)</sup> junction - heatsink	$R_{th(j-h)}$		-	0.88	1.03	K/W
Diode thermal resistance, <sup>3)</sup> junction - heatsink	$R_{th(j-h)}$		-	1.75	1.92	K/W
Thermal resistance junction - ambient	$R_{th(j-a)}$		-	-	65	K/W

<sup>1)</sup> Equivalent current rating in TO-247-3 at  $T_h = 65^{\circ}\text{C}$  using reference insulation material: 152 $\mu\text{m}$ , 1.3 W/mK, standard polyimide based reinforced carrier insulator

<sup>2)</sup> For a proper handling and assembly of the advanced isolation device in the application refer to the note at the package drawing.

<sup>3)</sup> At force on body  $F = 500\text{N}$ ,  $T_a = 25^{\circ}\text{C}$

## TRENCHSTOP™ Advanced Isolation

Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE} = 0\text{V}, I_C = 0.50\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CEsat}$	$V_{GE} = 15.0\text{V}, I_C = 40.0\text{A}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	- -	1.85 2.25	2.30 -	V
Diode forward voltage	$V_F$	$V_{GE} = 0\text{V}, I_F = 20.0\text{A}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	- -	1.45 1.40	1.75 -	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C = 0.58\text{mA}, V_{CE} = V_{GE}$	4.1	5.1	5.7	V
Zero gate voltage collector current	$I_{CES}$	$V_{CE} = 600\text{V}, V_{GE} = 0\text{V}$ $T_{vj} = 25^{\circ}\text{C}$ $T_{vj} = 175^{\circ}\text{C}$	- -	- 600	40 -	$\mu\text{A}$
Gate-emitter leakage current	$I_{GES}$	$V_{CE} = 0\text{V}, V_{GE} = 20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE} = 20\text{V}, I_C = 40.0\text{A}$	-	17.0	-	S

Electrical Characteristic, at  $T_{vj} = 25^{\circ}\text{C}$ , unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Dynamic Characteristic</b>						
Input capacitance	$C_{ies}$	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$	-	2293	-	pF
Output capacitance	$C_{oes}$		-	94	-	
Reverse transfer capacitance	$C_{res}$		-	62	-	
Gate charge	$Q_G$	$V_{CC} = 480\text{V}, I_C = 40.0\text{A},$ $V_{GE} = 15\text{V}$	-	210.0	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	13.0	-	nH
Short circuit collector current Max. 1000 short circuits Time between short circuits: $\geq 1.0\text{s}$	$I_{C(SC)}$	$V_{GE} = 15.0\text{V}, V_{CC} \leq 400\text{V},$ $t_{SC} \leq 5\mu\text{s}$ $T_{vj} = 150^{\circ}\text{C}$	-	235	-	A

## Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>IGBT Characteristic, at <math>T_{vj} = 25^{\circ}\text{C}</math></b>						
Turn-on delay time	$t_{d(on)}$	$T_{vj} = 25^{\circ}\text{C},$ $V_{CC} = 400\text{V}, I_C = 40.0\text{A},$ $V_{GE} = 0.0/15.0\text{V},$ $R_{G(on)} = 8.0\Omega, R_{G(off)} = 8.0\Omega,$ $L\sigma = 75\text{nH}, C\sigma = 30\text{pF}$ $L\sigma, C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	25	-	ns
Rise time	$t_r$		-	35	-	ns
Turn-off delay time	$t_{d(off)}$		-	212	-	ns
Fall time	$t_f$		-	22	-	ns
Turn-on energy	$E_{on}$		-	1.22	-	mJ
Turn-off energy	$E_{off}$		-	0.61	-	mJ
Total switching energy	$E_{ts}$		-	1.83	-	mJ

## TRENCHSTOP™ Advanced Isolation

Diode reverse recovery time	$t_{rr}$	$T_{vj} = 25^{\circ}\text{C}$ , $V_R = 400\text{V}$ , $I_F = 20.0\text{A}$ , $di_F/dt = 1000\text{A}/\mu\text{s}$	-	64	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.51	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	11.7	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	-840	-	$\text{A}/\mu\text{s}$

## Switching Characteristic, Inductive Load

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

IGBT Characteristic, at  $T_{vj} = 175^{\circ}\text{C}$ 

Turn-on delay time	$t_{d(on)}$	$T_{vj} = 175^{\circ}\text{C}$ , $V_{CC} = 400\text{V}$ , $I_C = 40.0\text{A}$ , $V_{GE} = 0.0/15.0\text{V}$ , $R_{G(on)} = 8.0\Omega$ , $R_{G(off)} = 8.0\Omega$ , $L\sigma = 75\text{nH}$ , $C\sigma = 30\text{pF}$ $L\sigma$ , $C\sigma$ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	24	-	ns
Rise time	$t_r$		-	34	-	ns
Turn-off delay time	$t_{d(off)}$		-	246	-	ns
Fall time	$t_f$		-	22	-	ns
Turn-on energy	$E_{on}$		-	1.73	-	mJ
Turn-off energy	$E_{off}$		-	0.81	-	mJ
Total switching energy	$E_{ts}$		-	2.54	-	mJ
Diode reverse recovery time	$t_{rr}$	$T_{vj} = 175^{\circ}\text{C}$ , $V_R = 400\text{V}$ , $I_F = 20.0\text{A}$ , $di_F/dt = 1000\text{A}/\mu\text{s}$	-	103	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	1.24	-	$\mu\text{C}$
Diode peak reverse recovery current	$I_{rrm}$		-	17.2	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	-502	-	$\text{A}/\mu\text{s}$

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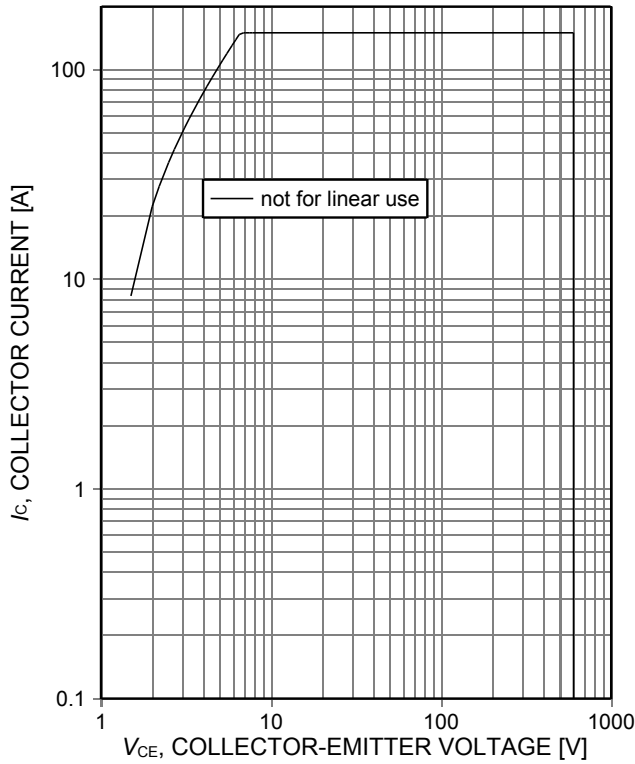


Figure 1. **Forward bias safe operating area**  
 ( $D=0$ ,  $T_h=25^\circ\text{C}$ ,  $T_j\leq 175^\circ\text{C}$ ,  $V_{GE}=15\text{V}$ ,  $t_p\leq 1\mu\text{s}$ )

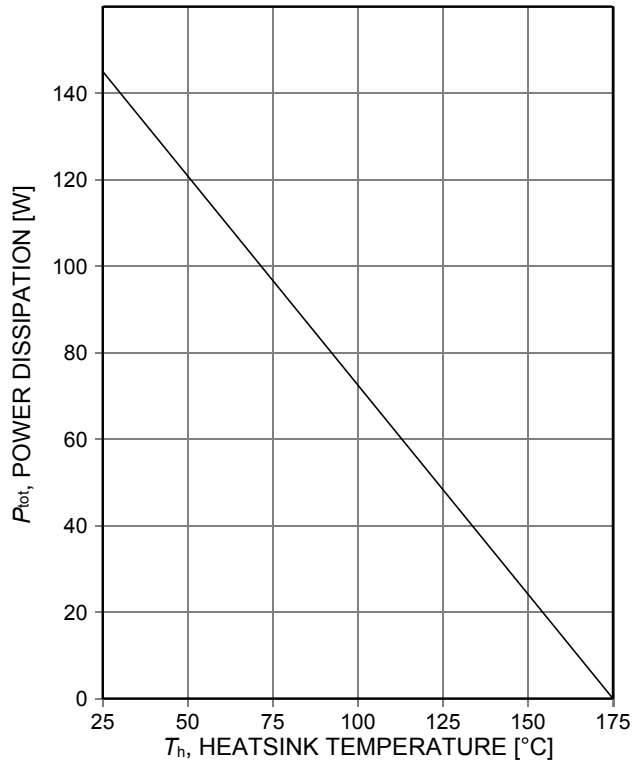


Figure 2. **Power dissipation as a function of heatsink temperature**  
 ( $T_j\leq 175^\circ\text{C}$ )

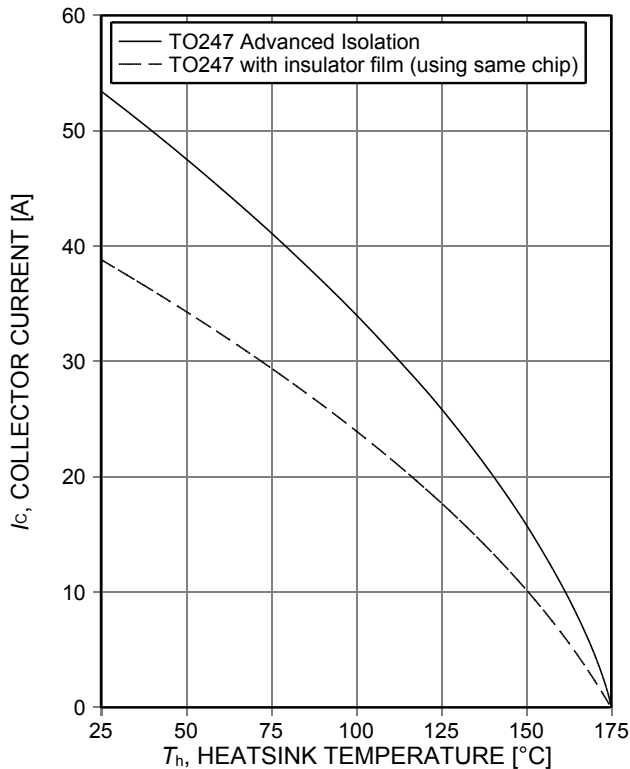


Figure 3. **Collector current as a function of heatsink temperature**  
 ( $V_{GE}\geq 15\text{V}$ ,  $T_j\leq 175^\circ\text{C}$ , insulator film:  $152\mu\text{m}$ ,  $1.3\text{W/mK}$ )

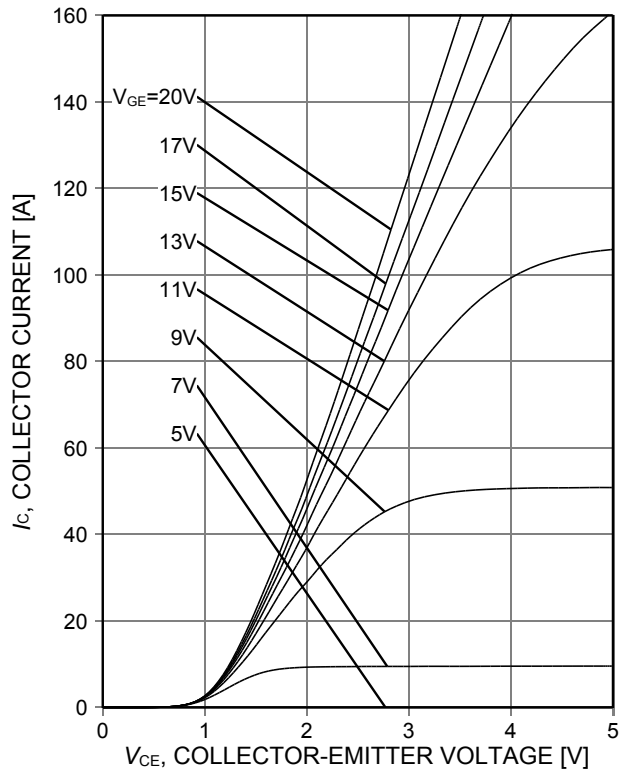


Figure 4. **Typical output characteristic**  
 ( $T_j=25^\circ\text{C}$ )

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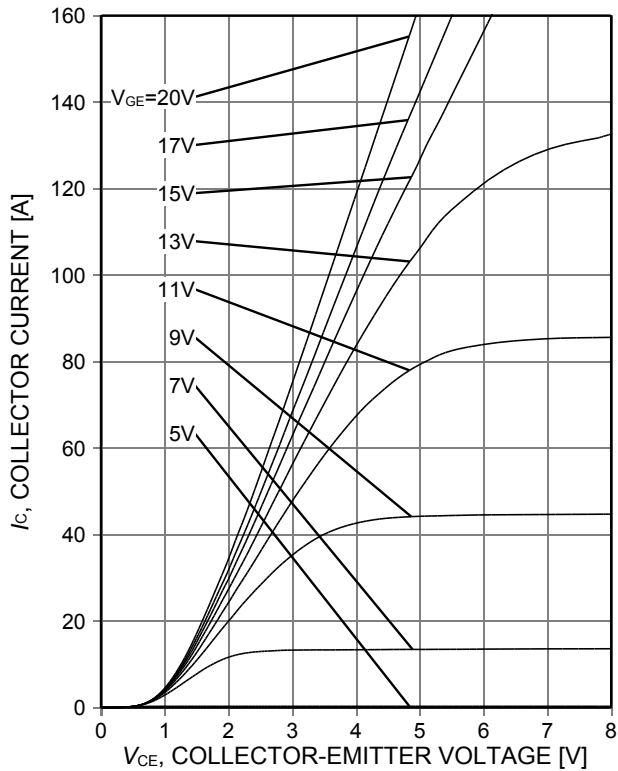


Figure 5. Typical output characteristic ( $T_j=175^\circ\text{C}$ )

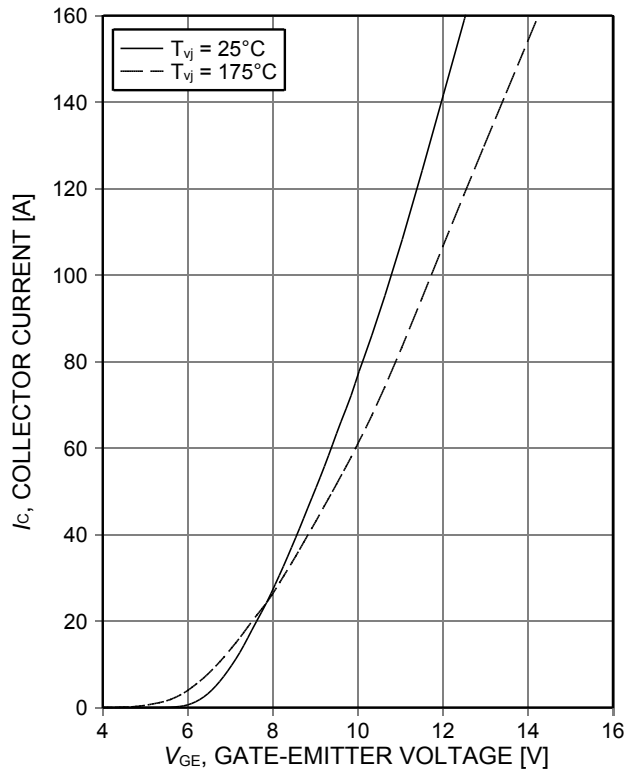


Figure 6. Typical transfer characteristic ( $V_{CE}=20\text{V}$ )

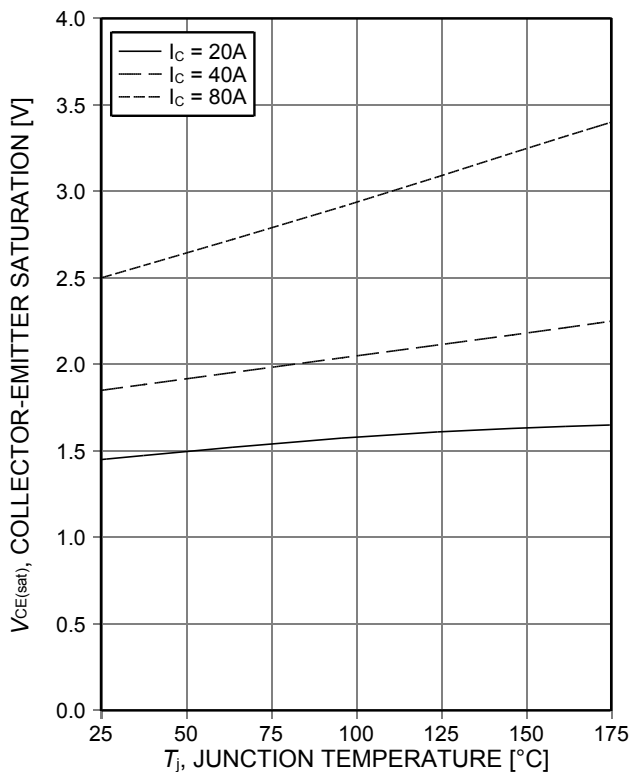


Figure 7. Typical collector-emitter saturation voltage as a function of junction temperature ( $V_{GE}=15\text{V}$ )

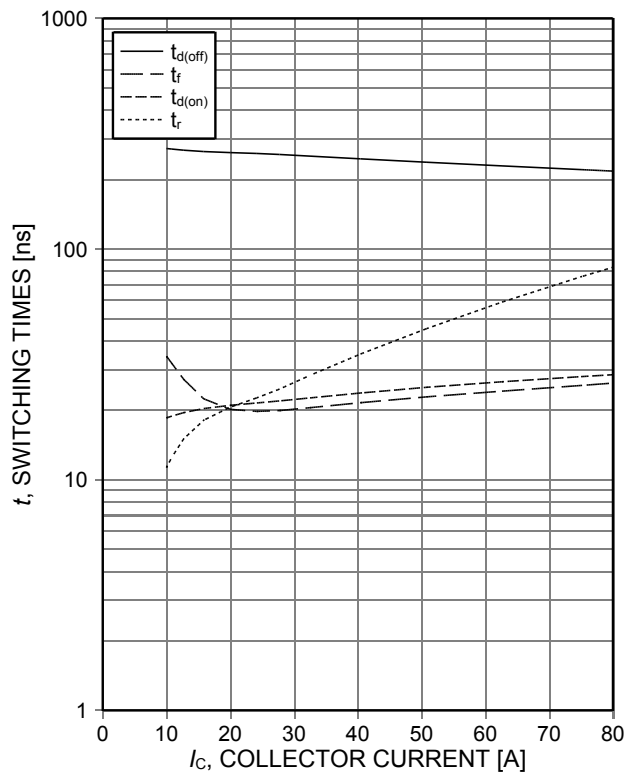


Figure 8. Typical switching times as a function of collector current (ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $R_G=8\Omega$ , test circuit in Fig. E)

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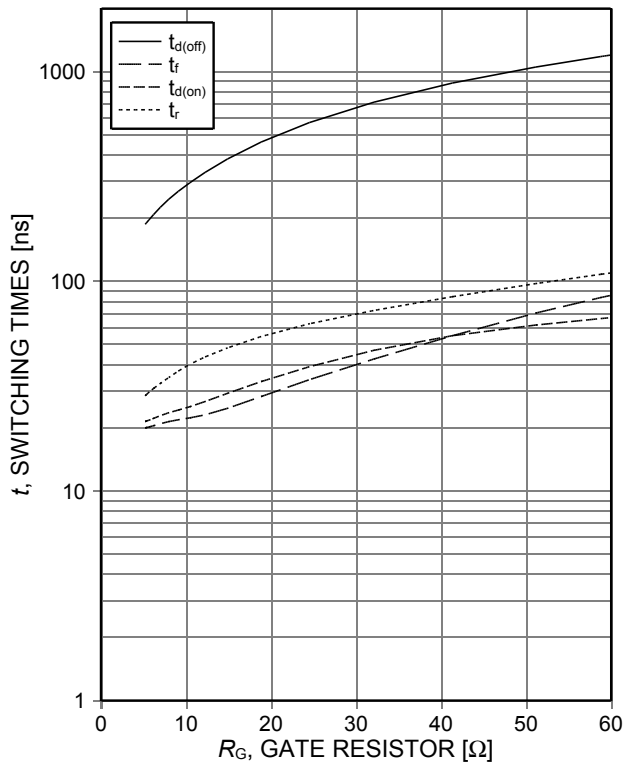


Figure 9. Typical switching times as a function of gate resistor (ind. load,  $T_j=175^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=40\text{A}$ , test circuit in Fig. E)

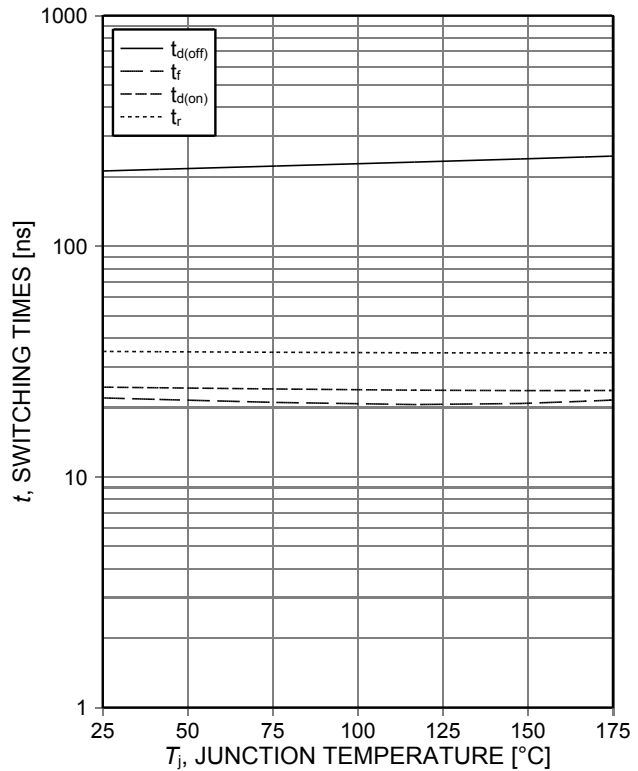


Figure 10. Typical switching times as a function of junction temperature (ind. load,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=40\text{A}$ ,  $r_G=8\Omega$ , test circuit in Fig. E)

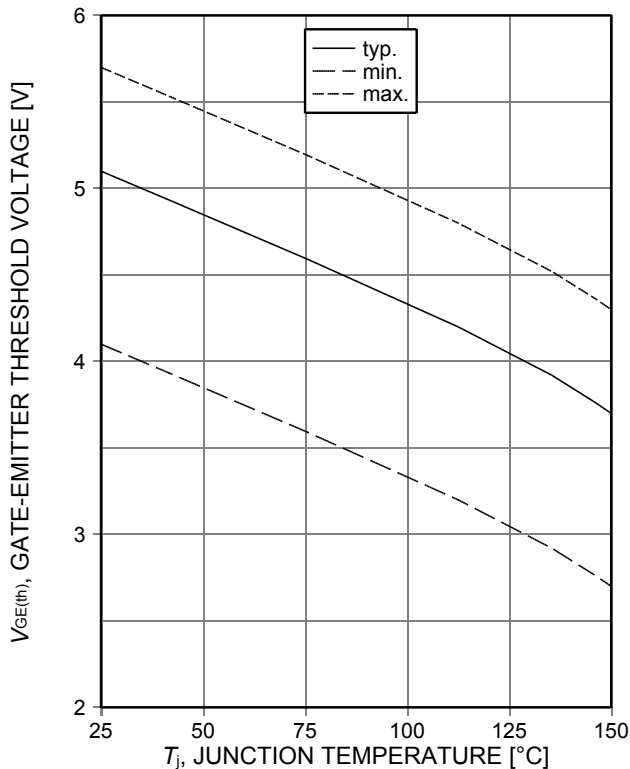


Figure 11. Gate-emitter threshold voltage as a function of junction temperature ( $I_C=0.58\text{mA}$ )

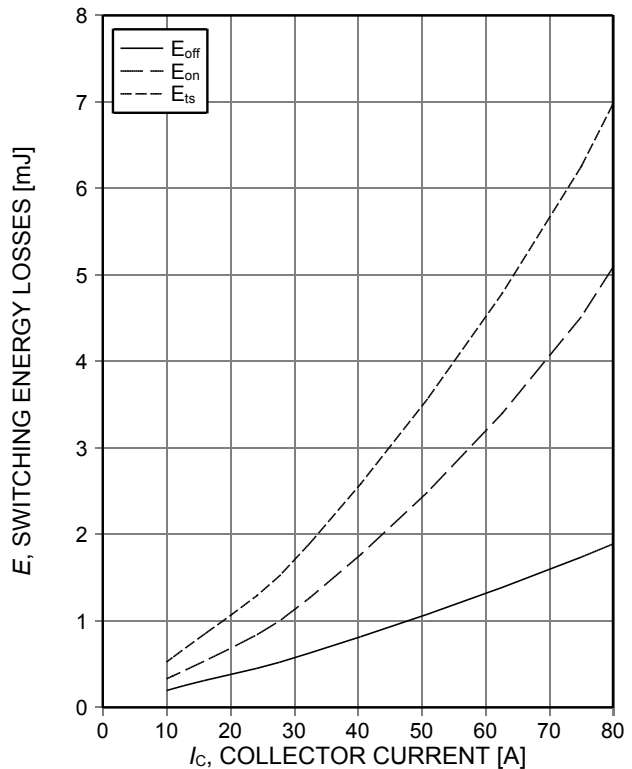


Figure 12. Typical switching energy losses as a function of collector current (ind. load,  $T_j=175^{\circ}\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $R_G=8\Omega$ , test circuit in Fig. E)



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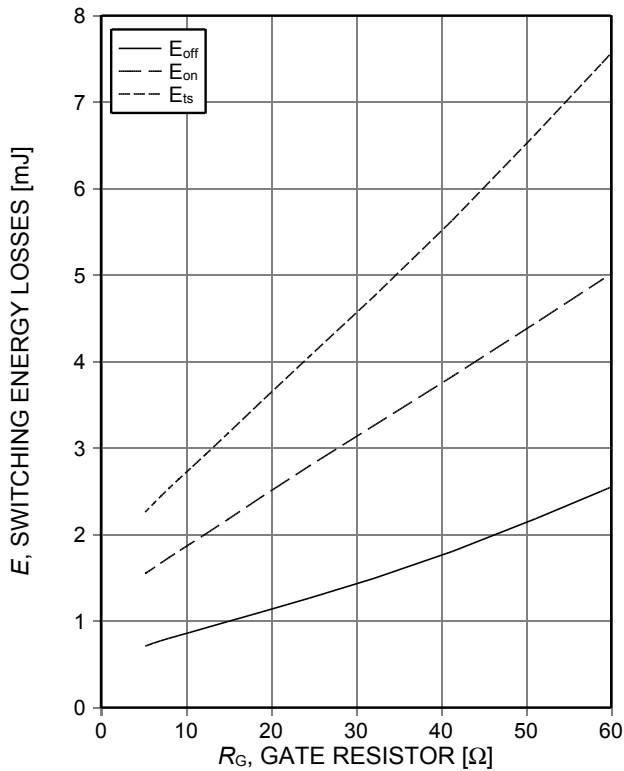


Figure 13. **Typical switching energy losses as a function of gate resistor**  
(ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=40\text{A}$ , test circuit in Fig. E)

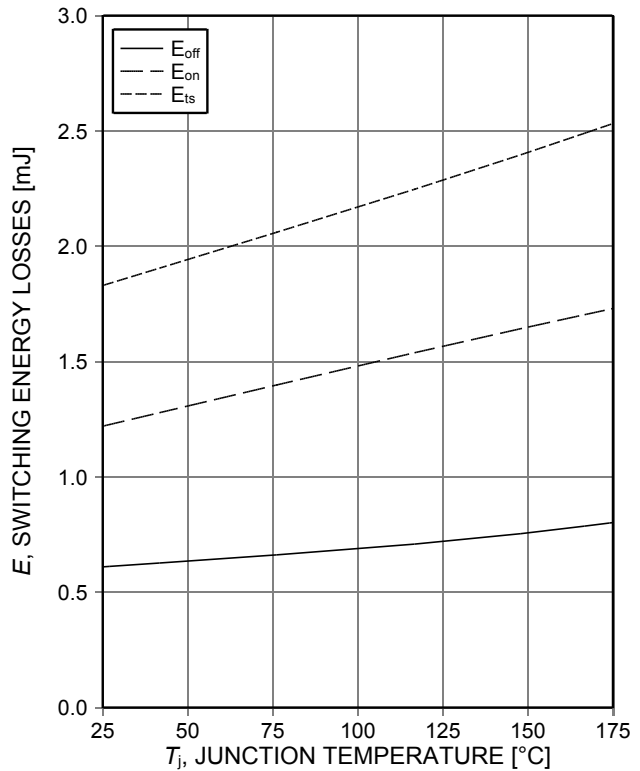


Figure 14. **Typical switching energy losses as a function of junction temperature**  
(ind load,  $V_{CE}=400\text{V}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=40\text{A}$ ,  $R_G=8\Omega$ , test circuit in Fig. E)

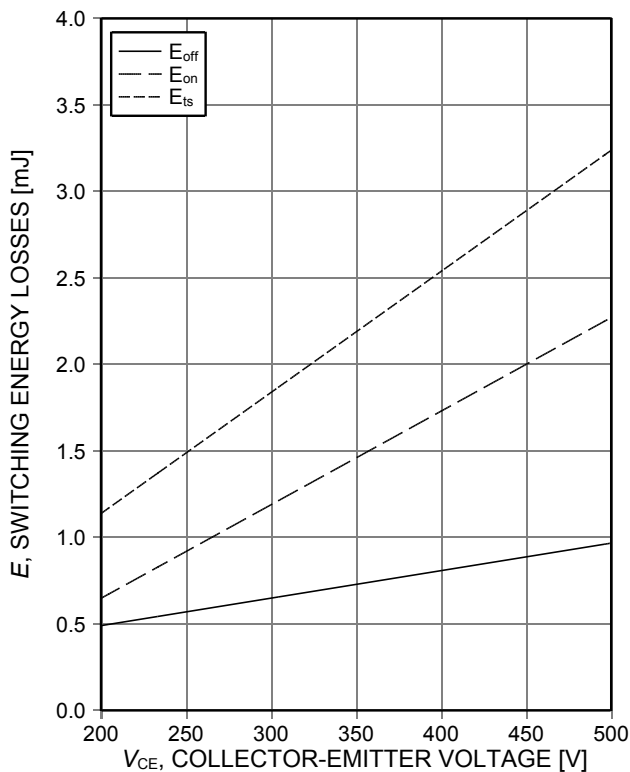


Figure 15. **Typical switching energy losses as a function of collector emitter voltage**  
(ind. load,  $T_j=175^\circ\text{C}$ ,  $V_{GE}=0/15\text{V}$ ,  $I_C=40\text{A}$ ,  $R_G=8\Omega$ , test circuit in Fig. E)

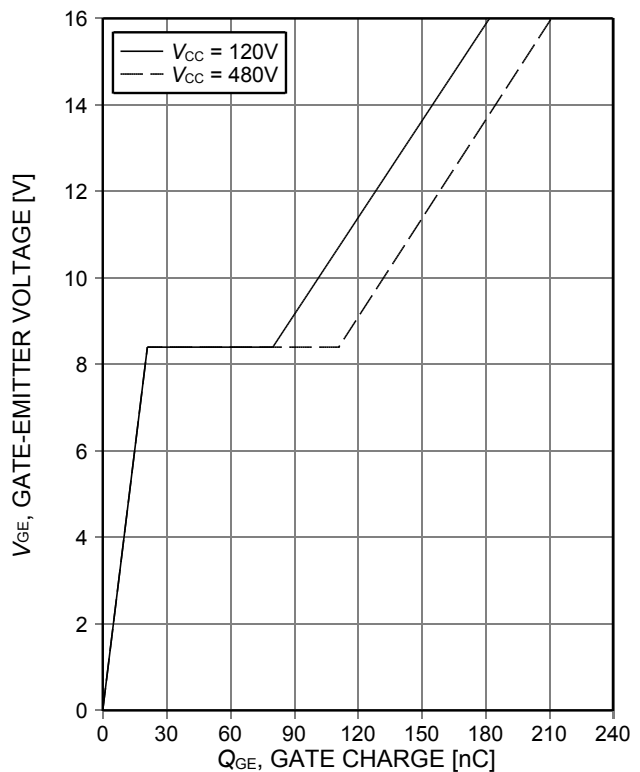


Figure 16. **Typical gate charge**  
( $I_C=40\text{A}$ )

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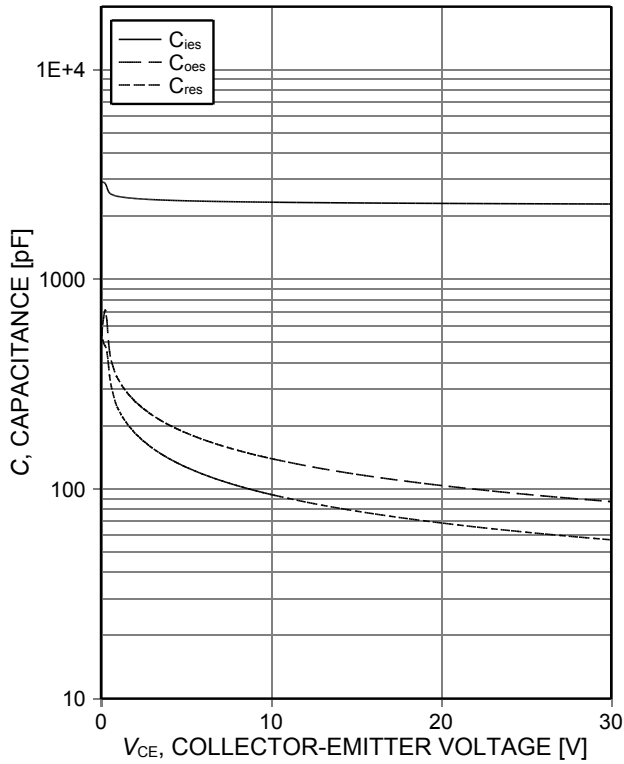


Figure 17. Typical capacitance as a function of collector-emitter voltage ( $V_{GE}=0V$ ,  $f=1MHz$ )

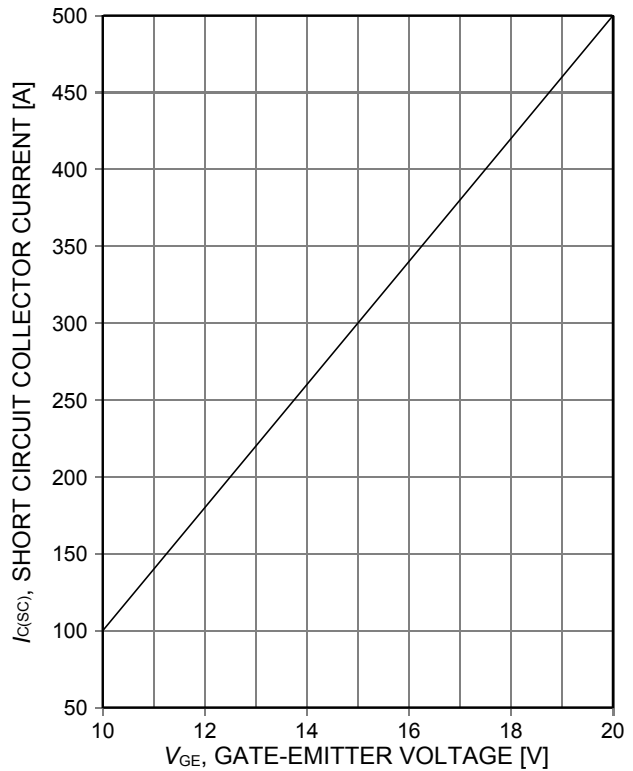


Figure 18. Typical short circuit collector current as a function of gate-emitter voltage ( $V_{CE}\leq 400V$ , start at  $T_j=25^\circ C$ )

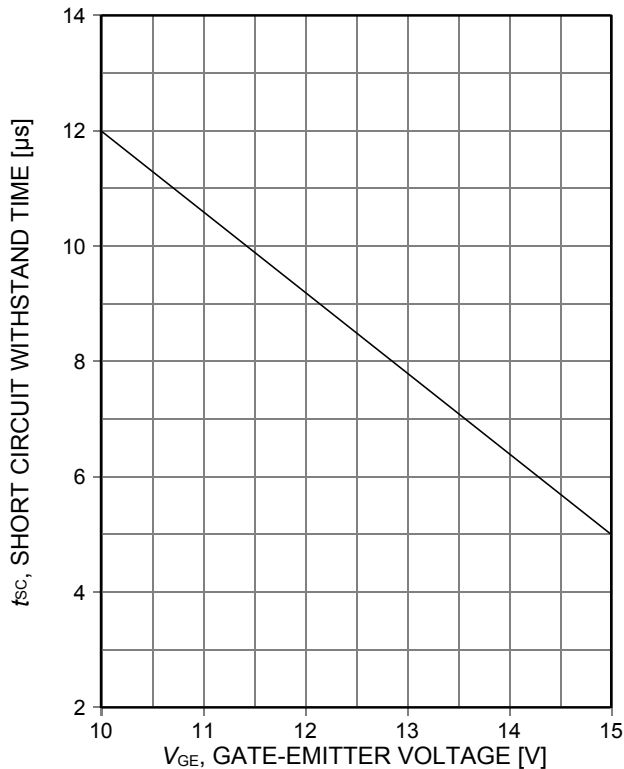


Figure 19. Short circuit withstand time as a function of gate-emitter voltage ( $V_{CE}\leq 400V$ , start at  $T_j\leq 150^\circ C$ )

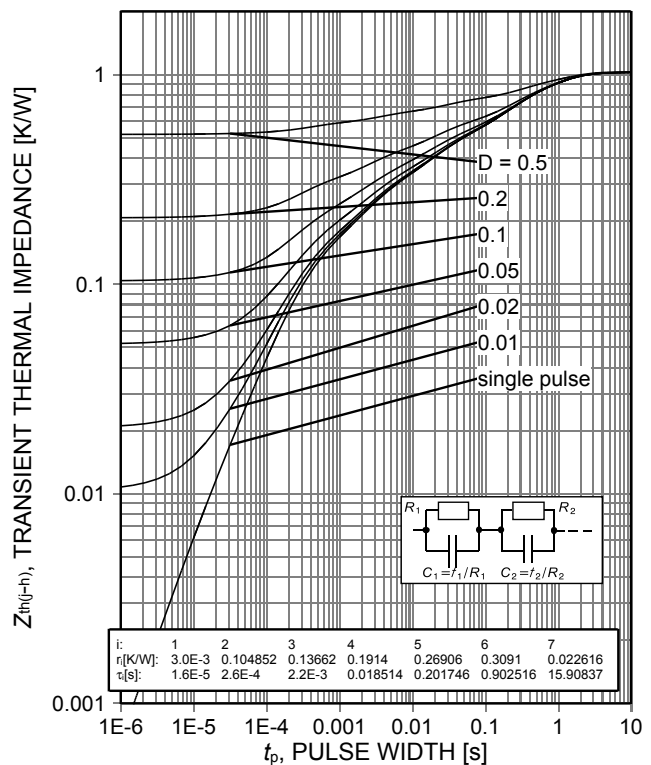


Figure 20. IGBT transient thermal impedance as a function of pulse width ( $D=t_p/T$ )

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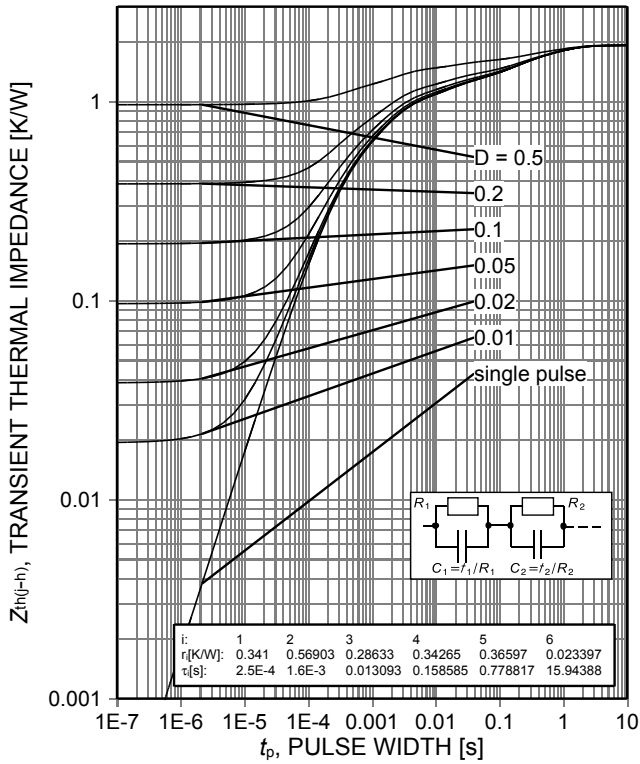


Figure 21. Diode transient thermal impedance as a function of pulse width ( $D=t_p/T$ )

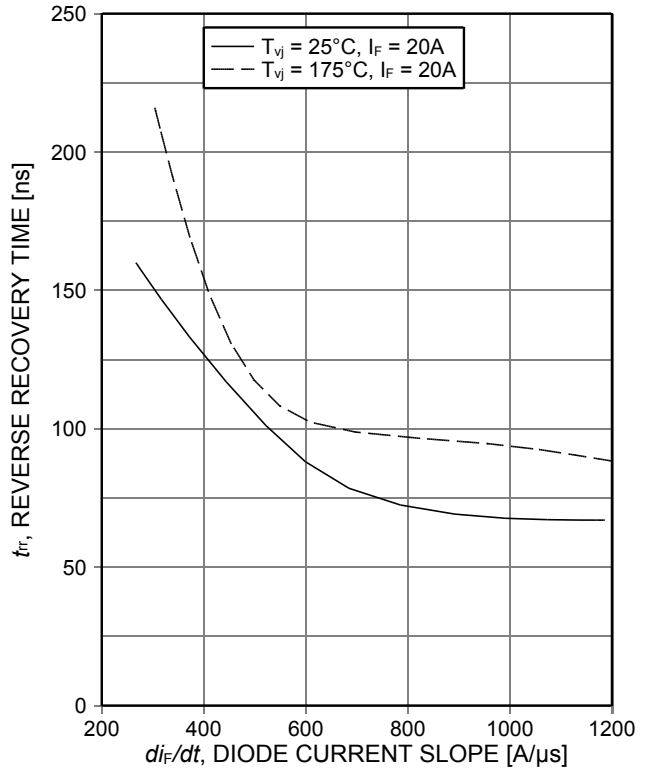


Figure 22. Typical reverse recovery time as a function of diode current slope ( $V_R=400V$ )

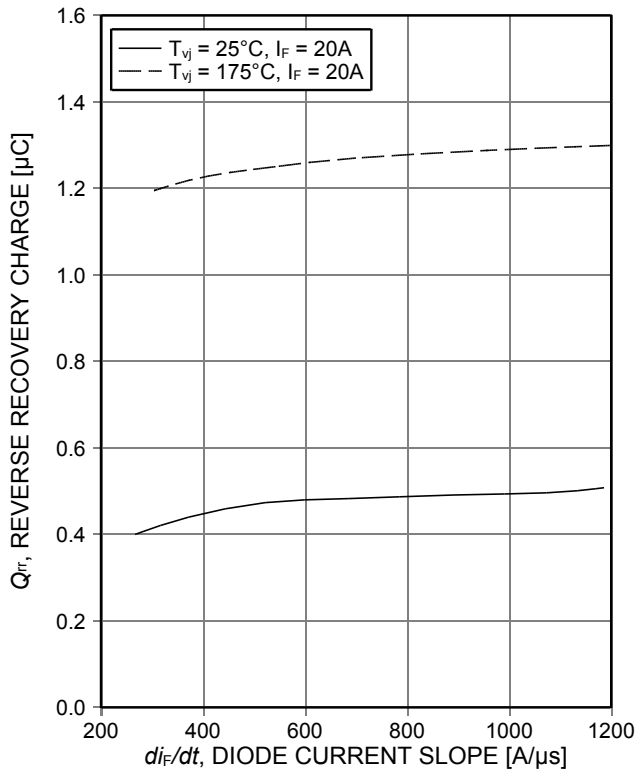


Figure 23. Typical reverse recovery charge as a function of diode current slope ( $V_R=400V$ )

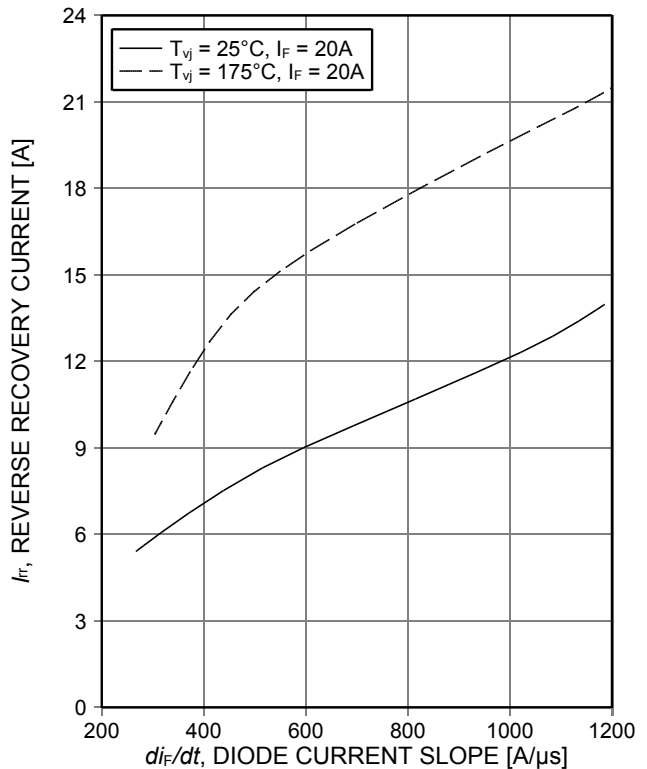


Figure 24. Typical reverse recovery current as a function of diode current slope ( $V_R=400V$ )

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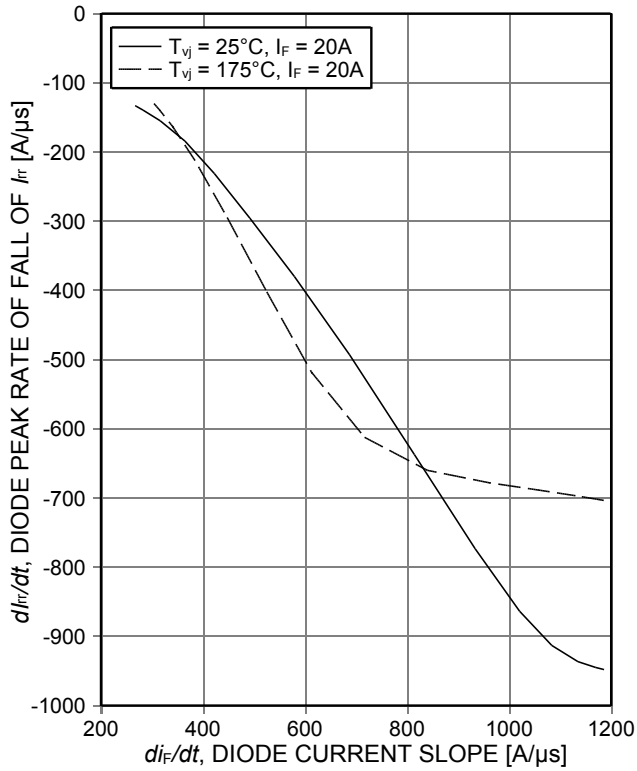


Figure 25. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope ( $V_R=400V$ )

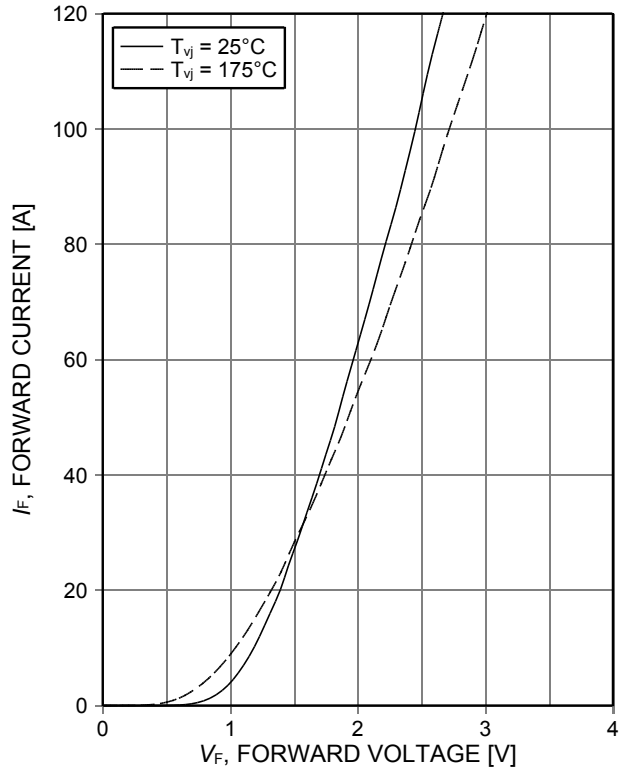


Figure 26. Typical diode forward current as a function of forward voltage

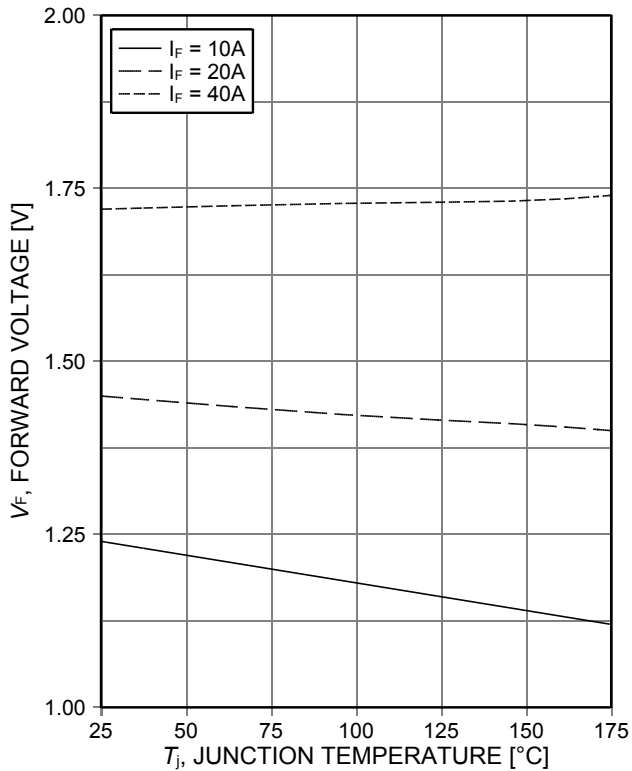
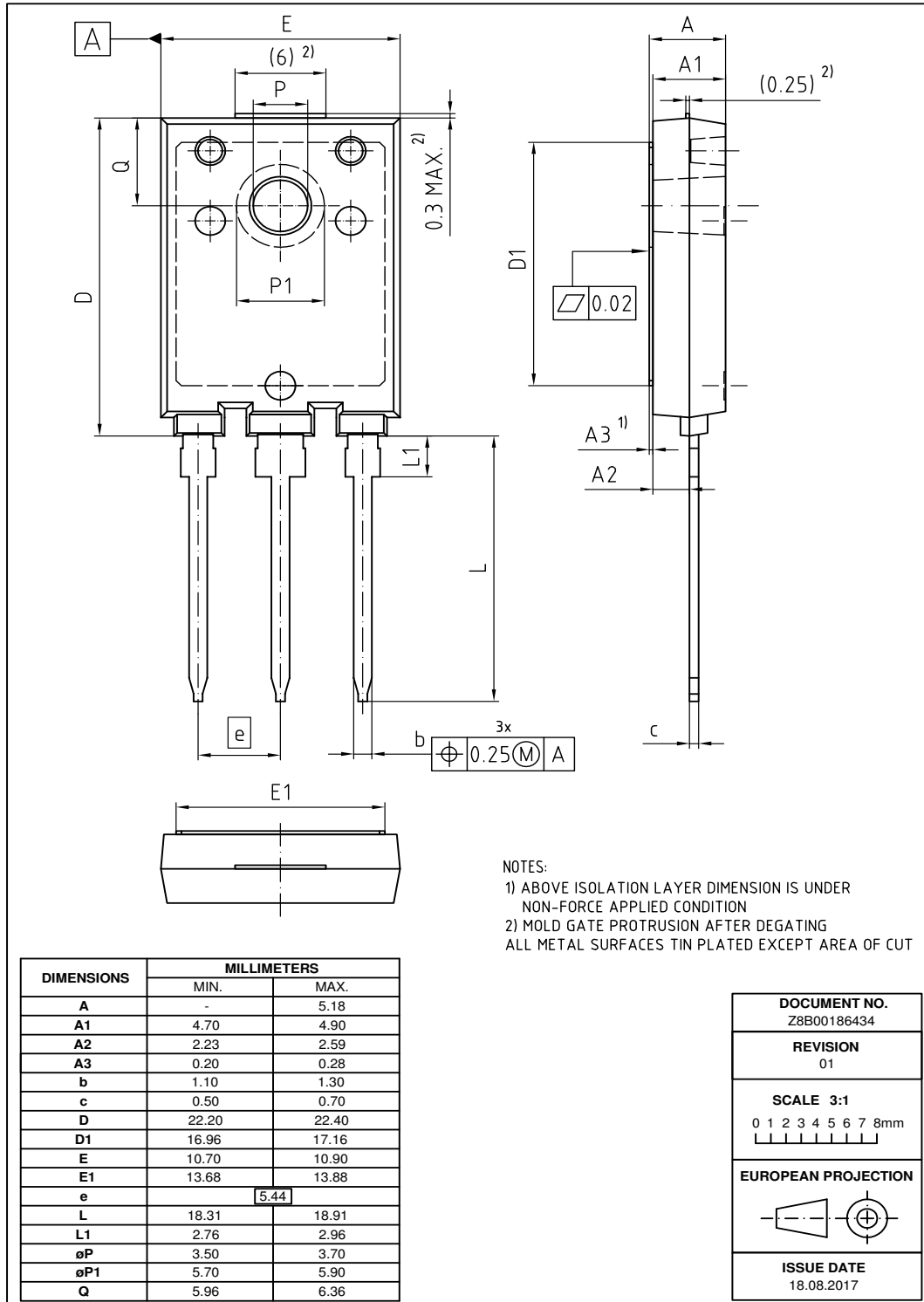


Figure 27. Typical diode forward voltage as a function of junction temperature

**PG-TO247-3-AI (PG-HSIP247-3)**



Note: For a proper handling and assembly of the advanced isolation device in the application the isolation layer must not be exposed to potential penetration via sharp implements or mechanical impacts/shocks, which exceed levels indicated in International Standard (IEC60068-2-6 and IEC60068-2-27). The advanced isolation device is intended only to be used assembled on an appropriate heatsink with recommended flatness of <20µm per 100mm and roughness of <10µm.

Testing Conditions



Figure A. Definition of switching times



Figure B. Definition of switching losses



Figure C. Definition of diode switching characteristics

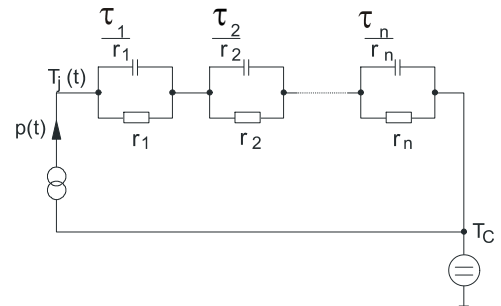


Figure D. Thermal equivalent circuit

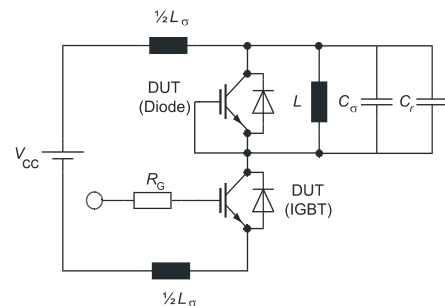


Figure E. Dynamic test circuit  
Parasitic inductance  $L_{\sigma}$ ,  
parasitic capacitor  $C_{\sigma}$ ,  
relief capacitor  $C_r$ ,  
(only for ZVT switching)

## Revision History

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IKFW50N60DH3

**Revision: 2017-09-21, Rev. 2.1**

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Previous Revision

Revision	Date	Subjects (major changes since last revision)
2.1	2017-09-21	Final data sheet

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