

**DUAL P-CHANNEL ENHANCEMENT MODE MOSFET**
**Product Summary**

$V_{(BR)DSS}$	$R_{DS(ON) max}$	$I_D$ $T_A = +25^\circ C$
-20V	38m $\Omega$ @ $V_{GS} = -10V$	-5.7A
	43m $\Omega$ @ $V_{GS} = -4.5V$	-5.4A
	75m $\Omega$ @ $V_{GS} = -2.5V$	-4.1A

**Description**

This new generation MOSFET has been designed to minimize the on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

**Applications**

- Power Management Functions
- Battery Pack
- Load Switch

**Features**

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

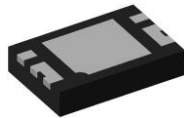
**Mechanical Data**

- Case: U-DFN2030-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections – NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.012 grams (Approximate)

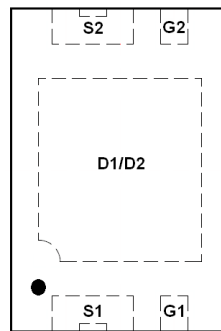
**NEW PRODUCT**


ESD PROTECTED

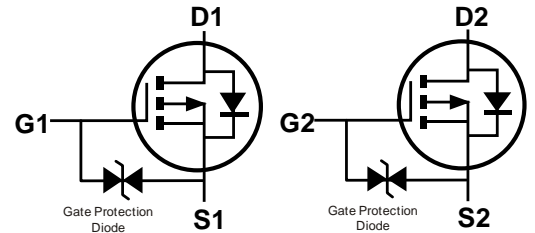
U-DFN2030-6



Bottom View



Top View

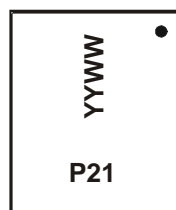


Equivalent Circuit

**Ordering Information** (Note 4)

Part Number	Case	Packaging
DMP2100UFU-7	U-DFN2030-6	3000 / Tape & Reel
DMP2100UFU-13	U-DFN2030-6	10000 / Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
  2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <http://www.diodes.com>.

**Marking Information**


P21 = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY = Last Digit of Year (ex: 14 for 2014)  
 WW = Week Code (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-Source Voltage	V <sub>GSS</sub>	±10	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	I <sub>D</sub>	T <sub>A</sub> = +25°C	-5.7
Steady State		T <sub>A</sub> = +70°C	-4.4
Maximum Continuous Body Diodes Forward Current (Note 6)	I <sub>S</sub>	-2	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I <sub>DM</sub>	-30	A
Avalanche Current (Note 7) L = 0.1mH	I <sub>AS</sub>	-15	A
Avalanche Energy (Note 7) L = 0.1mH	E <sub>AS</sub>	12	mJ

**Thermal Characteristics**

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 5)	P <sub>D</sub>	0.9	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	138	°C/W
Total Power Dissipation (Note 6)	P <sub>D</sub>	1.9	W
Thermal Resistance, Junction to Ambient (Note 6)	R <sub>θJA</sub>	66	°C/W
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	9.6	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 8)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-20	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	-1	µA	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±10	µA	V <sub>GS</sub> = ±8V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 8)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-0.3	—	-1.4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	25	38	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -3.5A
		—	29	43		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -3A
		—	37	75		V <sub>GS</sub> = -2.5V, I <sub>D</sub> = -1A
		—	47	—		V <sub>GS</sub> = -1.8V, I <sub>D</sub> = -0.5A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -2.9A
<b>DYNAMIC CHARACTERISTICS (Note 9)</b>						
Input Capacitance	C <sub>ISS</sub>	—	906	—	pF	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>OSS</sub>	—	103	—	pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>	—	29	—	pF	
Gate Resistance	R <sub>g</sub>	—	259	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1.0MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	10.3	—	nC	V <sub>DS</sub> = -10V, I <sub>D</sub> = -4A
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	21.4	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	1.6	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	2.3	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	70	—	ns	V <sub>DS</sub> = -10V, V <sub>GS</sub> = -4.5V, R <sub>L</sub> = 2.5Ω, R <sub>G</sub> = 3.0Ω
Turn-On Rise Time	t <sub>r</sub>	—	144	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	626	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	396	—	ns	
Body Diode Reverse Recovery Time	t <sub>RR</sub>	—	279	—	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	—	466	—	nC	I <sub>F</sub> = -3.5A, di/dt = -100A/µs

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
  - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
  - I<sub>AS</sub> and E<sub>AS</sub> rating are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

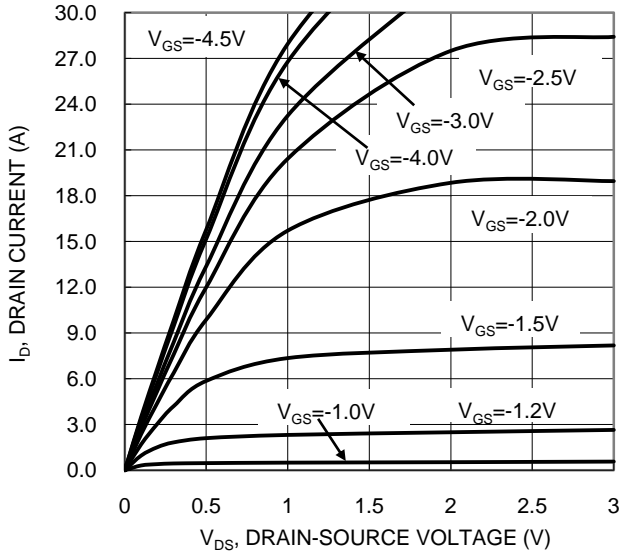


Figure 1. Typical Output Characteristic

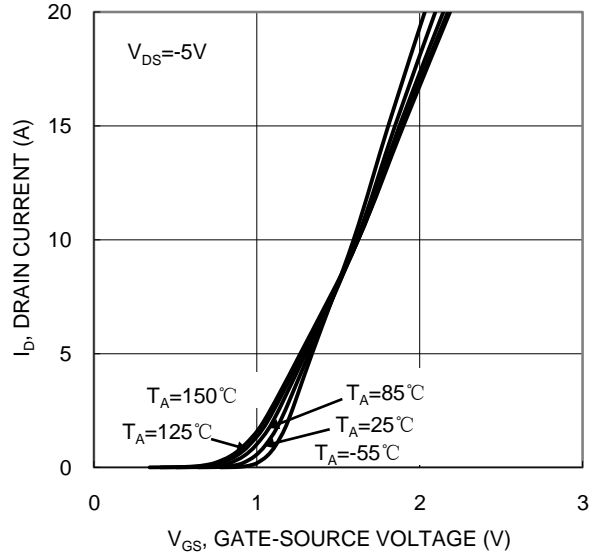


Figure 2. Typical Transfer Characteristic

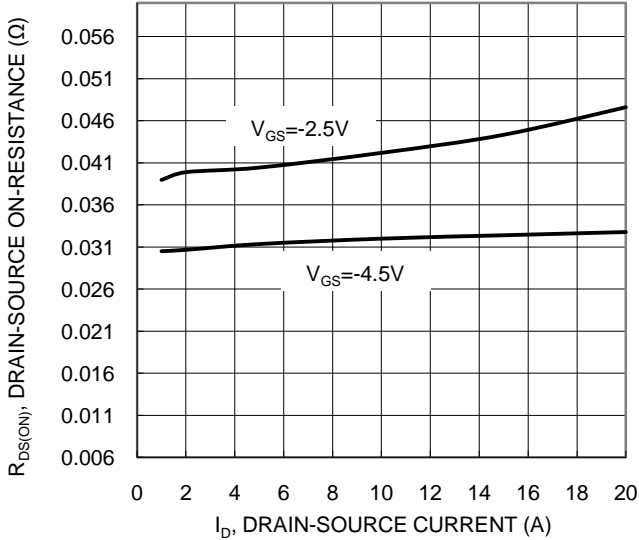


Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

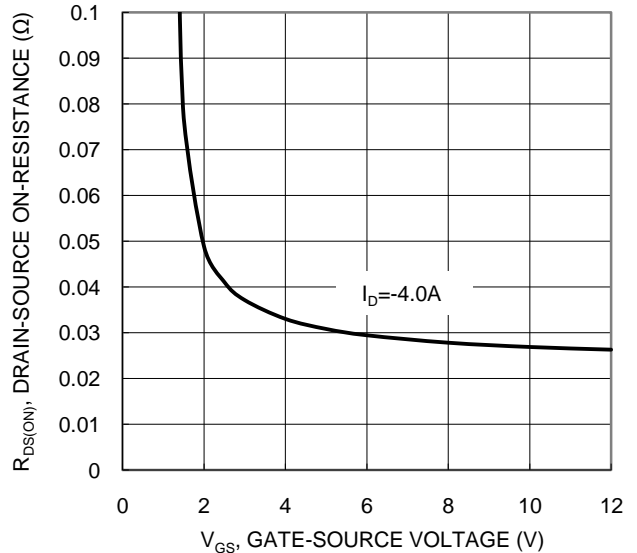


Figure 4. Typical Transfer Characteristic

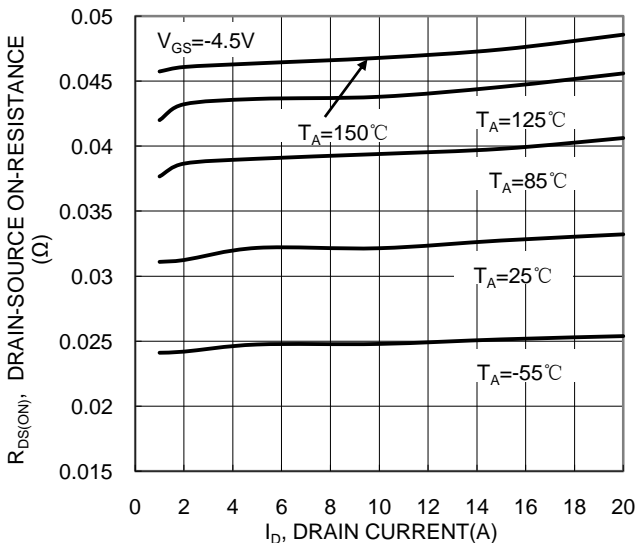


Figure 5. Typical On-Resistance vs Drain Current and Temperature

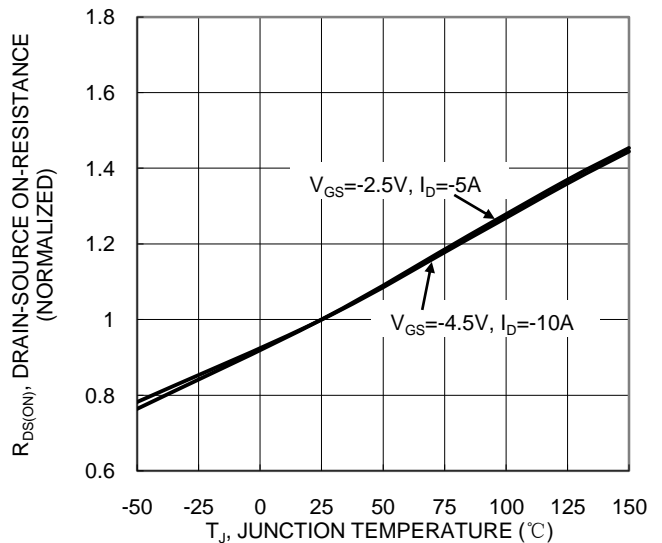


Figure 6. On-Resistance Variation with Temperature

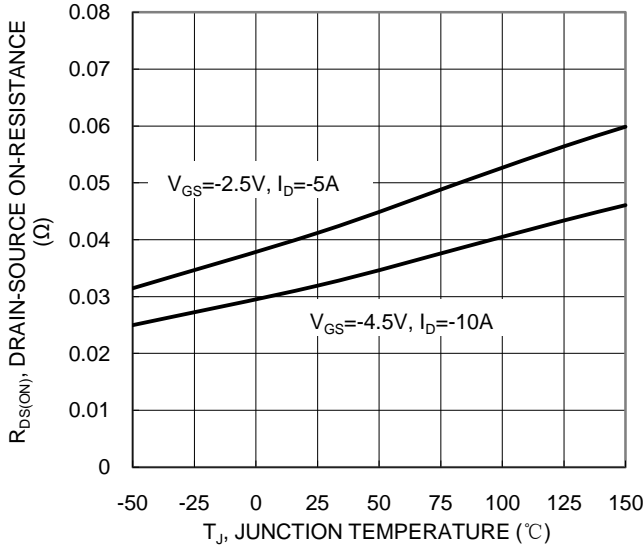


Figure 7. On-Resistance Variation with Temperature

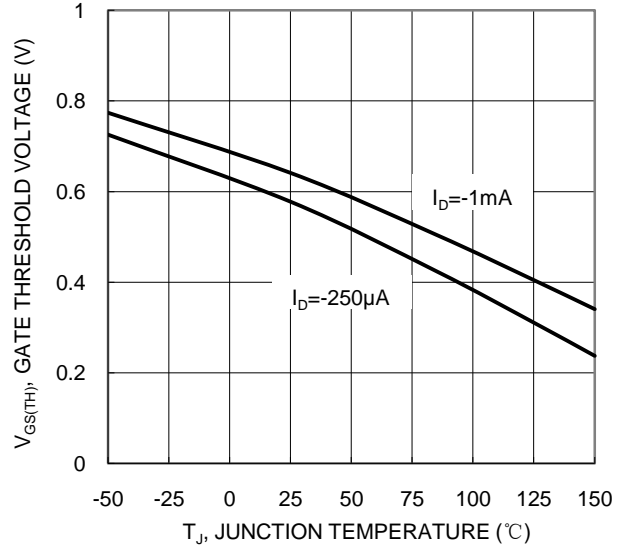


Figure 8. Gate Threshold Variation vs Temperature

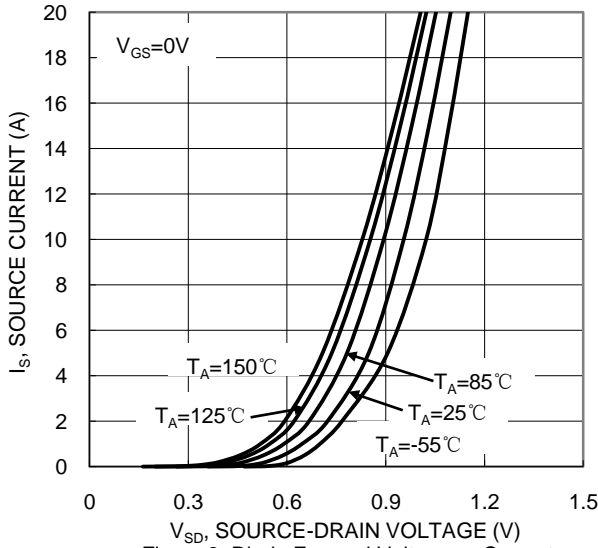


Figure 9. Diode Forward Voltage vs Current

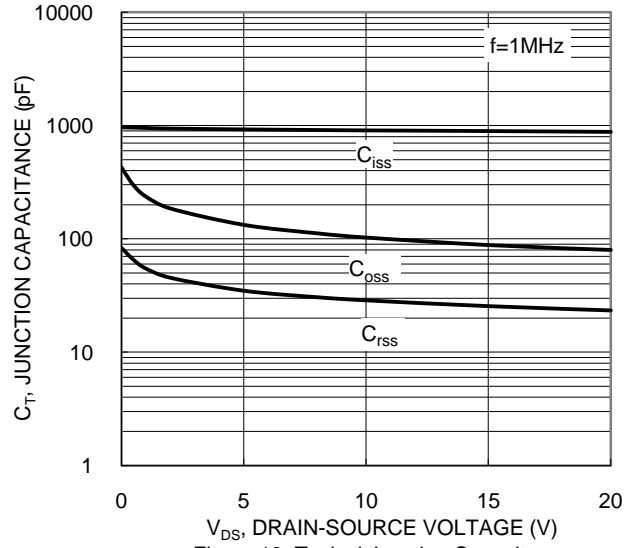


Figure 10. Typical Junction Capacitance

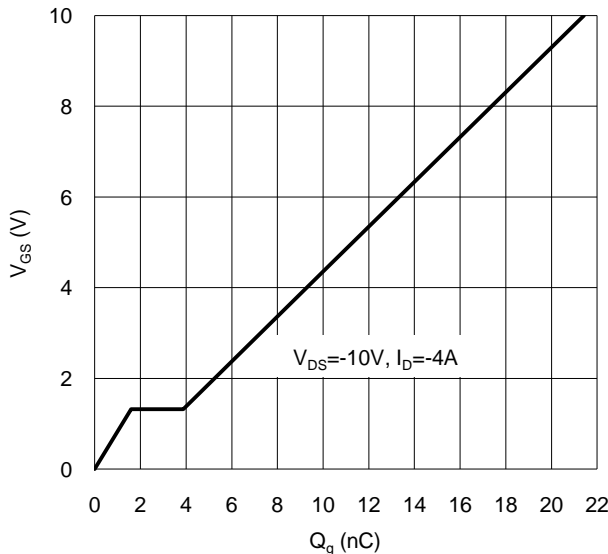


Figure 11. Gate Charge

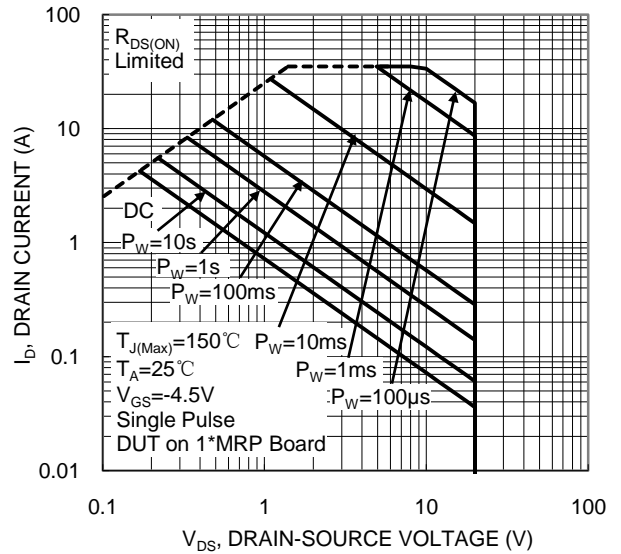


Figure 12. SOA, Safe Operation Area

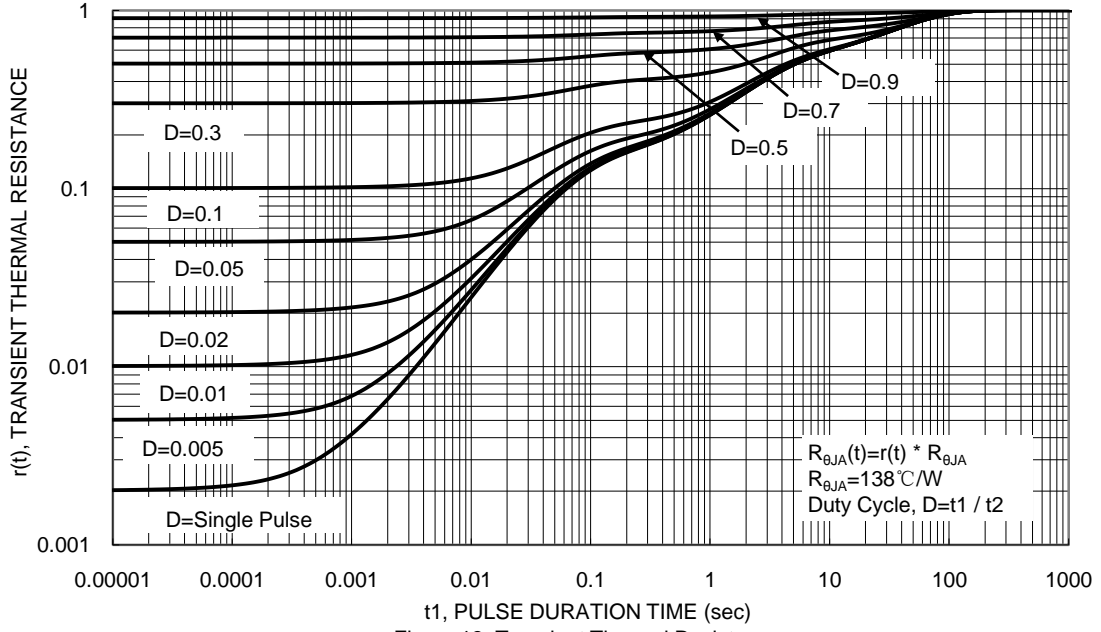
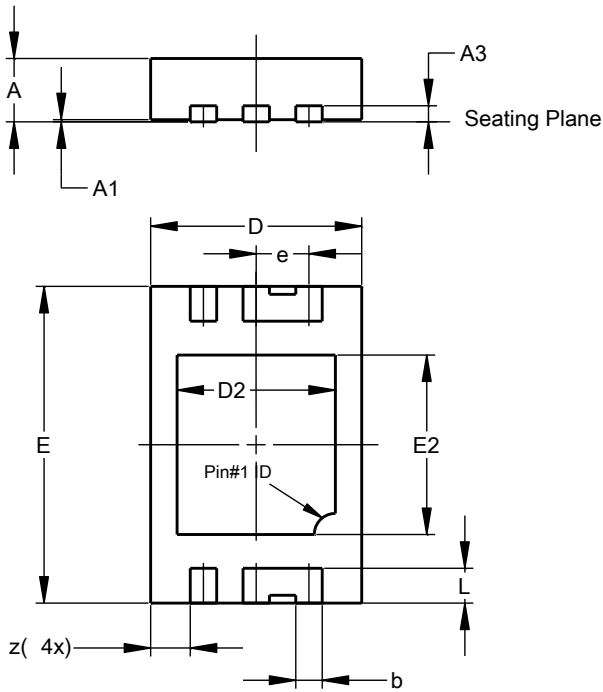


Figure 13. Transient Thermal Resistance

**Package Outline Dimensions**

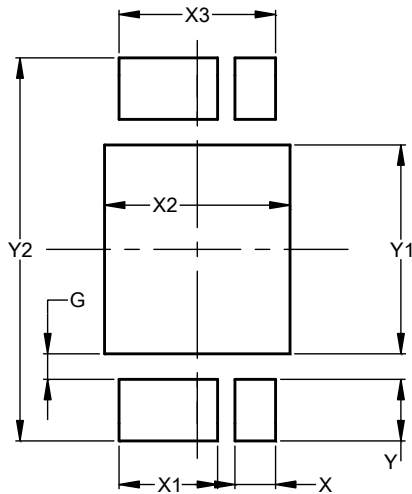
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



U-DFN2030-6 (Type B)			
Dim	Min	Max	Typ
A	0.55	0.65	0.60
A1	0.00	0.05	0.02
A3	--	--	0.15
b	0.20	0.30	0.25
D	1.95	2.05	2.00
D2	1.40	1.60	1.50
E	2.95	3.05	3.00
E2	1.65	1.75	1.70
e	--	--	0.50
L	0.28	0.38	0.33
z	--	--	0.375
All Dimensions in mm			

**Suggested Pad Layout**

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
G	0.220
X	0.350
X1	0.850
X2	1.600
X3	1.350
Y	0.530
Y1	1.800
Y2	3.300

NEW PRODUCT

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