





NPN-POWER TRANSISTOR



CSD200 TO-3 Metal Can Package

ABSOLUTE MAXIMUM RATING

PARAMETER	SYMBOL	VALUE	UNITS
Collector-base voltage (open emitter)	V _{CBO}	1500	V
Collector-emitter voltage (V _{BE} = 0)	V _{CES}	1500	V
Emitter-base voltage (open collector)	V _{EBO}	5	V
Collector current (peak)	l _{CP}	2.5	Α
Base current (peak)	l _{BP}	2.5	Α
Total power dissipation up to $T_c = 90^{\circ}C$	P _{tot}	16	W
Junction temperature	T _J	200	°C
Storage temperature	T _{stg}	-65 to 200	°C





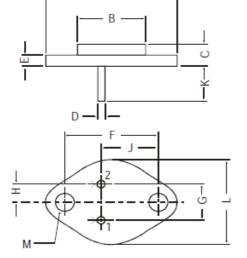
$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_{_{\!A}}\text{=}25^{\circ}\text{C unless otherwise specified})$

			VALUE			
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS	
Collector cut-off current	I _{CBO}	I _E =0, V _{CB} = 750V		50	μА	
		I _E =0, V _{CB} = 1500V		1	mA	
Collector-emitter voltage	V _{CES}	$I_C = 1 \text{ mA}, V_{BE} = 0$	1500		V	
Collector-base voltage	V _{CBO}	I _C = 1mA, I _E = 0	1500		V	
Emitter-base voltage	V _{EBO}	I _E = 1mA, I _C = 0	5		V	
Collector-emitter saturation voltage	V _{CEsat}	I _C = 2A, I _B = 1A		5.0	V	
Base-emitter saturation voltage	V_{BEsat}	I _C = 2A, I _B = 1A		1.5	V	
D.C. Current gain	h _{FE}	I _C = 2A, V _{CE} = 5V	typ.	2.5		

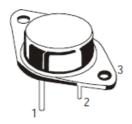




TO-3 Metal Can Package



	DIM	MIN.	MAX.
	Α	_	39.37
	В	_	22.22
	С	6.35	8.50
	D	0.96	1.09
	Е	_	1.77
	F	29.90	30.40
E E ⊑	G	10.69	11.18
	Η	5.20	5.72
ons	J	16.64	17.15
SUS	K	11.15	12.25
dimensions	L	_	26.67
Ē	М	3.84	4.19



PIN CONFIGURATION

- 1. BASE
- 2. EMITTER
- 3. COLLECTOR

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTIER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	GrWt
TO-3	100 pcs/pkt	1.3 kg/100 pcs	12.5" x 8" x 1.8"	0.1K	17" x 11.5" x 21"	2K	27.5 kgs







Customer Notes:

Component Disposal Instructions

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

DISCLAIMER

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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Continental Device India Pvt. Limited
C-120 Naraina Industrial Area, New Delhi 110 028, India.
Telephone + 91-11-2579 6150, 4141 1112 Fax + 91-11-2579 5290, 4141 1119
email@cdil.com www.cdil.com
CIN No. U32109DL1964PTC004291