

dsPIC33EPXXXMU806/810/814 and PIC24EPXXXGU810/814

dsPIC33EPXXXMU806/810/814 and PIC24EPXXXGU810/814 PRODUCT FAMILIES

The device names, pin counts, memory sizes and peripheral availability of each device are listed in [Table 1](#). Their pinout diagrams appear on the following pages.

TABLE 1: dsPIC33EPXXXMU806/810/814 and PIC24EPXXXGU810/814 CONTROLLER FAMILIES

Device	Pins	Packages	Program Flash Memory (Kbyte) ⁽¹⁾	RAM (Kbyte) ⁽²⁾	Remappable Peripherals											RTCC	I ² C™	CRC Generator	10-bit/12-bit ADC ⁽⁸⁾	USB	Parallel Master Port	I/O Pins	
					16-bit Timer ^(3,4)	Input Capture	Output Compare (with PWM)	Motor Control PWM (Channels) ⁽⁵⁾	QEI	UART with IrDA®	SPI	ECAN™	External Interrupts ⁽⁶⁾	DMA Controller (Channels)	DCI								Analog Comparators/Inputs Per Comparator ⁽⁷⁾
dsPIC33EP256MU806	64	QFN, TQFP	280	28	9	16	16	8	2	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 24 ch	1	Y	51
dsPIC33EP256MU810	100	TQFP	280	28	9	16	16	12	2	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	83
	121	XBGA																					
dsPIC33EP256MU814	144	TQFP, LQFP	280	28	9	16	16	14	2	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	122
dsPIC33EP512MU810	100	TQFP	536	52	9	16	16	12	2	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	83
	121	XBGA																					
dsPIC33EP512MU814	144	TQFP, LQFP	536	52	9	16	16	14	2	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	122
PIC24EP256GU810	100	TQFP	280	28	9	16	16	0	0	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	83
	121	XBGA																					
PIC24EP256GU814	144	TQFP, LQFP	280	28	9	16	16	0	0	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	122
PIC24EP512GU810	100	TQFP	536	52	9	16	16	0	0	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	83
	121	XBGA																					
PIC24EP512GU814	144	TQFP, LQFP	536	52	9	16	16	0	0	4	4	2	5	15	1	3/4	1	2	1	2 ADC, 32 ch	1	Y	122

- Note**
- 1: Flash size is inclusive of 24 Kbytes of auxiliary Flash.
 - 2: RAM size is inclusive of 4 Kbytes of DMA RAM (DPSRAM) for all devices.
 - 3: Up to eight of these timers can be combined into four 32-bit timers.
 - 4: Eight out of nine timers are remappable.
 - 5: PWM faults and Sync signals are remappable.
 - 6: Four out of five interrupts are remappable.
 - 7: Comparator output is remappable.
 - 8: The ADC2 module supports 10-bit mode only.