

# μPFC<sup>™</sup>

# SOT-23 Boost PFC Control IC

#### **Features**

- Critical-conduction mode PFC control
- High PF and ultra-low THD
- Wide load and line range
- Regulated and programmable DC bus voltage
- No secondary winding required
- MOSFET cycle-by-cycle over-current protection
- DC bus over-voltage protection
- Low EMI gate drive
- Ultra-low start-up current
- 20.8V internal zener clamp on VCC
- Excellent ESD and latch immunity
- RoHS compliant
- 5-pin SOT-23 package

### **Applications**

- Off-line power supply
- Electronic ballast
- LED power supply

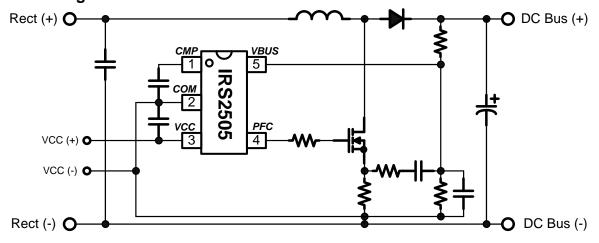
### **Description**

The IRS2505 is a control IC for boost-type PFC circuits operating in critical-conduction mode. The IC incorporates a free-running frequency oscillator with on- and off-time control of the boost power MOSFET without the need for a secondary winding. Also included in the design is over-voltage protection of the DC bus and cycle-by-cycle over-current protection of the power MOSFET. Micro-power start-up current and an internal 20.8V zener clamp at VCC are provided to simplify the external VCC supply circuitry. ESD and latch immune CMOS technology enables a rugged monolithic IC construction.

### **Package Options**



### **Application Diagram**

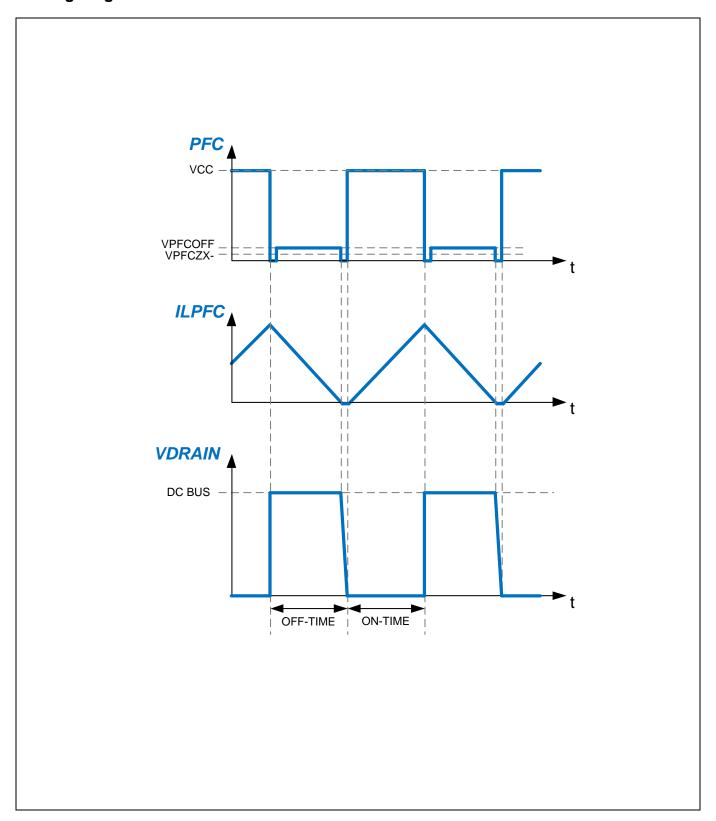


### **Ordering Information**

Daga Baut Neurolean	Daalaana Tama	Standar	d Pack	On land to Bart Name Lan	
Base Part Number	Package Type	Form	Quantity	Orderable Part Number	
IRS2505LPBF	5L-SOT-23	Tape and Reel	3000	IRS2505LTRPBF	



### **Timing Diagram**





### Qualification Information<sup>†</sup>

Qualification information					
		Industrial <sup>††</sup> (per JEDEC JESD 47E)			
Qualification Level		Comments: This family of ICs has passed JEDEC's Industrial qualification. IR's Consumer qualification level is granted by extension of the higher Industrial level.			
Moisture Sensitivity Level		SOT-23	MSL1 <sup>†††</sup> (per IPC/JEDEC J-STD-020C)		
ESD	Machine Model	Class B (per JEDEC standard EIA/JESD22-A115-A)			
E3D	Human Body Model	Class 2 (per EIA/JEDEC standard JESD22-A114-B)			
IC Latch-Up Test		Class I, Level A			
			(per JESD78A)		
RoHS Compliant		Yes			

- † Qualification standards can be found at International Rectifier's web site <a href="http://www.irf.com/">http://www.irf.com/</a>
- †† Higher qualification ratings may be available should the user have such requirements. Please contact your International Rectifier sales representative for further information.
- ††† Higher MSL ratings may be available for the specific package types listed here. Please contact your International Rectifier sales representative for further information.



#### **Absolute Maximum Ratings**

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are absolute voltages referenced to COM, all currents are defined positive into any pin. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions.

Symbol	Definition	Min.	Max.	Units	
VPFC	PFC pin voltage		-0.3	VCC + 0.3	V
VBUS	VBUS pin voltage		-0.3	VCC + 0.3	V
ICC	VCC pin supply current <sup>†</sup>			25	mA
ICMP	CMP pin current <sup>††</sup>			1	IIIA
PD	Package power dissipation @ Ta ≤ SOT-23 +25 °C 5L			250	mW
RΘja	Thermal resistance, junction to SOT-23 ambient SL			191	°C/W
Tj	Junction temperature		-55	150	
Ts	Storage temperature		-55	150	°C
TL	Lead temperature (soldering, 10 sec	onds)		300	

- † This IC contains a voltage clamp structure between the chip VCC and COM which has a nominal breakdown voltage of 20.8V. This supply pin should not be driven by a DC, low impedance power source greater than the VCLAMP specified in the Electrical Characteristics section.
- This IC contains a voltage clamp structure between the CMP and COM which has a †† nominal breakdown voltage of 10.2V. This pin should not be driven by a DC, low impedance power source greater than the VZCMP specified in the Electrical Characteristics section.



## **Recommended Operating Conditions**

For proper operation the device should be used within the recommended conditions.

Symbol	Definition	Min.	Max.	Units
VCC	Supply voltage	VCCUV+ + 0.5V	VCLAMP	V
ICC	Supply current	0	10	mA
VCMP	CMP pin voltage	0	VZCMP	V
Tj	Junction temperature	-40	125	°C

### **Recommended Component Values**

4 www.irf.com

Symbol	Component	Min.	Max.	Units
CCMP	Compensation capacitor value	470		nF
CVCC VCC filter capacitor		100		nF
CVBUS	VBUS pin filter capacitor	1.0		nF

© 2013 International Rectifier July 22, 2013



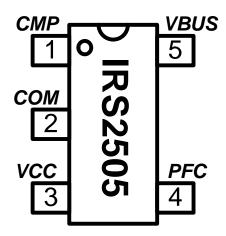
### **Electrical Characteristics**

VCC=14V, CVCC=0.1uF, CCMP=0.68uF, CPFC=1nF, CVBUS=10nF, and Ta=25°C unless otherwise specified.

Symbol	Definition	Min	Тур	Max	Units	Test Conditions	
Supply Charac	teristics						
VCLAMP	VCC clamp voltage	19.8	20.8	21.8		ICC = 19 mA	
VCCUV+	Rising VCC under-voltage lock-out threshold	10.0	11.1	12.0	V		
VCCUV-	Falling VCC under-voltage lock-out threshold	7.0	7.9	9.0	V	N/A	
VCCUVHYS	VCC UVLO hysteresis		3.2				
IQCCUV	Micro-power start-up VCC supply current			60.0	μA	VCC = VCCUV+ - 500mV rising	
ICC50kHz	VCC current @ 50kHz		2.5		mA	MOSFET = IRF840A	
PFC Character	istics						
VBUSREG	VBUS Pin Regulation Voltage	4.02	4.1	4.18			
VBUSOV+	VBUS Pin OVP Threshold		4.47				
VBUSOV-	VBUS Pin OVP Fault Reset Threshold		4.26		V		
VBUSOC+	VBUS Pin Over-Current Threshold		0.56			VBUS = 3V	
tWD	Watch Dog Timer Pulse Interval		125				
ICMP+	CMP Pin OTA IO+	-	30		^	VBUS=3.5V,VCMP=0V	
ICMP-	CMP Pin OTA IO-		-30		μΑ	VBUS=4.5V,VCMP=5V	
VСМРОН	OTA Output Voltage Swing (high state)	9.9	10.2	11.5		VBUS=3.5V	
VCMPOL	OTA Output Voltage Swing (low state)		0		V	VBUS=4.5V	
VCMPFLT	OTA Output Voltage in Fault Mode		0				
VZCMP	CMP Pin Clamp Voltage	9.9	10.2	11.5		VBUS=3.5V	
Gate Driver Ou	tput Characteristics (PFC)						
VPFCON	Gate High Voltage		VCC		V		
VPFCOFF	Gate Low Voltage		0.59		V		
tf	Output fall time		20		nsec	Falling, 80% to 20%	
10-	Output sink current	-	450		mA		



### **Pin Assignments and Definitions**

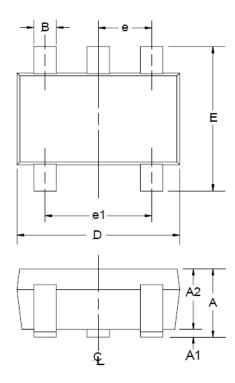


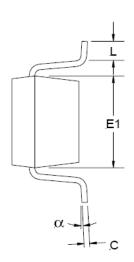
Pin	Name	Description		
1	CMP	PFC error amplifier compensation		
2	СОМ	IC power and signal ground		
3	VCC	Logic and gate drive supply voltage		
4	PFC	PFC gate driver output and zero-crossing detection		
5	VBUS	DC bus sensing input, OVP and OCP		

n © 2013 International Rectifier July 22, 2013



## Package Details: 5 Lead SOT23



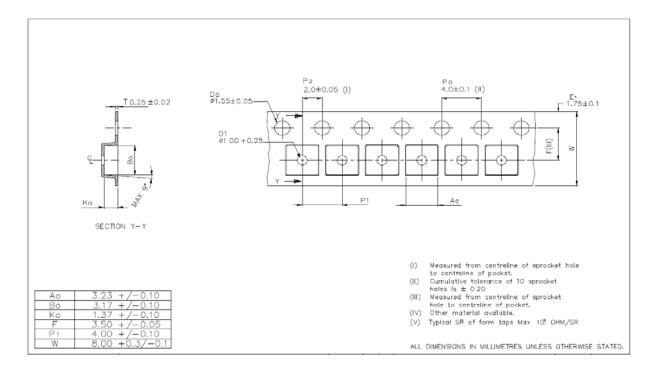


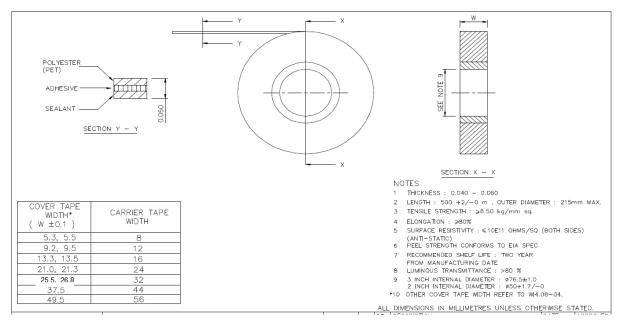
SYMBOL	MIN	MAX	
Α	0.90	1.45	
A1	0.00	0.15	
A2	0.90	1.30	
В	0.25	0.50	
С	0.09	0.20	
D	2.80	3.00	
Ш	2.60	3.00	
E1	1.50	1.75	
е	0.95	REF	
e1	1.90 REF		
L	0.35	0.55	
α	08	108	

NOTE: ALL MEASUREMENTS ARE IN MILLIMETERS.



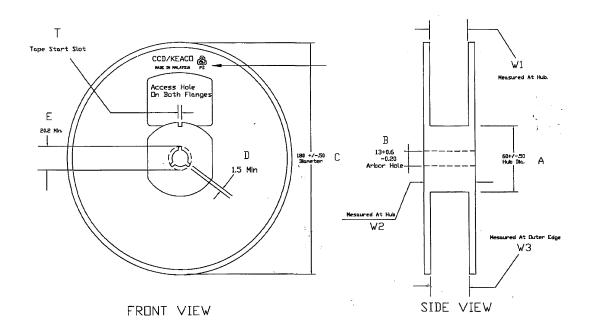
### Tape and Reel Details: 5 Lead SOT23

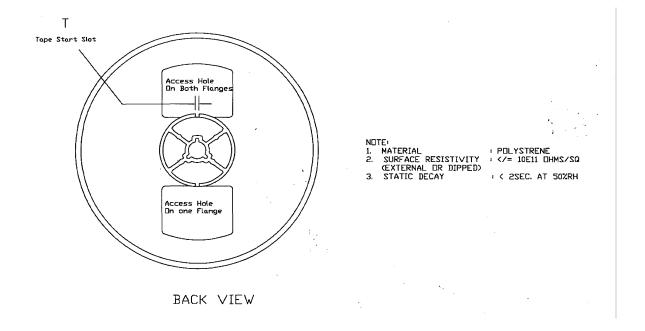






### Tape and Reel Details: 5 Lead SOT23



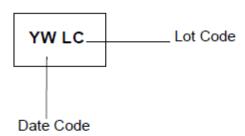


© 2013 International Rectifier July 22, 2013

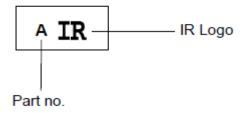


#### Part Marking Information: 5 Lead SOT23

### **Top Marking**



### **Bottom Marking**



The information provided in this document is believed to be accurate and reliable. However, International Rectifier assumes no responsibility for the consequences of the use of this information. International Rectifier assumes no responsibility for any infringement of patents or of other rights of third parties which may result from the use of this information. No license is granted by implication or otherwise under any patent or patent rights of International Rectifier. The specifications mentioned in this document are subject to change without notice. This document supersedes and replaces all information previously supplied.

For technical support, please contact IR's Technical Assistance Center <a href="http://www.irf.com/technical-info/">http://www.irf.com/technical-info/</a>

#### **WORLD HEADQUARTERS:**

233 Kansas St., El Segundo, California 90245 Tel: (310) 252-7105