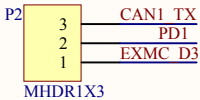


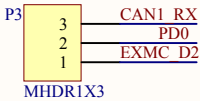
Company Name: GigaDevice		
File Name: AD_DA		
Revision: 1.0	Data: 2013-8-20	Author: XuFei

CAN

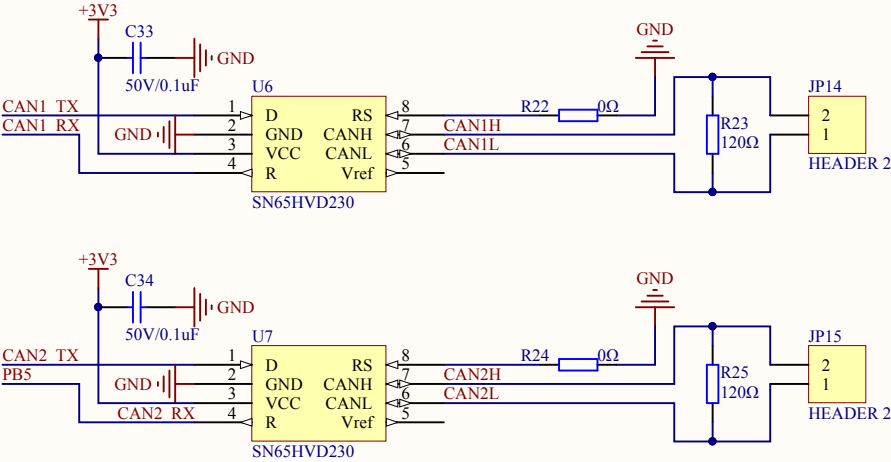
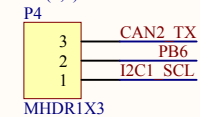
Short P2(1,2) for EXMC function
Short P2(2,3) for CAN1 function



Short P3(1,2) for EXMC function
Short P3(2,3) for CAN1 function



Short P4(1,2) for I2C1 function
Short P4(2,3) for CAN2 function



Company Name: GigaDevice

File Name: CAN

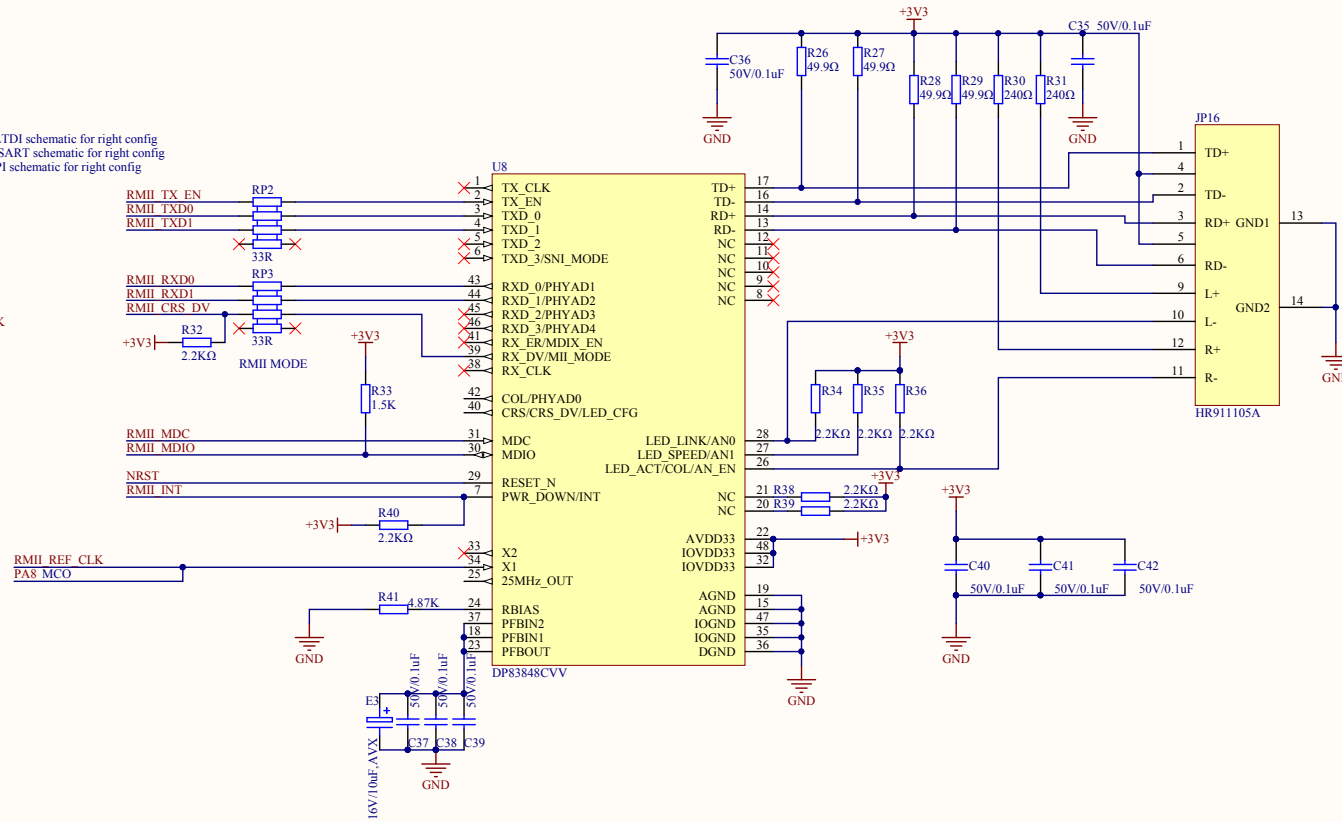
Revision: 1.0

Data: 2013-7-24

Author: XuFei

PB11 is an AFIO, refer to LTD1 schematic for right config
PA2 is an AFIO, refer to USART schematic for right config
PA7 is an AFIO, refer to SPI schematic for right config

PB11 RMII TX_EN
PB12 RMII TXD0
PB13 RMII TXD1
PC4 RMII RXD0
PC5 RMII RXD1
PA7 RMII CRS_DV
PC1 RMII MDC
PA2 RMII MDIO
PB0 RMII INT
PA1 RMII REF_CLK



Company Name: GigaDevice

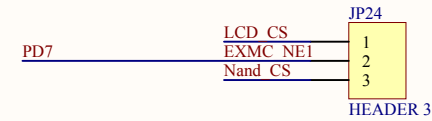
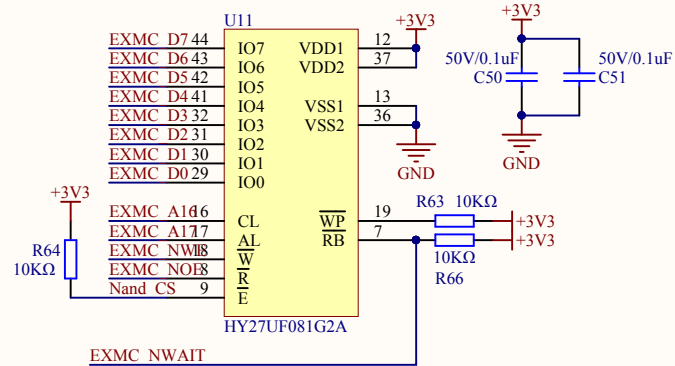
File Name: Ethernet

Revision: 1.0

Date: 2013-8-21

Author: Xufei

Nand Flash



PD0,PD1 are AFIOs, please refer to CAN schematic for right config

PD14	EXMC D0
PD15	EXMC D1
PD0	EXMC D2
PD1	EXMC D3
PE7	EXMC D4
PE8	EXMC D5
PE9	EXMC D6
PE10	EXMC D7

PD11	EXMC A16
PD12	EXMC A17
PD4	EXMC NOE
PD5	EXMC NWE
PD6	EXMC NWAIT

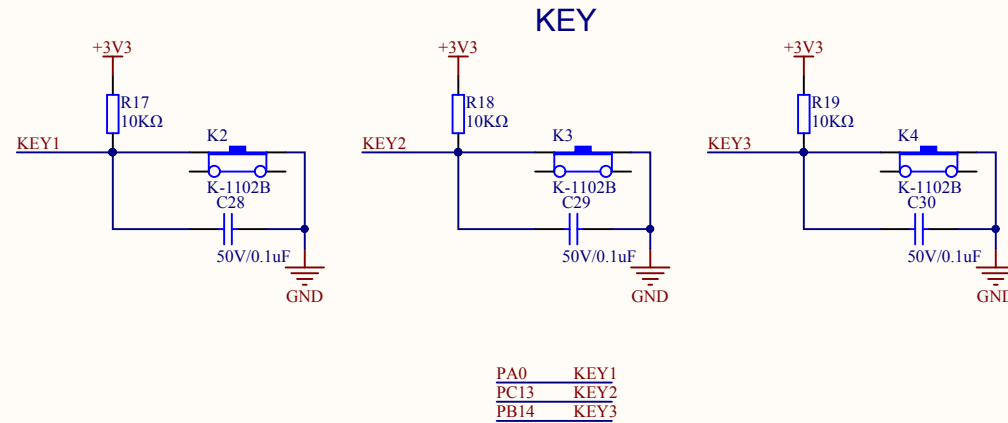
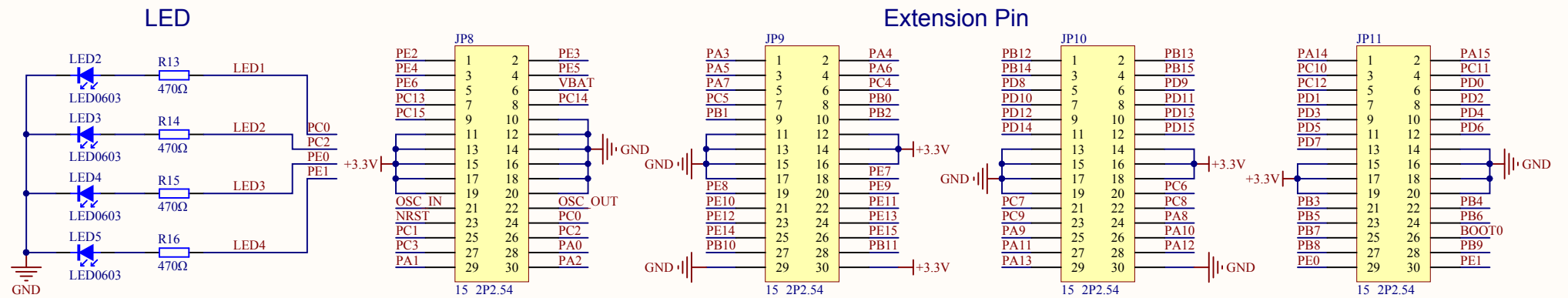
Company Name: GigaDevice

File Name: EXMC

Revision: 1.0

Data: 2013-8-20

Author: XuFei



Company Name: GigaDevice

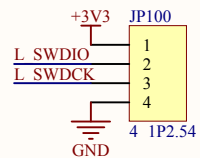
File Name: Extension

Revision: 1.0

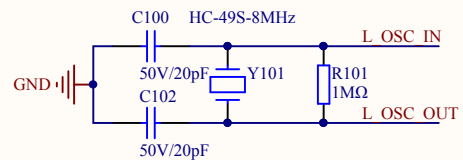
Data: 2013-8-20

Author: XuFei

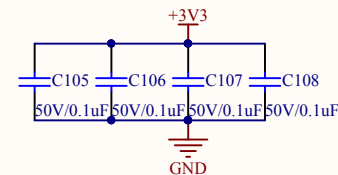
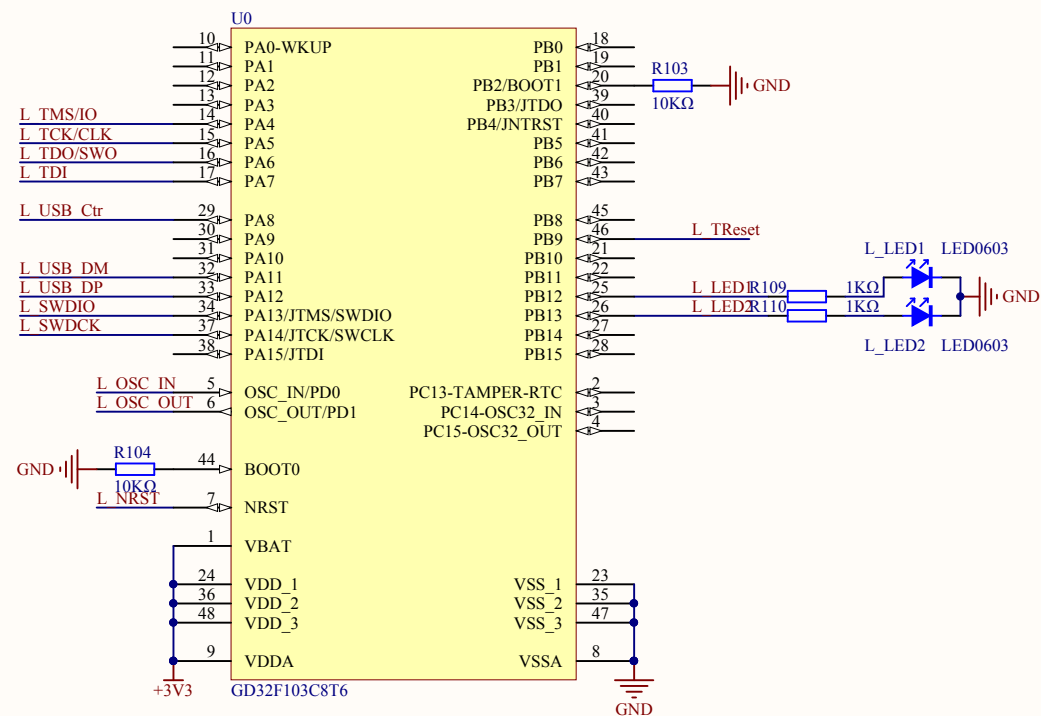
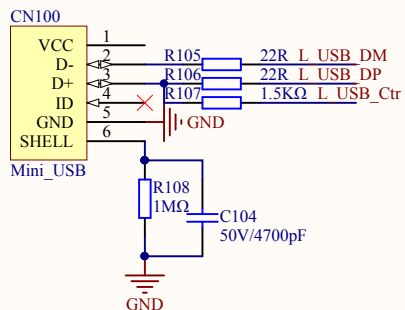
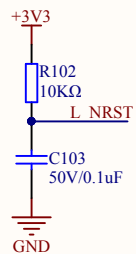
MCU SWD



HSE



Reset



Company Name: GigaDevice

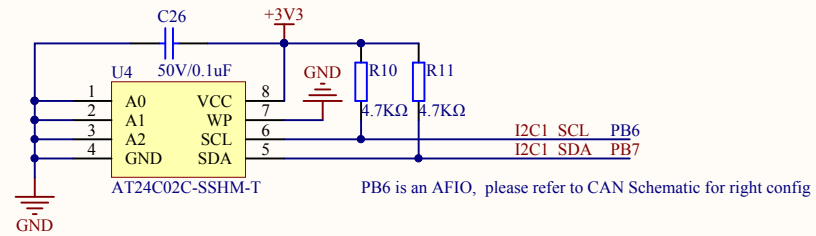
File Name: GDLink

Revision: 1.0

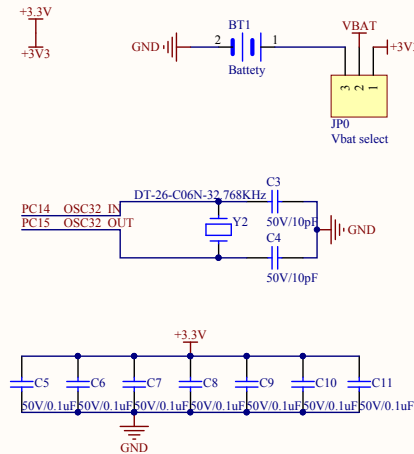
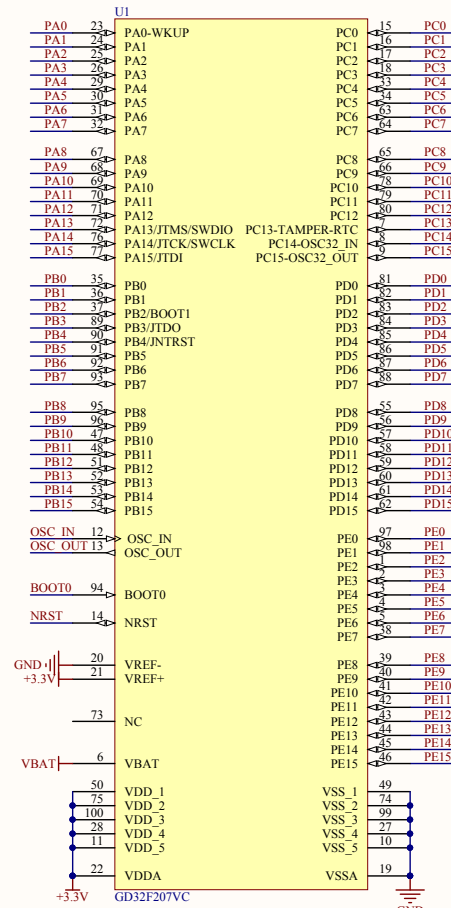
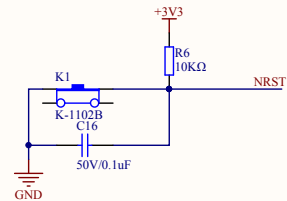
Data: 2015-01-09

Author: XuFei

I2C



Company Name: GigaDevice		
File Name: I2C_SMbus		
Revision: 1.0	Data: 2013-8-20	Author: XuFei



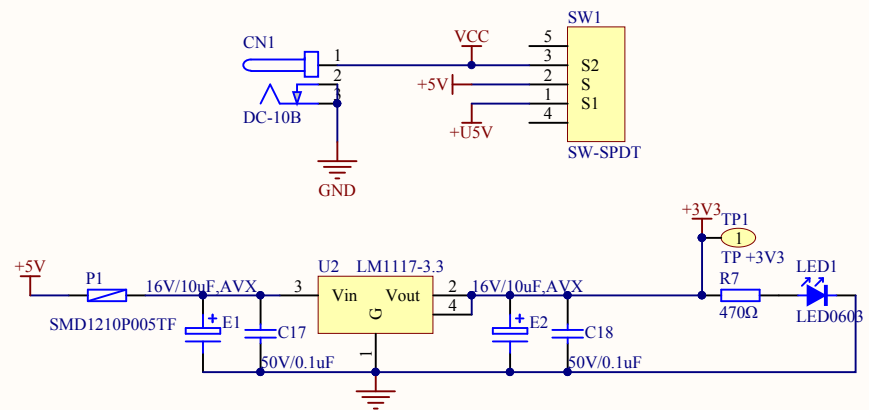
Company Name: GigaDevice

File Name: MCU

Revision: 1.0

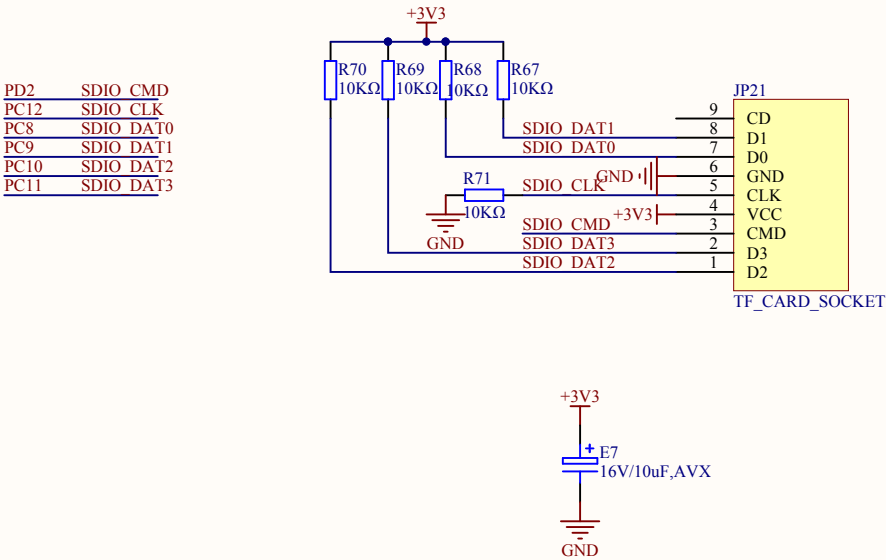
Data: 2013-8-20

Author: XuFei



Company Name: GigaDevice		
File Name: Power		
Revision: 1.0	Data: 2013-8-20	Author: XuFei

SDIO



Company Name: GigaDevice

File Name: SDIO

Revision: 1.1

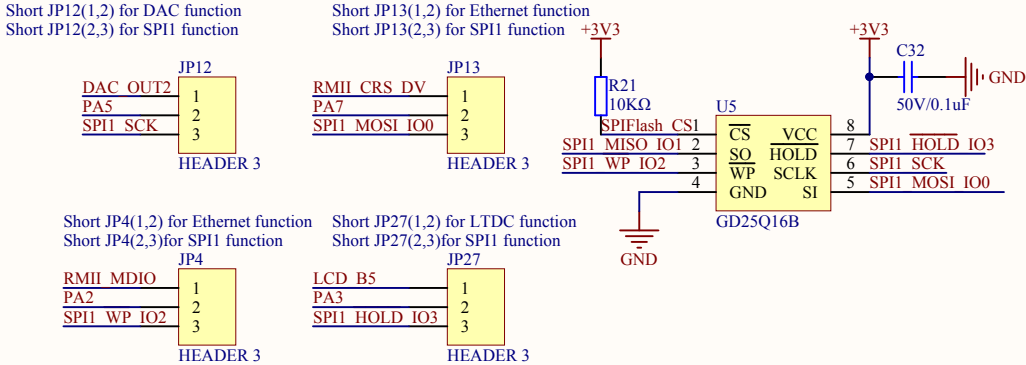
Data: 2015-2

Author: Xufei

Standard & Quad SPI Flash

PA6 is AFIO, please refer to LTDC schematic for correct config

PA6	SPI1	MISO	IO1
PE3	SPIFlash	CS	



Company Name: GigaDevice

File Name: SPI

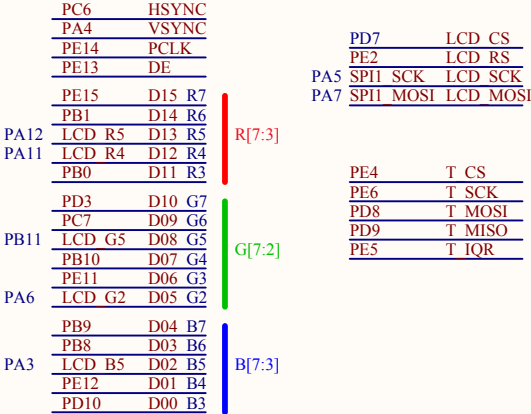
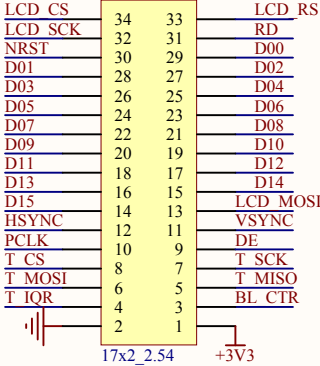
Revision: 1.0

Data: 2013-8-20

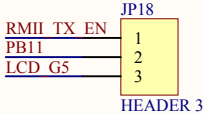
Author: XuFei

LCD

JP23

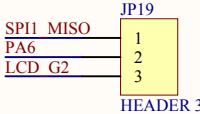


Short JP18(1,2)for Ethernet function
Short JP18(2,3)for TLDI function



HEADER 3

Short JP19(1,2)for SPI function
Short JP19(2,3)for TLDI function



HEADER 3

Company Name: GigaDevice

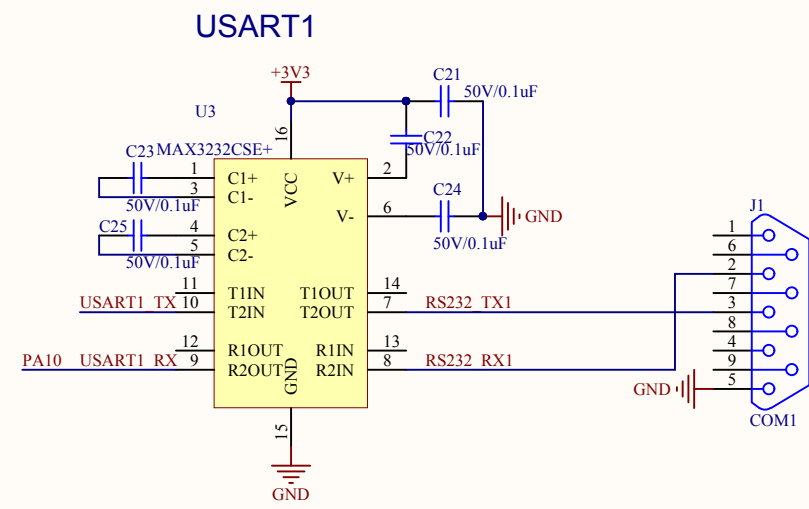
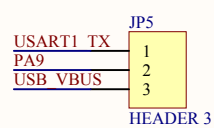
File Name: TLDI

Revision: 1.1

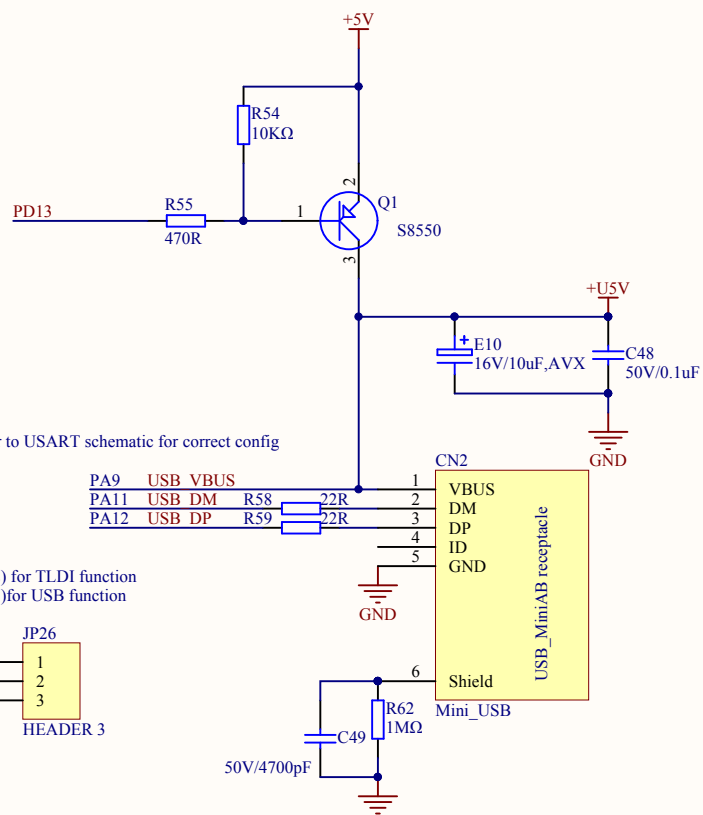
Data: 2015-08

Author: XuFei

Short JP5(1,2)for USART1 function
Short JP5(2,3)for USB Device function



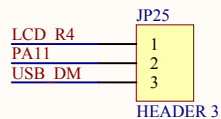
Company Name: GigaDevice		
File Name: USART		
Revision: 1.0	Data: 2015-07	Author: XuFei



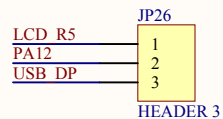
PA9 is AFIO, please refer to USART schematic for correct config

PA9	USB	VBUS
PA11	USB	DM
PA12	USB	DP

Short JP25(1,2) for TLDI function
Short JP25(2,3)for USB function



Short JP26(1,2) for TLDI function
Short JP26(2,3)for USB function



Company Name: GigaDevice		
File Name: USB_OTG		
Revision: 1.1	Data: 2013.9.10	Author: Xufei

