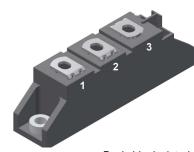
### **MDNA50P2200TG**

| High Voltage Standard Rectifier Module |         | <b>= 2</b> 2 | x 2200 V |  |
|--|---------|--------------|----------|--|
| 3                                      |         | =            | 50 A     |  |
|  | $V_{F}$ | =            | 1.09 V   |  |
|  |         |              |          |  |

Phase leg

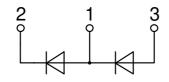
Part number **MDNA50P2200TG** 



Backside: isolated



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### Features / Advantages:

- Package with DCB ceramic
- Improved temperature and power cycling
- Planar passivated chips
- Very low forward voltage drop
- · Very low leakage current

### **Applications:**

- Diode for main rectification
- For single and three phase
- bridge configurations
- Supplies for DC power equipment
- Input rectifiers for PWM inverter
- Battery DC power supplies
- · Field supply for DC motors

### Package: TO-240AA

- Isolation Voltage: 4800 V~
- Industry standard outline
- RoHS compliant
- Height: 30 mm
- Base plate: DCB ceramic
- Reduced weight
- Advanced power cycling

#### Terms and Conditions of Usage

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact your local sales office. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact your local sales office. Should you intend to use the product in aviation, in health or life endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747and per semiconductor unless otherwise specified

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## MDNA50P2200TG

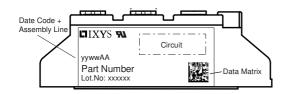
| Rectifier                | ·                                 |  |                                 |      | Rating | S    |      |
|--------------------------|-----------------------------------|--|---------------------------------|------|--------|------|------|
| Symbol                   | Definition                        | Conditions   |                                 | min. | typ.   | max. | Unit |
| V <sub>RSM</sub>         | max. non-repetitive reverse bloc  | king voltage                                       | $T_{VJ} = 25^{\circ}C$          |      |        | 2300 | V    |
| V <sub>RRM</sub>         | max. repetitive reverse blocking  | voltage  | $T_{VJ} = 25^{\circ}C$          |      |        | 2200 | V    |
| I <sub>R</sub>           | reverse current                   | $V_{R} = 2200 V$                                   | $T_{VJ} = 25^{\circ}C$          |      |        | 50   | μA   |
|                          |                                   | $V_{R} = 2200 V$                                   | $T_{VJ} = 150^{\circ}C$         |      |        | 1.5  | mA   |
| V <sub>F</sub>           | forward voltage drop              | I <sub>F</sub> = 50 A                              | $T_{VJ} = 25^{\circ}C$          |      |        | 1.13 | V    |
|                          |                                   | $I_{F} = 100 \text{ A}$                            |                                 |      |        | 1.34 | V    |
|                          |                                   | $I_{F} = 50 \text{ A}$                             | T <sub>VJ</sub> = 125 °C        |      |        | 1.09 | V    |
|                          |                                   | $I_{F} = 100 \text{ A}$                            |                                 |      |        | 1.37 | V    |
| FAV                      | average forward current           | T <sub>c</sub> = 100°C                             | T <sub>vJ</sub> = 150°C         |      |        | 50   | Α    |
|                          |                                   | rectangular d = 0.5                                |                                 |      |        |      |      |
| V <sub>F0</sub>          | threshold voltage                 |  | T <sub>vj</sub> = 150°C         |      |        | 0.80 | V    |
| r <sub>F</sub>           | slope resistance } for power      | loss calculation only                              |                                 |      |        | 5.7  | mΩ   |
| <b>R</b> <sub>thJC</sub> | thermal resistance junction to ca | ase  |                                 |      |        | 0.65 | K/W  |
| R <sub>thCH</sub>        | thermal resistance case to heats  | sink   |                                 |      | 0.20   |      | K/W  |
| P <sub>tot</sub>         | total power dissipation           |  | $T_c = 25^{\circ}C$             |      |        | 190  | W    |
|                          | max. forward surge current        | t = 10 ms; (50 Hz), sine                           | $T_{VJ} = 45^{\circ}C$          |      |        | 850  | Α    |
|                          |                                   | t = 8,3 ms; (60 Hz), sine                          | $V_{R} = 0 V$                   |      |        | 920  | Α    |
|                          |                                   | t = 10 ms; (50 Hz), sine                           | T <sub>vJ</sub> = 150°C         |      |        | 725  | Α    |
|                          |                                   | t = 8,3 ms; (60 Hz), sine                          | $V_{R} = 0 V$                   |      |        | 780  | Α    |
| l²t                      | value for fusing                  | t = 10 ms; (50 Hz), sine                           | $T_{VJ} = 45^{\circ}C$          |      |        | 3.62 | kA²s |
|                          |                                   | t = 8,3 ms; (60 Hz), sine                          | $V_{R} = 0 V$                   |      |        | 3.52 | kA²s |
|                          |                                   | t = 10 ms; (50 Hz), sine                           | $T_{vJ} = 150 ^{\circ}\text{C}$ |      |        | 2.63 | kA²s |
|                          |                                   | t = 8,3 ms; (60 Hz), sine                          | $V_R = 0 V$                     |      |        | 2.53 | kA²s |
| C                        | junction capacitance              | $V_{R} = 400 \text{ V}; \text{ f} = 1 \text{ MHz}$ | $T_{VJ} = 25^{\circ}C$          |      | 27     |      | pF   |

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## **MDNA50P2200TG**

| Package TO-240AA            |   | Ratings                        |                             |      |      |      |      |      |
|-----------------------------|---|--------------------------------|-----------------------------|------|------|------|------|------|
| Symbol                      | Definition  | Conditions                     |                             |      | min. | typ. | max. | Unit |
|                             | RMS current   | per terminal                   |                             |      |      |      | 200  | Α    |
| T <sub>vj</sub>             | virtual junction temperature                        |                                |                             |      | -40  |      | 150  | °C   |
| T <sub>op</sub>             | operation temperature                               |                                |                             |      | -40  |      | 125  | °C   |
| T <sub>stg</sub>            | storage temperature                                 |                                |                             |      | -40  |      | 125  | °C   |
| Weight                      |   |                                |                             |      |      | 76   |      | g    |
| M <sub>D</sub>              | mounting torque                                     |                                |                             |      | 2.5  |      | 4    | Nm   |
| M <sub>T</sub>              | terminal torque                                     |                                |                             |      | 2.5  |      | 4    | Nm   |
| d <sub>Spp/App</sub>        | creepage distance on surface   striking distance th |                                | terminal to terminal        | 13.0 | 9.7  |      |      | mm   |
| <b>d</b> <sub>Spb/Apb</sub> | creepage distance on surrac                         | e   Sunking distance unough an | terminal to backside        | 16.0 | 16.0 |      |      | mm   |
| V                           | isolation voltage                                   | t = 1 second                   | 50/60 Hz, RMS; I⊨soL ≤ 1 mA |      | 4800 |      |      | V    |
|                             |   | t = 1 minute                   |                             |      | 4000 |      |      | V    |



### Part description

M = Module

D = Diode N = High Voltage Standard Rectifier

A = (>= 2000V)50 = Current Rating [A]

P = Phase leg 2200 = Reverse Voltage [V] TG = TO-240AA

| Ordering | Ordering Number | Marking on Product | Delivery Mode | Quantity | Code No. |
|----------|-----------------|--------------------|---------------|----------|----------|
| Standard | MDNA50P2200TG   | MDNA50P2200TG      | Box           | 36       | 525439   |

| Equiva                       | alent Circuits for | Simulation | * on die level | T <sub>vj</sub> = 150 °C |
|------------------------------|--------------------|------------|----------------|--------------------------|
|                              | )R                 | Rectifier  |                |                          |
| V <sub>0 max</sub>           | threshold voltage  | 0.8        |                | V                        |
| $\mathbf{R}_{0 \text{ max}}$ | slope resistance * | 4.5        |                | mΩ                       |

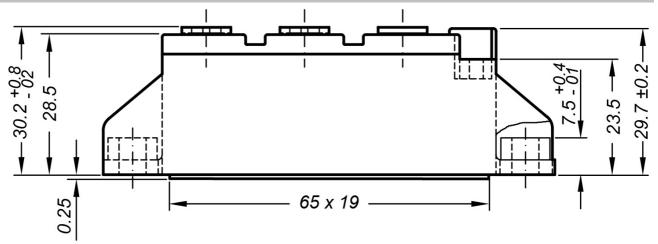
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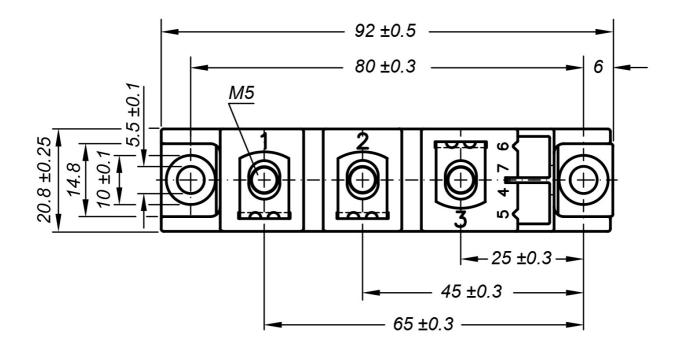
## **MDNA50P2200TG**

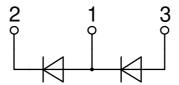
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### **Outlines TO-240AA**



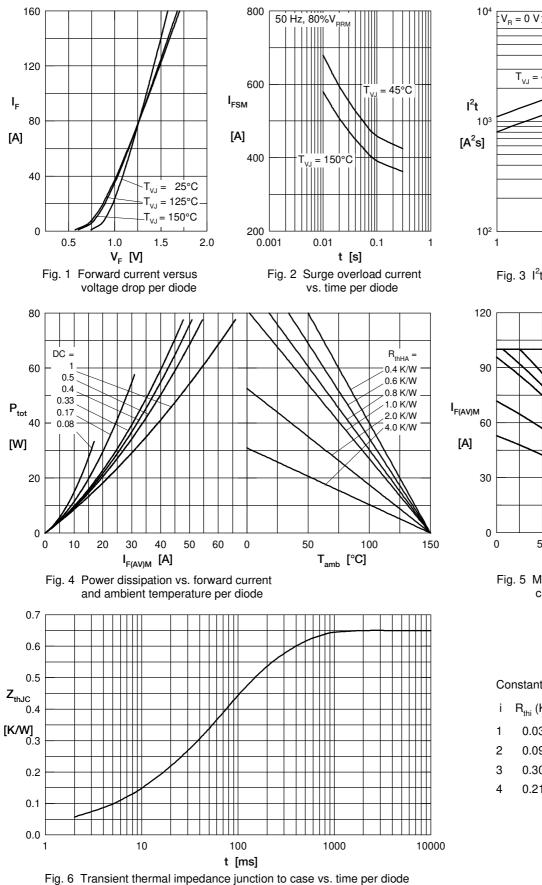
General tolerance: DIN ISO 2768 class "c"

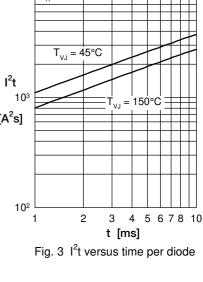


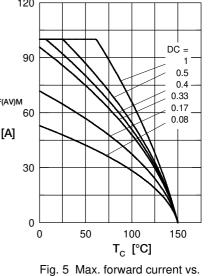


### **MDNA50P2200TG**

### Rectifier







ig. 5 Max. forward current vs. case temperature per diode

Constants for  $Z_{thJC}$  calculation:

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| i | R <sub>thi</sub> (K/W) | t <sub>i</sub> (s) |
|---|------------------------|--------------------|
| 1 | 0.032                  | 0.001              |
| 2 | 0.098                  | 0.010              |
| 3 | 0.305                  | 0.060              |
| 4 | 0.215                  | 0.270              |



