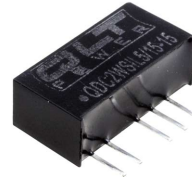


**QDC2WSIL**

1KV ISOLATED, 2W UNREGULATED DUAL OUTPUT, SIL 7 PACKAGE, MTBF>1M HOURS

**Available Inputs:**  
5, 12, 15 and 24 VDC

**Available Outputs:**  
(+/-) 3.3, 5, 9, 12, 15 and 18 VDC



**Electrical Specifications**

(Typical at + 25° C, nominal input voltage, rated output current unless otherwise specified)

**Input Specifications**

Voltage range	+/- 10 %
Filter	Capacitors
Isolation Specifications	
Rated voltage	1000 VDC
Leakage current	1 mA
Resistance	10 <sup>9</sup> Ohm
Capacitance	60 pF type.

**Output Specifications**

Voltage accuracy	+/- 5 %, max.
Ripple and noise (at 20 MHz BW)	75 mV p-p, max.
Short circuit protection	Momentary
Line voltage regulation	+/- 1,2 % / 1,0 % of Vin
Load voltage regulation	+/- 8 %, load = 20 ~ 100 %
Temperature coefficient	+/- 0,02 % / °C

**General Specifications**

Efficiency	70 % to 85 %
Switching frequency	125 KHz, type.

**Environmental Specifications**

Operating temperature (ambient)	- 40° C to + 85° C
Storage temperature	- 55° C to + 125° C
Derating	See graph
Humidity	Up to 90 %, non condensing
Cooling	Free air convection

**Physical Characteristics**

Dimensions SIL	19,50 x 7,00 x 9,50 mm 0,76 x 0,28 x 0,37 inches
Weight	2 .5g
Case material	Non conductive black plastic

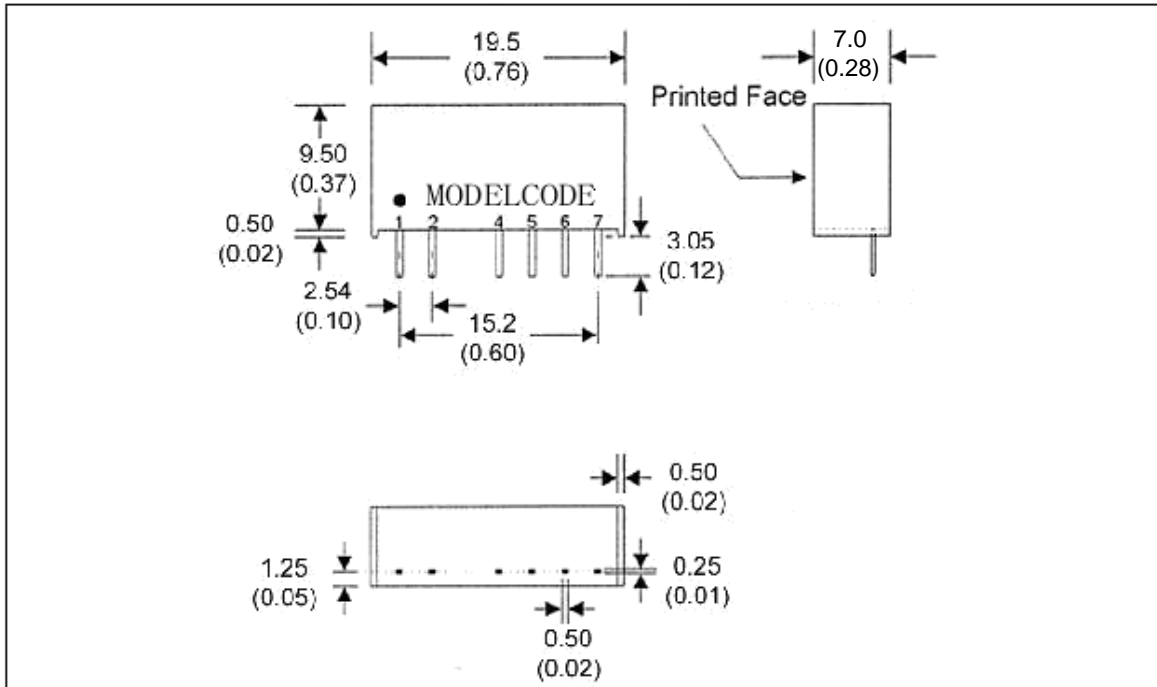
**Examples of Part Numbers**

PART NO.	INPUT VOLTAGE (VDC)	INPUT CURRENT NO LOAD	INPUT CURRENT FULL LOAD	OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT (max. mA)	EFFICIENCY FULL LOAD (% TYPE.)
QDC2WSIL5/5-5	5	28	555	+/-5	+/-200	72
QDC2WSIL5/15-15	5	36	486	+/-15	+/-67	82
QDC2WSIL12/5-5	12	22	225	+/-5	+/-200	74
QDC2WSIL12/12-12	12	20	208	+/-12	+/-84	80
QDC2WSIL12/15-15	12	20	208	+/-15	+/-67	80
QDC2WSIL24/5-5	24	11	111	+/-5	+/-200	75
QDC2WSIL24/12-12	24	8	102	+/-12	+/-84	81
QDC2WSIL24/15-15	24	8	102	+/-15	+/-67	82

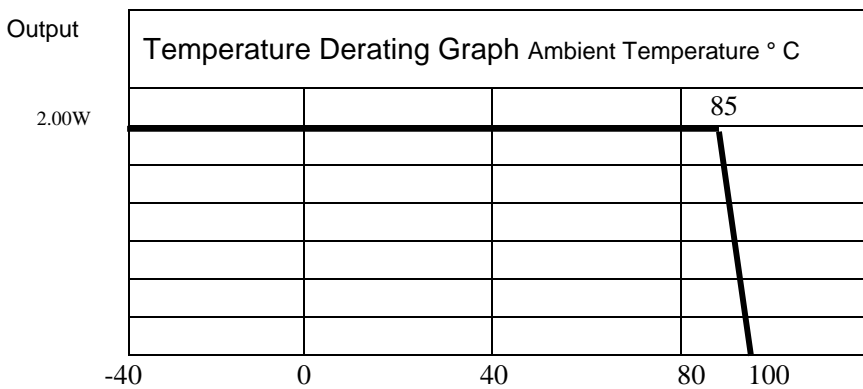
**QDC2WSIL**

1KV ISOLATED, 2W UNREGULATED DUAL OUTPUT, SIL 7 PACKAGE, MTBF>1M HOURS

**Dimensions**



**Derating Graph and Pinning**



Pin	Connection		
1	+	V <sub>in</sub>	Input
2	-	V <sub>in</sub>	Input
3			Omitted
4	-	V <sub>out</sub>	Output
5	0	0V	Common
6	+	V <sub>out</sub>	Output
7			Omitted