TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP624, TLP624-2, TLP624-4

Programmable Controllers AC/DC-Input Module Telecommunication

The TOSHIBA TLP624, -2 and -4 consist of a gallium arsenide infrared emitting diode optically coupled to a photo-transistor. The TLP624-2 offers two isolated channels in an eight lead plastic DIP, while the TLP624-4 provides four isolated channels in a sixteen plastic DIP.

- Collector-emitter voltage: 55 V (min)
- Isolation voltage: 5000 Vrms (min)
- UL recognized: UL1577, file No.E67349
- cUL recognized : CSA Component Acceptance Service No. 5A File No.E67349
- Option (D4) type

VDE approved : EN60747-5-5 (Note)

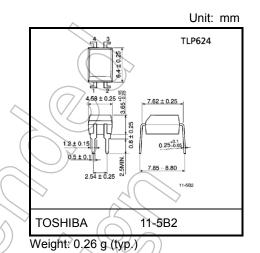
Maximum operating insulation voltage: 890 VPK Highest permissible over voltage: 8000 VPK

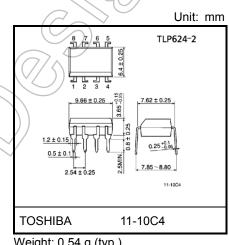
Note: When an EN 60747-5-5 approved type is needed, please designate the "Option(D4)".

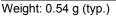
	Cur	Marking		
Classification	Ta =	25°C	Ta=-25 to 75°C	of
Classification	IF=1mA	IF=0.5mA	IF=1mA	classification
	VCE=0.5V	VCE=1.5V	VCE=0.5V	olassinoation
Rank BV	200%	100%	100%	BV
Standard	100%	50%	50%	BV, blank

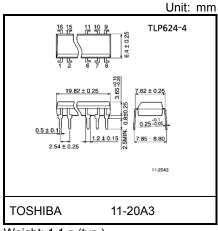
Note: Application type name for certification test, please use standard

product type name, i.e. TLP624(BV): TLP624 TLP624-2(BV); TLP624-2





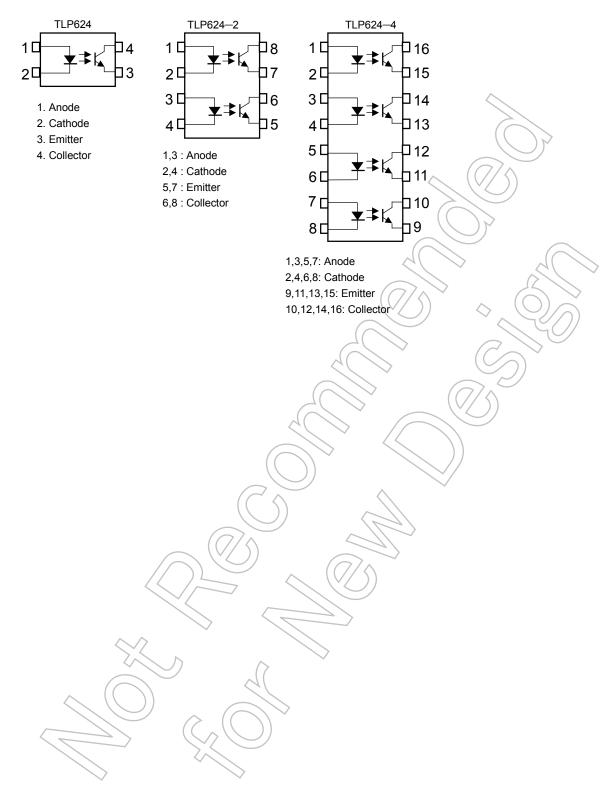






Start of commercial production 1986-04

Pin Configurations (top view)



Absolute Maximum Ratings (Ta = 25°C)

			Ra	ting	
	Characteristic	Symbol	TLP624	TLP624-2 TLP624-4	Unit
	Forward current	lF	60	50	mA
	Forward current detating	ΔI _F / °C	-0.7(Ta ≥ 39°C)	-0.5(Ta ≥ 25°C)	mA / °C
	Pulse forward current	IFP	1 (100µs, pt	ilse, 100pps)	А
LED	Diode Power dissipation	PD	100	70	mW
	Diode Power dissipation derating	ΔP _D / °C	-1.2(Ta ≥ 39°C)	-0.7(Ta ≥ 25°C)	mW / °C
	Reverse voltage	VR	(\bigcirc)	5	V
Junction temperature		Tj	125		°C
	Collector-emitter voltage		() 5	V	
	Emitter-collector voltage	VECO		7	V
ctor	Collector current	IC	5	io 7 ()	mA
Detector	Collector power dissipation (1 circuit)	PC	150	100	mW
	Collector power dissipation derating (Ta \ge 25°C, 1 circuit)	∆Pc/°C	-1.5	-1.0	mW / °C
	Junction temperature	Tj		25	°C
Stor	age temperature range	T _{stg}	-55 to 125		°C
Ope	rating temperature range	Popr	-55 to 100		°C
Lead soldering temperature		T _{sol}	260 (10s)		°C
Total package power dissipation (1 circuit)		PT	250	150	mW
Tota	Il package power dissipation derating (Ta ≥ 25°C, ↑ circuit)	ΔP _T / °C	-2.5	-1.5	mW / °C
Isola	ation voltage (Note 1)	BVS	5000 (AC, 60	s, R.H.≤60%)	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered a two terminal device: LED side pins shorted together, and detector side pins shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	Vcc	_	5	24	V
Forward current	F	_	1.6	20	mA
Collector current	lc	_	1	10	mA
Operating temperature	Topr	-25		75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	VF	IF = 10mA	1.0	1.15	1.3	V
LED	Reverse current	IR	V _R = 5V	_	—	10	μA
	Capacitance	CT	V = 0 V, f = 1MHz	_ <	30	-	pF
	Collector-emitter breakdown voltage	V(BR)CEO	IC = 0.5mA	55	$\langle \rangle$	1	V
or	Emitter-collector breakdown voltage	V(BR)ECO	IE = 0.1mA	7	T	- (1	V
Detector	Collector dark current	1	V _{CE} = 24V	for	10	100	nA
		ICEO	V _{CE} = 24V, Ta = 85°C	, KA)2	50	μA
	Capacitance collector to emitter	C _{CE}	V=0 V, f=1MHz	X	12	_	pF

Coupled Electrical Characteristics (Ta = 25°C)

-	•		\searrow		λ	
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Current transfer ratio	IC / IF	IF = 1mA, V _{CE} = 0.5V Rank BV	100 200	Nr	1200 1200	%
Low input CTR	IC / IF (low)	I _F = 0.5mA, V _{CE} = 1.5V Rank BV	50 100	Ð	-	%
		IC = 0.5mA, IF = 1mA	(A/s)) —	0.4	
Collector-emitter saturation voltage	VCE (sat)	Ic = 1mA, I _F = 1mA		0.2	_	V
	G	Rank BV))-	—	0.4	

Coupled Electrical Characteristics (Ta = -25°C to 75°C)

Character	ristic	Symbol	Test Condi	tion	Min	Тур.	Max	Unit
Current transfer ratio		I _F = 1mA, V _{CE} = 0	.5V	50	—	_	%	
	current transfer ratio			Rank BV	100	-		70
		IF = 0.5mA, VCE =	1.5V	_	50	_	%	
Low input CTR	Low input CTR	IC / IF (low)		Rank BV	_	100	_	70

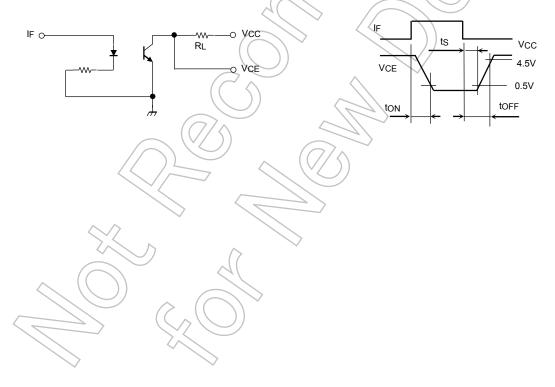
Isolation Characteristics (Ta = 25°C)

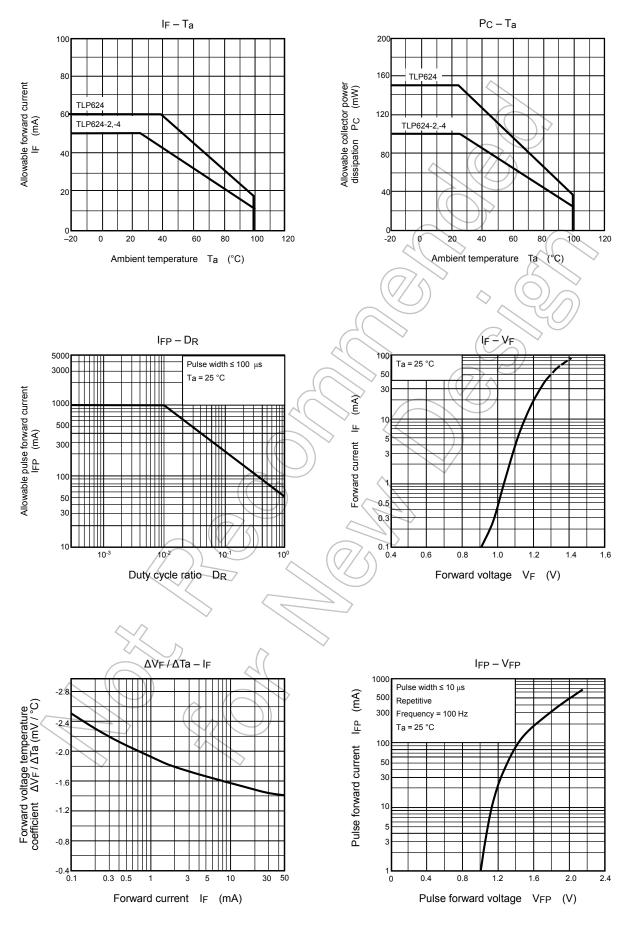
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1MHz	—	0.8	_	pF
Isolation resistance	Rs	V _S = 500 V, R.H. ≤ 60%	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage	BVs	AC, 60 s	5000 <	/	_	
		AC, 1 s, in oil	—	10000	-	Vrms
		DC, 60 s, in oil	—	10000	- ~(1	Vdc

Switching Characteristics (Ta = 25°C)

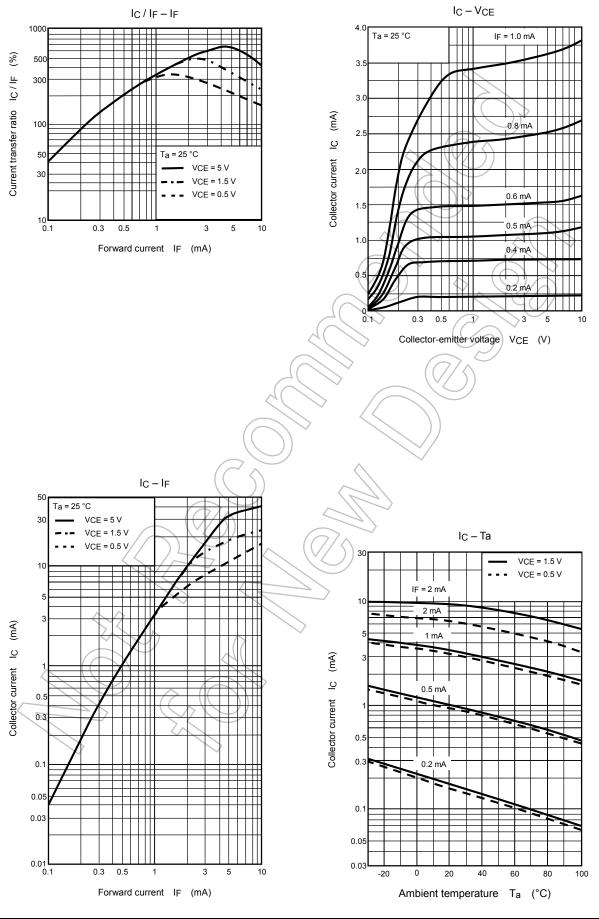
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	tr)	8	$\langle \langle$	
Fall time	t _f	$V_{CC} = 10V$, $I_C = 2mA$ $R_L = 100\Omega$	<u> </u>	8	ZF	
Turn-on time	t _{on}		> _	10	1	μS
Turn-off time	t _{off}		_◇	8	JA)
Turn-on time	ton		-	10	Ś	
Storage time	ts	$R_L = 4.7 \text{ k}\Omega \text{ (Fig.1)}$ $V_{CC} = 5 \text{ V, I}_F = 1.6 \text{mA}$	-((50	~ _	μS
Turn-off time	tOFF			300	_	

Fig. 1 Switching time test circuit

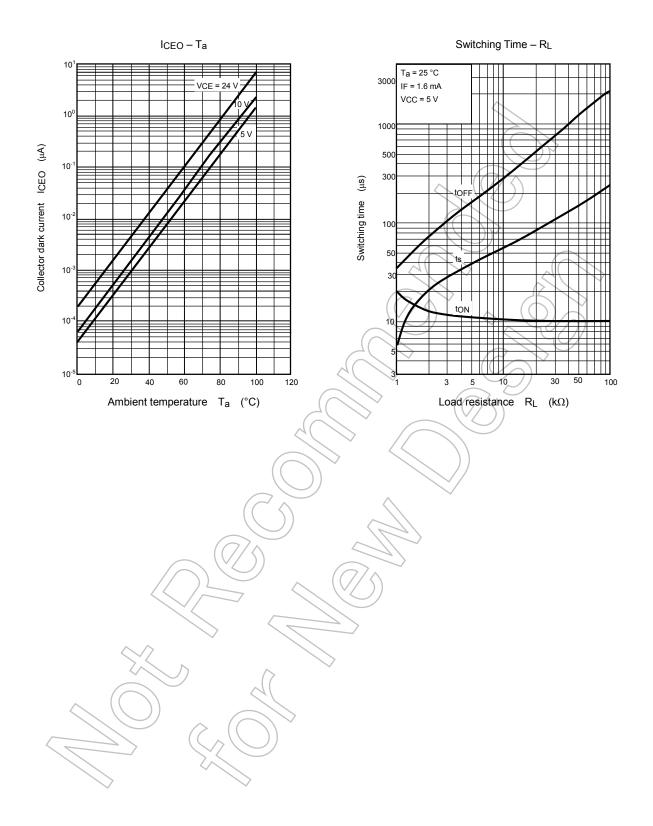




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