

**Display Elektronik GmbH**

**DATA SHEET**

**LCD MODULE**

**DEM 320240A TMH-PW-N  
(C-TOUCH)**

**3,5" TFT with Projective  
Capacitive Touch**

**Product Specification**

**Ver.: 2**

**18.08.2017**



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**1 General Description and Features**

DEM 320240A TMH-PW-N(C-TOUCH) is a TM (Transmissive) type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD module, a driver circuit, PCT and a back-light unit. The resolution of a 3.5" contains 320RGBx240 dots and can display up to 65K/262K colors. The following table described the features of DEM 320240A TMH-PW-N(C-TOUCH).

**1.1 Features**

- Transmissive and back-light with 6 LEDs are available.
- TN (Twisted Nematic) mode.
- 8 Bits i80 system interface
- Projected capacitive touch panel
- RoHS Compliance

**1.2 LCD Module**

Item	Specification	Unit
Screen Size	3.5 Inches	Diagonal
Display Resolution	320 x RGB x 240	Dot
Pixel Pitch	0.073 x 0.219	mm
Active Area	70.08 x 52.56	mm
Outline Dimension	76.9 x 63.9 x 5.23	mm
Display Mode	Normally White / Transmissive	--
Pixel Arrangement	RGB Side-Stripe	--
Surface Treatment	Glare, 7H	--
Display Color	65K/262K	--
Viewing Direction	6 o'clock (Gray Inversion)	--
TFT Driver	SSD2119Z7 or equivalent	--
Input Interface	8 Bits i80 System Interface.	--

**2 Mechanical Information**

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	76.6	76.9	77.2	mm	--
	Vertical (V)	63.6	63.9	64.2	mm	(1)
	Thickness (T)	4.93	5.23	5.53	mm	(1)
Weight		--	TBD	--	g	--

Note (1) Not include FPC.

Refer to the Dimensional Outlines for further information.

**3 Electrical Specifications**

**3.1 Absolute Max. Ratings**

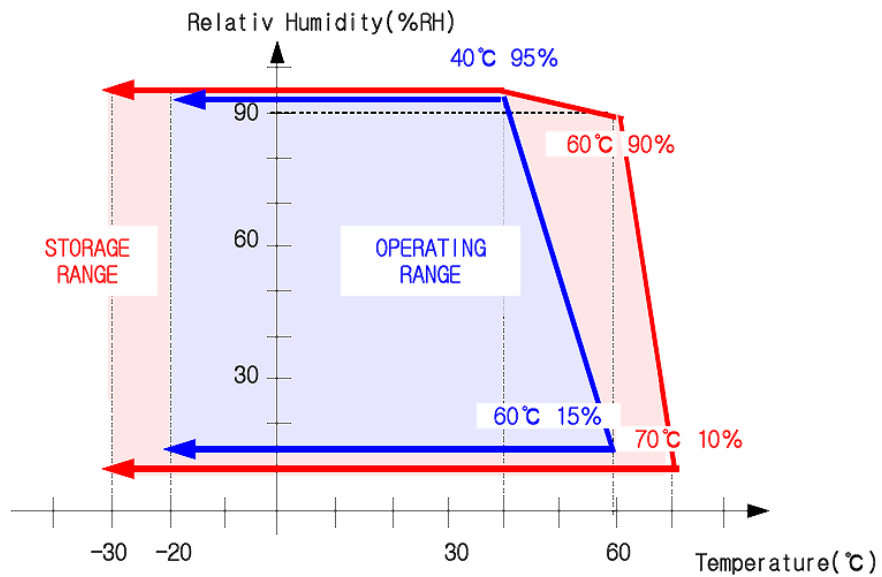
3.1.1 Absolute Ratings of Environment

If the operating condition exceeds the following absolute maximum ratings, the TFT LCD module may be damaged permanently.

(Ta=+25±2°C, Vss=GND=0)

Item	Symbol	Min.	Max.	Unit	Note
Storage Temperature	T <sub>STG</sub>	-30	+80	°C	(1)
Operating Temperature	T <sub>OPR</sub>	-20	+70	°C	(1,2,3)

Note (1) 95 % RH Max. (+40°C ≥ Ta). Maximum wet-bulb temperature at +39°C or less. (Ta > +40°C) No condensation.



Note (2) In case of below 0°, the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of LC's character

Note (3) Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at +25°C.

**3.2 Electrical Absolute Rating**

## 3.2.1 TFT-LCD Module

(Ta=+25±2°C, V<sub>SS</sub>=GND=0)

Item	Symbol	Value		Unit	Condition
		Min.	Max.		
Logic Power Supply	V <sub>DDIO</sub>	-0.3	4.0	V	--
Logic Input Voltage	V <sub>CI</sub>	V <sub>SS</sub> -0.3	5.0	V	--
Current Drain Per Pin Excluding VDD and VSS	I	--	25	mA	--

Note: Temp. ≤ +60°C, 90% RH MAX.

Temp. &gt; +60°C, absolute humidity shall be less than 90% RH at +60°C

## 3.2.2 Back-Light Unit

(Ta=+25±2°C)

Item	Symbol	Min.	Max.	Unit	Note
Current	I <sub>f</sub>	--	30	mA	(1)

Note (1) Permanent damage to the device may occur if maximum values are exceeded or reverse voltage is loaded. Functional operation should be restricted to the conditions described under normal operating conditions.

**4 Electrical Characteristics**

**4.1 TFT-LCD Module**

(Ta=+25±2°C, V<sub>DDIO</sub> =3.3V)

Item	Symbol	Value			Unit	Condition
		Min.	Typ.	Max.		
Power Supply of IO Pins	V <sub>DDIO</sub>	1.4	--	3.6	V	
Booster Reference Supply Voltage Range	V <sub>CI</sub>	2.5 or V <sub>DDIO</sub>	--	3.6	V	

**4.2 Backlight Unit**

The backlight system is an edge-lighting type with six white LED (Light Emitting Diode)s.

(Ta=+25±2°C)

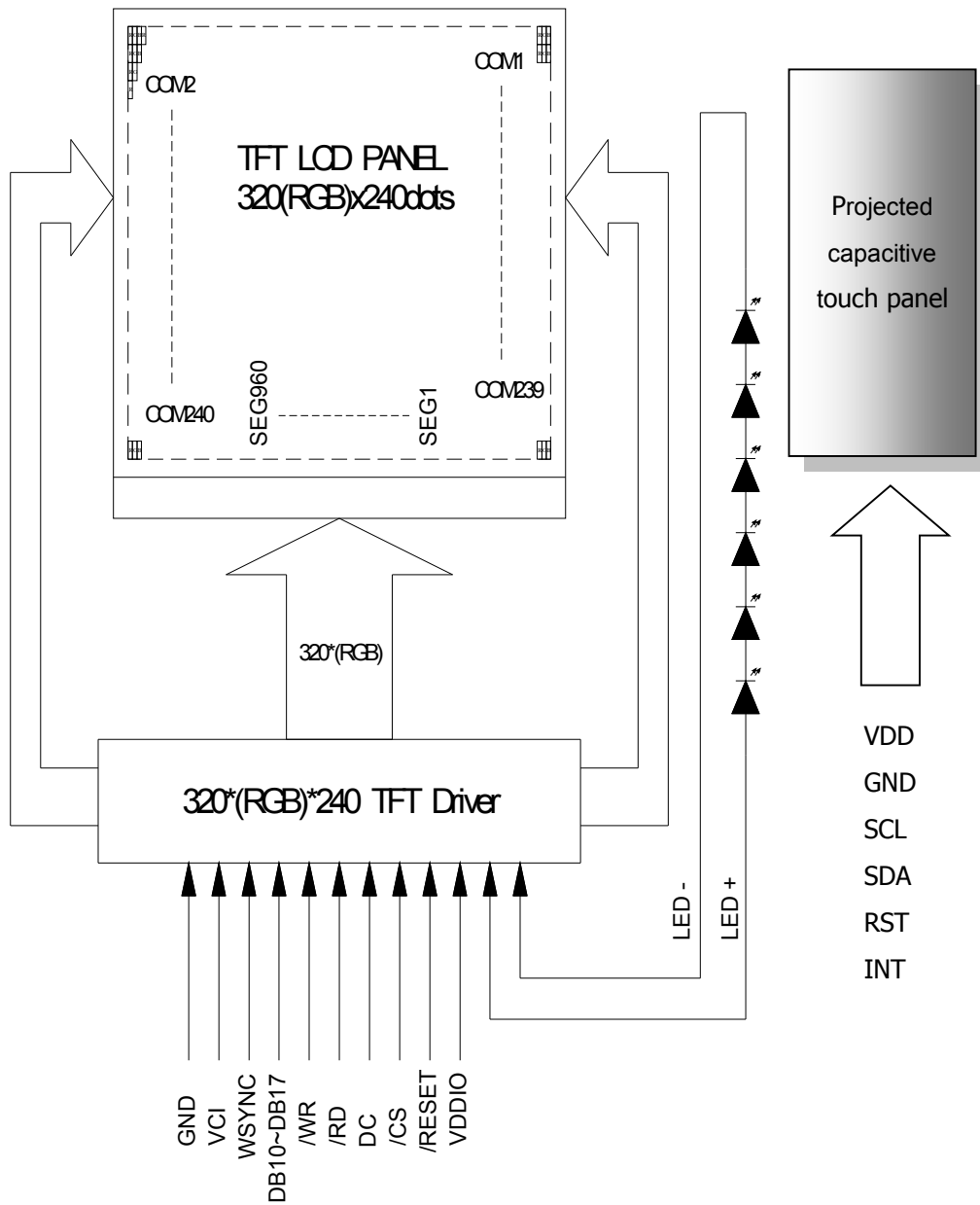
Item	Symbol	Value			Unit	Condition
		Min.	Typ.	Max.		
LED Voltage	VL	-	(19.2)	-	V	
LED Current	If	-	(20)	-	mA	
Power Consumption	P <sub>LED</sub>	-	(384)	-	mW	
LED Lifetime (+25°C)	-	(50000)	-	-	hr	

Note (1) 6 LEDs serial type.

(2) Where If = 20mA, P<sub>LED</sub> = V<sub>F</sub> × I<sub>B</sub>

5 Block Diagram

5.1 Interface System Structure with Backlight Unit





## 6 Input Terminal Pin Assignment

### 6.1 CN1 Pin Assignment (LCD)

Pin No.	Symbol	I/O	Function
1	LED_A	P	Ground
2	LED_K	P	Ground
3	V <sub>DDIO</sub>	P	Logic power supply(+3.0~3.6V)
4	NC	I/O	Not Connection
5~12	NC	I/O	Not Connection
13	NC	I/O	Not Connection
14~21	DB10~DB17	I/O	Data bus
22	WSYNC	O	Ram Write Synchronization output
23	GND	P	Ground
24	/CS	I	Chip select pin for 8080 Parallel Interface. Low: chip can be accessed; High: chip cannot be accessed.
25	/WR	I	Indicates read cycle when High, write cycle when Low)
26	/RD	I	Enable signal
27	DC	I	Parallel Interface
28	/RESET	P	Hardware Reset
29	V <sub>DDIO</sub>	P	Logic power supply(+3.0~3.6V)
30	VCI	P	Booster input voltage pin. - Connect to voltage source between 2.5V to 3.6V
31	NC	I	Not Connection
32	NC	I	Not Connection
33	NC	I	Not Connection
34	NC	I	Not Connection
35	GND	P	Ground
36	GND	P	Ground

7 Optical Characteristics

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (a).  
 Measuring equipment: BM-5A, BM-7

(Ta=+25±2°C)

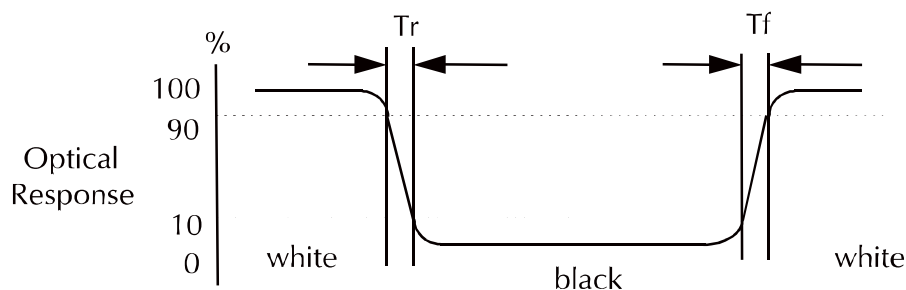
Item	Symbol	Condition	Min	Type	Max	Unit	Note
Brightness	--	--	(200)	(250)	--	cd/m <sup>2</sup>	(a),(b)
Response Time	T <sub>R</sub>	θ=0°	--	(8)	(12)	ms	(a),(b)
	T <sub>F</sub>		--	(17)	(23)	ms	
Contrast Ratio	CR	At optimized viewing angle	(320)	(400)	--	--	(a)
Color Chromaticity	Red	R <sub>X</sub>	(0.627)	(0.647)	(0.667)	--	(a)
		R <sub>Y</sub>	(0.316)	(0.336)	(0.356)		
	Green	G <sub>X</sub>	(0.29)	(0.31)	(0.33)	--	
		G <sub>Y</sub>	(0.556)	(0.576)	(0.596)		
	Blue	B <sub>X</sub>	(0.116)	(0.136)	(0.156)	--	
		B <sub>Y</sub>	(0.109)	(0.129)	(0.149)		
	White	W <sub>X</sub>	(0.287)	(0.307)	(0.327)	--	
		W <sub>Y</sub>	(0.335)	(0.355)	(0.375)		
Viewing Angle (6H) Gary Inversion	Hor.	θ <sub>R</sub>	50	(60)	--	Degree	(a)
		θ <sub>L</sub>	50	(60)	--		
	Ver.	φ <sub>H</sub>	40	(50)	--		
		φ <sub>L</sub>	50	(60)	--		

a. Test equipment setup

After stabilizing and leaving the panel alone shall be warmed up for the stable operation of LCM, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7(fast) with a viewing angle of 2° at a distance of 50cm and normal direction.

b. Definition of response time: Tr and Tf

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



c. Definition of contrast ratio:

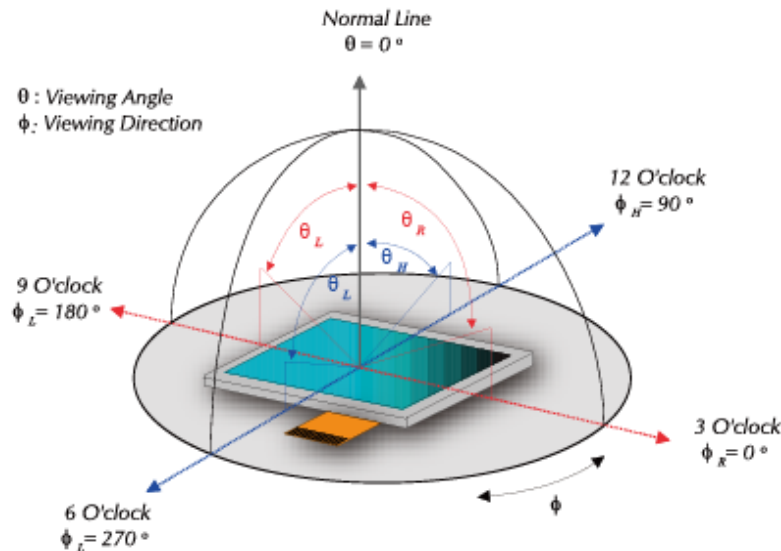
Brightness measured when LCD is at "white state"

$$\text{Contrast Ratio (CR)} = \frac{\text{Brightness measured when LCD is at "white state"}}{\text{Brightness measured when LCD is at "black state"}}$$

Brightness measured when LCD is at "black state"

d. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

e. View Angle



f. Definition of Luminance of White: Luminance of white at the center points

Light Source of Back-Light Unit	LED Type
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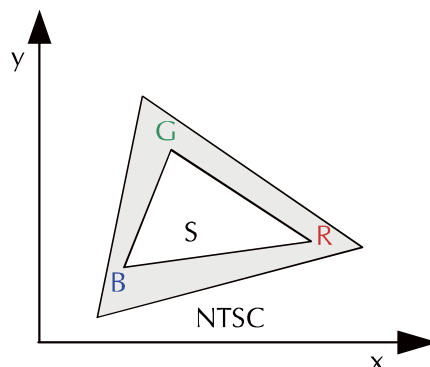
g. Definition of White Uniformity

$$\text{White Uniformity} = \frac{\text{Min. luminance of white among 9-points}}{\text{Max. luminance of white among 9-points}}$$

h. The definition of Color Gamut -Color Chromaticity CIE 1931

Color coordinate of white & red, green, blue at center point.

$$\text{Color Gamut: NTSC (\%)} = (\text{RGB Triangle Area} / \text{NTSC Triangle Area}) \times 100$$



8 Basic Display Color and Gray Scale

8.1 Mapping for writing an Instruction

		Hardware pins																	
Interface	Cycle	D17	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
18 bits		IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8	x	IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0	x
16 bits		IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8		IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0	
9 bits	1 <sup>st</sup>	IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8	x									
	2 <sup>nd</sup>	IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0	x									
8 bits	1 <sup>st</sup>	IB15	IB14	IB13	IB12	IB11	IB10	IB9	IB8										
	2 <sup>nd</sup>	IB7	IB6	IB5	IB4	IB3	IB2	IB1	IB0										

Remark : x Don't care bits  
 Not connected pins

8.2 Mapping for writing an Pixel Data

			Hardware pins																	
Interface	Color mode	Cycle	D17	D16	D15	D14	D13	D12	D11	D10	D9	D8	D7	D6	D5	D4	D3	D2	D1	D0
18 bits	262k		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
16 bits	262k	1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	x	x		G5	G4	G3	G2	G1	G0	x	x	
		2 <sup>nd</sup>	B5	B4	B3	B2	B1	B0	x	x		R5	R4	R3	R2	R1	R0	x	x	
		3 <sup>rd</sup>	G5	G4	G3	G2	G1	G0	x	x		B5	B4	B3	B2	B1	B0	x	x	
		1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	x	x		G5	G4	G3	G2	G1	G0	x	x	
		2 <sup>nd</sup>	x	x	x	x	x	x	x	x		B5	B4	B3	B2	B1	B0	x	x	
		2 <sup>nd</sup>	R5	R4	R3	R2	R1	R0	x	x		G5	G4	G3	G2	G1	G0	x	x	
	65k		R4	R3	R2	R1	R0	G5	G4	G3		G2	G1	G0	B4	B3	B2	B1	B0	
9 bits	262k	1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	G5	G4	G3									
		2 <sup>nd</sup>	G2	G1	G0	B5	B4	B3	B2	B1	B0									
8 bits	262k	1 <sup>st</sup>	R5	R4	R3	R2	R1	R0	x	x										
		2 <sup>nd</sup>	G5	G4	G3	G2	G1	G0	x	x										
		3 <sup>rd</sup>	B5	B4	B3	B2	B1	B0	x	x										
	65k	1 <sup>st</sup>	R4	R3	R2	R1	R0	G5	G4	G3										
	2 <sup>nd</sup>	G2	G1	G0	B4	B3	B2	B1	B0											

Remark : x Don't care bits  
 Not connected pins

## 9 AC CHARACTERISTICS

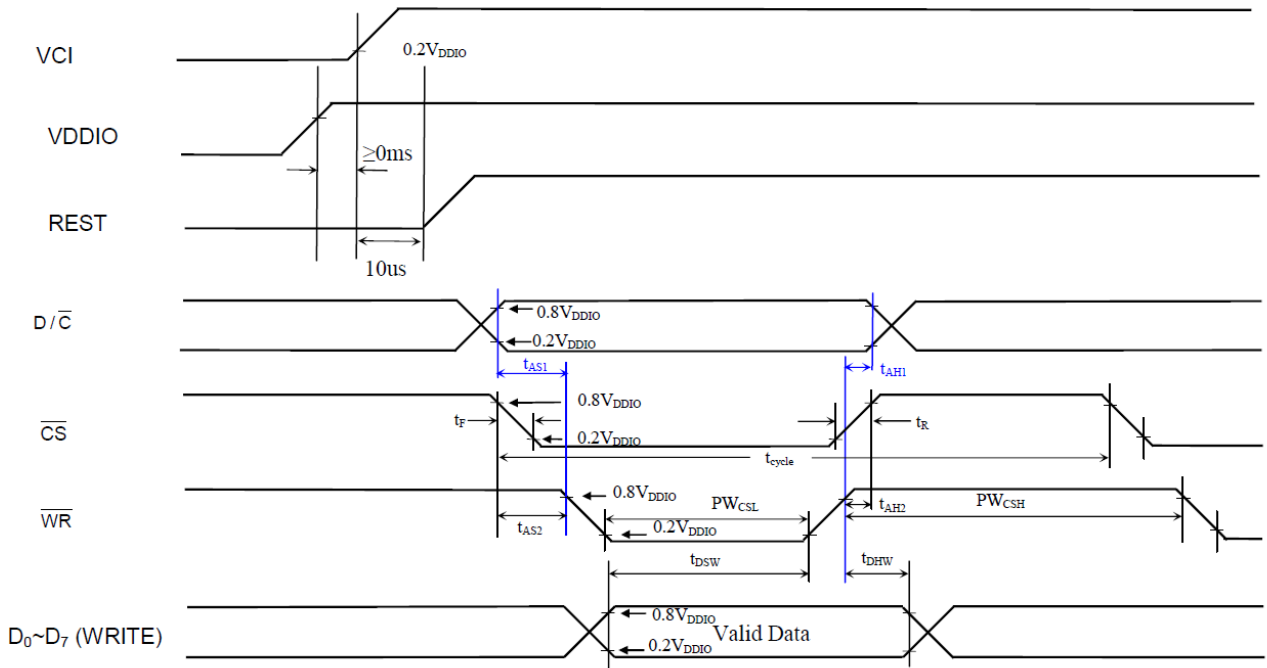
## 9.1 Parallel 8080 Timing Characteristics

(Ta=+25 ±2°C, V<sub>DDIO</sub> =3.3V)

Symbol	Parameter	Min	Typ	Max	Unit
t <sub>cycle</sub>	Clock Cycle Time (write cycle)	75	-	-	ns
t <sub>cycle</sub>	Clock Cycle Time (read cycle) (Based on VOL/VOH = 0.3*V <sub>DDIO</sub> /0.7*V <sub>DDIO</sub> )	450	-	-	ns
t <sub>AS1</sub>	Address Setup Time between (R/ $\overline{W}$ ) and D/ $\overline{C}$	0	-	-	ns
t <sub>AH1</sub>	Address Hold Time between (R/ $\overline{W}$ ) and D/ $\overline{C}$	0	-	-	ns
t <sub>AS2</sub>	Address Setup Time between (R/ $\overline{W}$ ) and $\overline{CS}$	0	-	-	ns
t <sub>AH2</sub>	Address Hold Time between (R/ $\overline{W}$ ) and $\overline{CS}$	0	-	-	ns
t <sub>DSW</sub>	Data Setup Time (D0~D7, WRITE)	5	-	-	ns
t <sub>DHW</sub>	Data Hold Time (D0~D7, WRITE))	5	-	-	ns
t <sub>ACC</sub>	Data Access Time (D0~D7, READ)	250	-	-	ns
t <sub>OH</sub>	Output Hold time (D0~D7, READ)	100	-	-	ns
PW <sub>CSL</sub>	Pulse width /CS low (write cycle)	40	-	-	ns
PW <sub>CSH</sub>	Pulse width /CS high (write cycle)	25	-	-	ns
PW <sub>CSL</sub>	Pulse width /CS low (read cycle)	500	-	-	ns
PW <sub>CSH</sub>	Pulse width /CS high (read cycle)	500	-	-	ns
t <sub>R</sub>	Rise time	-	-	4	ns
t <sub>F</sub>	Fall time	-	-	4	ns

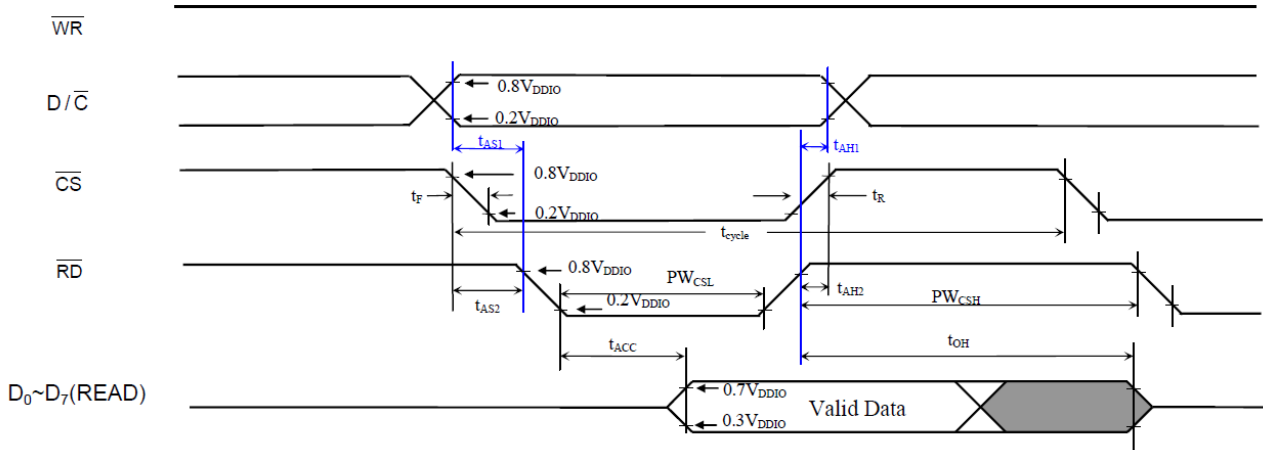
Note: CS can be pulled low during the write cycle, only /RW is needed to be toggled

Write Cycle



Remark: It's highly recommended that  $\overline{RD}$  remains high for the whole write cycle

Read Cycle



**10 Projected capacitive touch Screen Panel Specifications****10.1 Information**

Item	Specification	Unit
Screen Size	3.5 Inches	Diagonal
Type	Transparent Type Projected Capacitive Touch Panel	--
Input Mode	Human's Finger	--
Sensor Active Area	71.68 (W) × 54.16 (H)	mm
Interface	I2C	--
Cover Glass Pencil-Hardness	6H(min) by JIS K5400	--
Digital Power Supply	2.8 ~ 3.3	V
Power Consumption	TBD	mA
IC Solution	IC : ST1624	

**10.2 Pin Assignments and Definitions** (Connector Part No: "FH34SJ-6S-1.0SH" or equivalent.)

Item	Name	I/O	Unit
1	VDD	P	Power
2	GND	P	Ground
3	SCL	I	I2C Clock
4	SDA	I/O	I2C Data
5	INT	I	Interrupt request to the host
6	RST	I	External Reset, active low

**10.3 Electrical Characteristic**

## 10.3.1 DC Electrical Characteristics

Condition: VDD = IOVDD = 3.3V, TA = +25°C, unless be specified individually.

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
VDD	V <sub>VDD</sub>	2.7	-	3.6	V	
IOVDD	V <sub>IOVDD</sub>	1.6	-	3.6	V	
Operating Current	I <sub>NML</sub>	-	16	24	mA	15TX, 9RX
Idle Current	I <sub>IDLE</sub>	-	5.9	8.9	mA	15TX, 9RX, scan rate=20Hz
Power Down Current	I <sub>PD</sub>	-	-	20	uA	
Input High Voltage	V <sub>IH</sub>	0.85* IOVDD	-	-	V	IOVDD=3.3V
Input Low Voltage	V <sub>IL</sub>	-	-	0.15* IOVDD	V	IOVDD=3.3V
Input Pull Up Resistor	R <sub>PU</sub>	50	-	60	KOhm	
Output Driving Current	I <sub>DRV</sub>	6	-	-	mA	V <sub>OH</sub> = IOVDD x 0.8
Output Sinking Current	I <sub>SINK</sub>	10	-	-	mA	V <sub>OL</sub> = IOVDD x 0.2
Low Voltage Reset	V <sub>LVR</sub>	-	-	2.3	V	

10.3.2 AC Electrical Characteristics

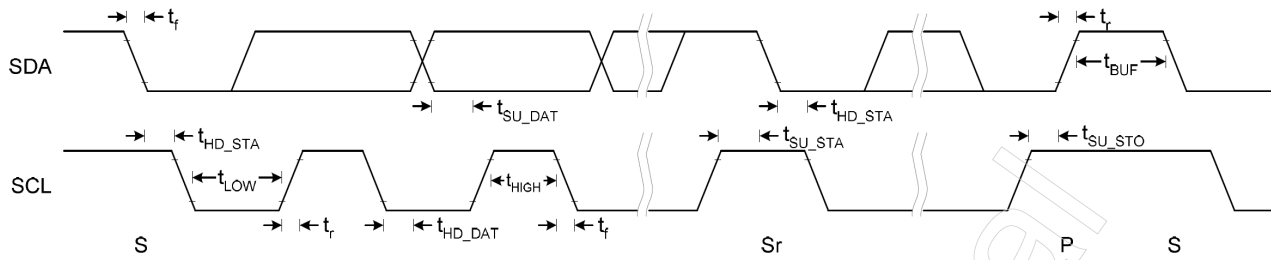


Figure 10-1 I2C Fast Mode Timing

I2C Fast Mode Timing Characteristic

Conditions: VDD = 3.3V, GND = 0V, TA = +25°C

Symbol	Parameter	Rating			Unit
		Min.	Typ.	Max.	
$f_{SCL}$	SCL clock frequency	0	-	400	kHz
$t_{LOW}$	Low period of the SCL clock	1.3	-	-	us
$t_{HIGH}$	High period of the SCL clock	0.6	-	-	us
$t_f$	Signal falling time	-	-	300	ns
$t_r$	Signal rising time	-	-	300	ns
$t_{SU\_STA}$	Set up time for a repeated START condition	0.6	-	-	us
$t_{HD\_STA}$	Hold time (repeated) START condition. After this period, the first clock pulse is generated	0.6	-	-	us
$t_{SU\_DAT}$	Data set up time	100	-	-	ns
$t_{HD\_DAT}$	Data hold time	0	-	0.9	us
$t_{SU\_STO}$	Set up time for STOP condition	0.6	-	-	us
$t_{BUF}$	Bus free time between a STOP and START condition	1.3	-	-	us
$C_b$	Capacitive load for each bus line	-	-	400	pF

10.4 SYSTEM MANAGEMENT

10.4.1 Power Down

In power down mode, all of the clocks of ST1624 are stopped. The way to exit power down mode is by a hardware reset or I2C.

10.4.2 Reset

Master can reset ST1624 through RESET pin. RESET pin is low active and needs hold low for 1us to take effect.

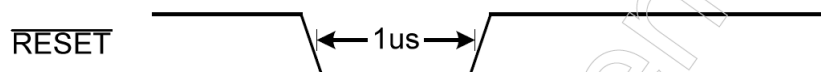


Figure 10-2 RESET Pin Low Pulse Width



10.4.3 Power On/Off Sequence

RESET pin should be held low before power on and power off. During power on, after both VDD and IOVDD reach normal voltage, RESET pin needs to be held low for 5ms to ensure internal block stable.

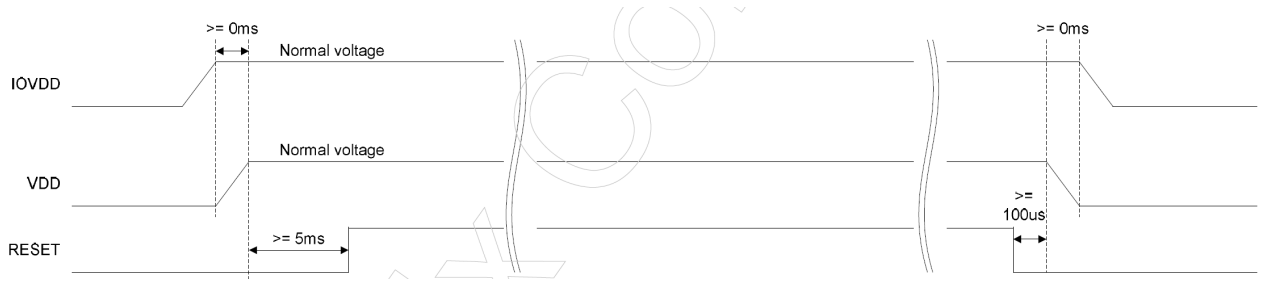


Figure 10-3 Power On/Off Sequence

**11 TEST**

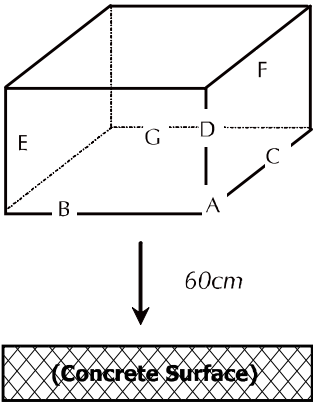
No change on display and in operation under the following test condition.

Condition: Unless otherwise specified, tests will be conducted under the following condition.

Temperature: +20±5°C.

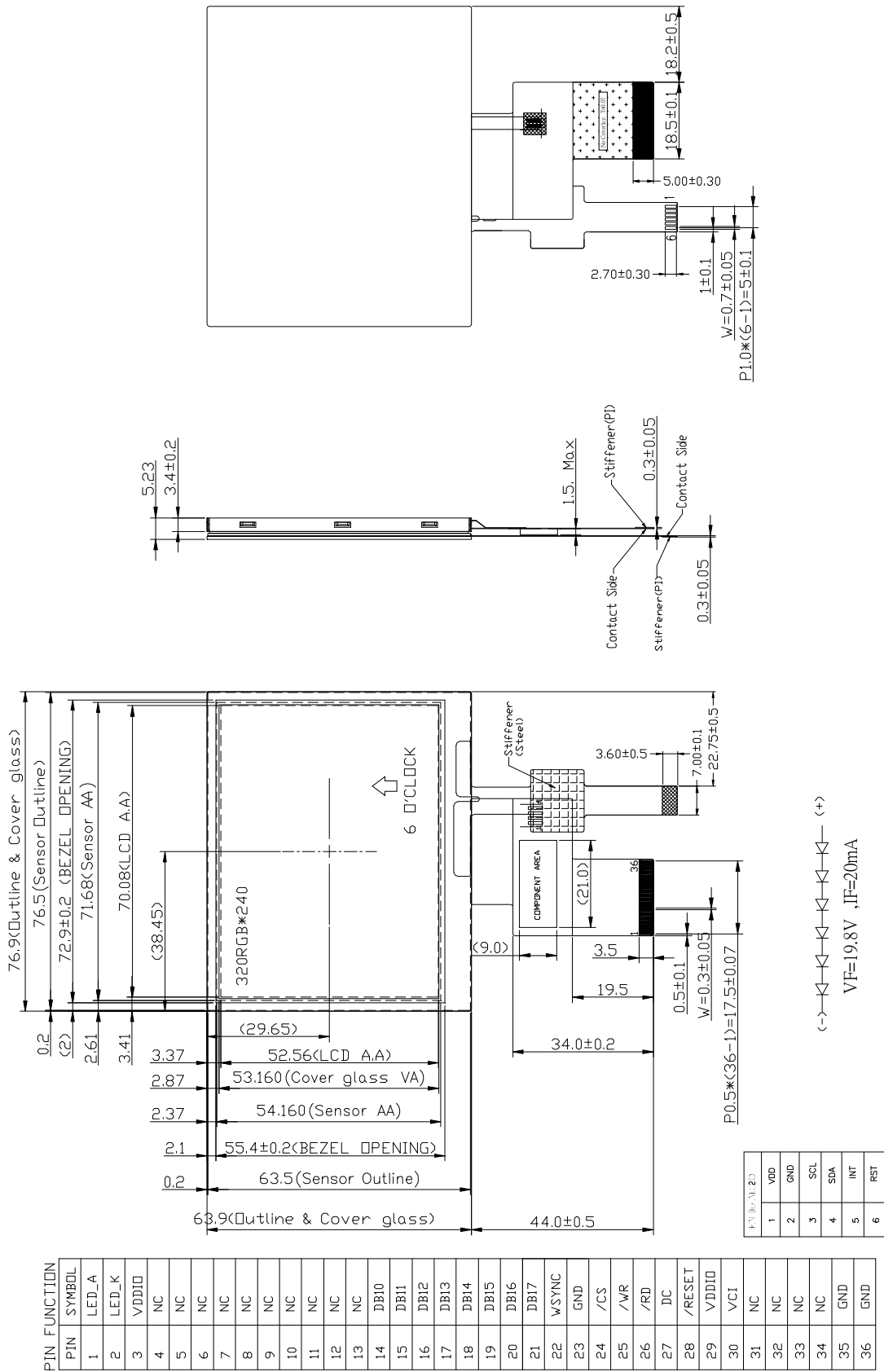
Humidity: 65±5%RH.

Tests will be not conducted under functioning state.

No.	Parameter	Condition	Notes
1	High Temperature Operating	+70°C±2°C, 240hrs (Operation state).	
2	Low Temperature Operating	-20°C±2°C, 240hrs (Operation state).	1
3	High Temperature Storage	+80°C±2°C, 240hrs.	2
4	Low Temperature Storage	-30°C±2°C, 240hrs.	1,2
5	High Temperature and High Humidity Operation Test	+60°C±2°C, 90%, 240hrs	1,2
6	Vibration Test	Total fixed amplitude: 1.5mm. Vibration Frequency: 10~55Hz. One cycle 60 seconds to 3 direction of X, Y, Z each 15 minutes.	3
7	Thermal Shock Test (non-operating)	-30°C ( 30min ) ~ +85°C ( 30min ) ,10 cycles	
8	Drop Test	<p>To be measured after dropping from 60cm high on the concrete surface in packing state.</p>  <p><i>Dropping method corner dropping:</i></p> <p><i>A corner: Once edge dropping.</i></p> <p><i>B, C, D edge: Once face dropping.</i></p> <p><i>E, F, G face: Once.</i></p>	

- Notes:
1. No dew condensation to be observed.
  2. The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.
  3. Vibration test will be conducted to the product itself without putting I in a container.

12 Dimensional Outlines

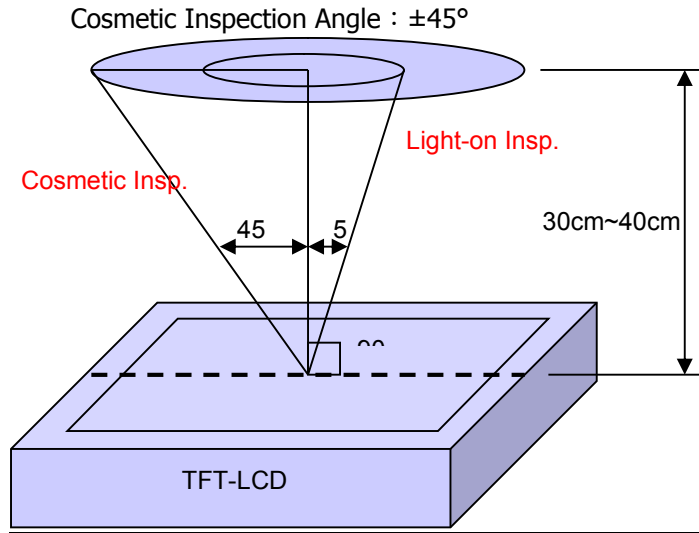


**13 Incoming Inspection Standards**

13.1 Inspection and Environment Conditions

13.1.1 Inspection Conditions:

- (1) Inspection Distance: 35 cm±5cm
- (2) View Angle: Light-on Inspection Angle : ±5°



(perpendicular to LCD panel surface)

13.1.2 Environment Conditions:

Ambient Temperature		+23°C±5°C
Ambient Humidity		55±10%RH
Ambient Illumination	Cosmetic Inspection	more than 600 Lux
	Functional Inspection	300~500 Lux

13.1.3 Sampling Conditions:

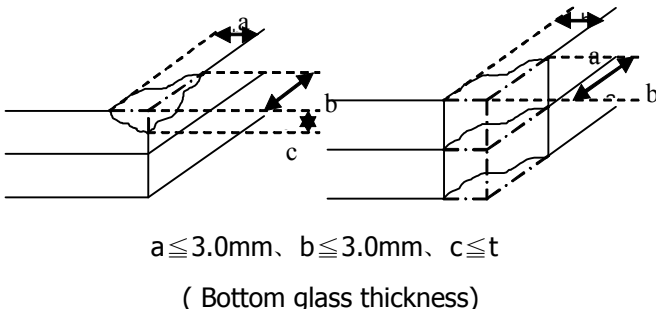
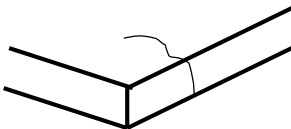
- (1) Lot Size: Quantity of shipment lot per model
- (2) Sampling Method:

Sampling Plan		MIL-STD-105E
		Normal Inspection, Single Sampling
		Level II
AQL	Major Defect	1.0%
	Minor Defect	1.5%

- (3) The classification of Major (MA) and Minor (MI) defects is shown as 3. Inspection Criteria.

13.1.4 Inspection Criteria

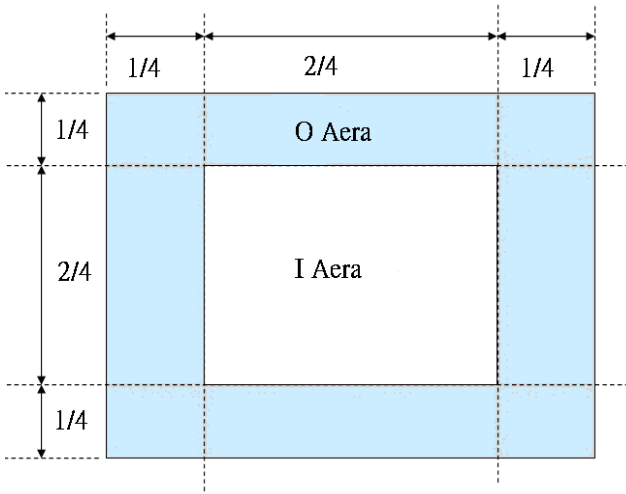
13.1.4.1 Cosmetic Inspection (Panel):

Item	Judgment Criteria	Classification
Chipping on Panel	 <p><math>a \leq 3.0\text{mm}</math>, <math>b \leq 3.0\text{mm}</math>, <math>c \leq t</math> ( Bottom glass thickness)</p>	MA
Scratch on Panel *Note-2	<p><math>W \leq 0.05\text{mm}</math> or <math>L &lt; 5\text{mm}</math>: Ignored  <math>0.05\text{mm} &lt; W \leq 0.1\text{mm}</math> and <math>L \leq 5\text{mm}</math>: <math>N \leq 5</math>  <math>W &gt; 0.1\text{mm}</math> or <math>L &gt; 5\text{mm}</math>: Not allowed</p>	MI
Bubble or Dent on Panel *Note-3	<p><math>D \leq 0.2\text{mm}</math>: Ignored  <math>0.2\text{mm} &lt; D \leq 0.3\text{mm}</math>: <math>N \leq 5</math>  <math>D &gt; 0.3\text{mm}</math>: Not allowed</p>	MI
Panel Crack	 <p>Not Allowed crack</p>	MA
Bezel Deformation	Obvious deformation is not allowed.	MI
Bezel Oxidation	Not allowed if it rusts continuously over 1 cm (It is out of warranty with rusted tin plate)	MI
Bezel Scratch	$L \leq 20\text{mm}$ , $W \leq 0.2$ , $N \leq 3$	MI
Metal Squash Dent /Flange(Front Side)	$D(W) \leq 1, L \leq 3, N \leq 3;$	MI
B/L High Voltage Wire Denudation	Not allowed	MA
Polarizer flaw or leak out resin	Defect is defined as the active area.	MI
Outline Dimension	Must in Spec, refer to related product spec.	MI

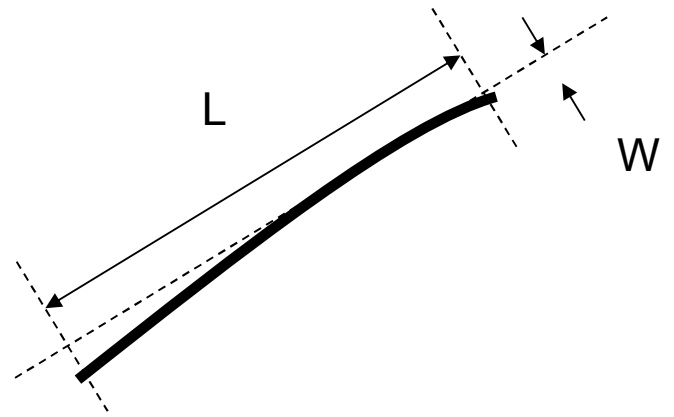
13.1.4.2 Functional Inspection:

Item	Judgment Criteria			Classification	
	Area(Note1)	I	O		
Point Defect	Bright dot	Random	2		MI
		2 dots adjacent	0	0	
		3 dots adjacent or more	0	0	
	Dark dot	Random	3		
		2 dots adjacent	0		
		3 dots adjacent or more	0	0	
	Total Dot Defect		5		
	Distance	Distance between Bright and Bright dot	$L \geq 5\text{mm}$		
		Distance between Bright and Dark dot	$L \geq 5\text{mm}$		
		Distance between Dark dot	$L \geq 5\text{mm}$		
(1) It is defined as Point Defect if defect area > 0.5dot (2) It is ignored if defect area $\leq 0.5\text{dot}$ (3) Weak point defect will be defined as Bright Dot if it can be observed through ND filter 5%( Full Screen Black Inspection)					
Line Defect	Obvious vertical or horizontal line defect is not allowed.			MA	
Mura	Not allowed if it can be observed through ND Filter 5 %			MI	
Foreign Material in spot shape *Note-3	$D \leq 0.2\text{mm}$ : Ignored $0.2\text{mm} < D \leq 0.5\text{mm}$ : $N \leq 8$ $D > 0.5\text{mm}$ : Not allowed			MI	
Foreign Material in line or spiral shape *Note-4	$W \leq 0.05\text{mm}$ or $L \leq 5\text{mm}$ : Ignored $0.05\text{mm} < W \leq 0.2\text{mm}$ and $L 1.0\text{mm} \leq 5\text{mm}$ : $N \leq 8$ $W > 0.2\text{mm}$ or $L > 5\text{mm}$ : Not allowed			MI	
Display Function Abnormal	No Malfunction can be allowed			MA	

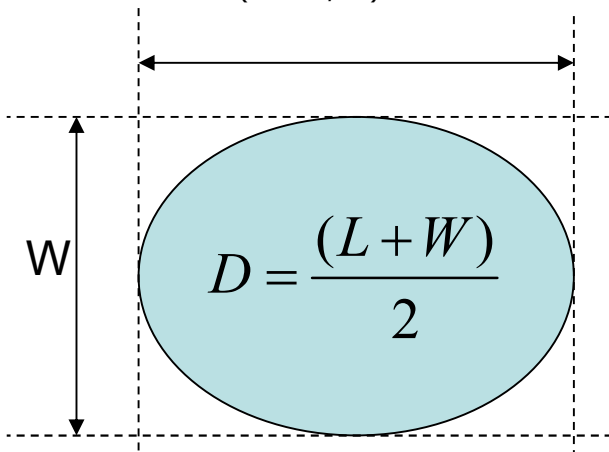
Note-1 : I/O Area Definition



Note-2 : Polarizer Scratch



Note-3 : Spot Foreign Material  
( $W \geq L / 4$ )



Note-4 : Line or Spiral Foreign Material  
( $W < L / 4$ )

