SPE	CIF	IC A	ATIC	SMC
JI L	VIII	$I \cup F$	7 I I C	IIIJ

CUSTOMER . PTC

SAMPLE CODE . NSC1602LRU-JWT-H

MASS PRODUCTION CODE . NPC1602LRU-JWT-H

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 001

DRAWING NO. (Ver.) · JLMD-NPC1602LRU-JWT-H _001

PACKAGING NO. (Ver.) : JPKG-NPC1602LRU-JWT-H _001

Customer Approved

Date:

Approved	Checked	Designer
閆偉	劉進	夏子豪

Preliminary specification for design input

■ Specification for sample approval

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Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
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Note: For detailed information please refer to IC data sheet: SITRONIX---ST7066U-0A



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Y/G , Positive , Transflective
Driver Condition	LCD Module : 1/16 Duty , 1/5 Bias
Viewing Direction	6 O'clock
Interface	6800-series 8-bit parallel
Driver IC	ST7066U
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Wite Chairman Specifications					
Item	Standard Value				
Outline Dimension	85.0 (L) * 30.0 (W) *12.7(H)				
Viewing Area	66.0 (L) * 16.0 (W)	mm			
Active Area	56.2 (L) * 11.5 (W)	mm			
Character Size	2.95mm * 5.55mm	mm			
Character Pitch	3.55mm * 5.95mm				

Note: For detailed information please refer to LCM drawing





1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	-	-0.3	7.0	٧
LCD Driver Supply Voltage	V_{LCD}	-	VDD -10.0	V _{DD} +0.3	٧
Input Voltage	V _{IN}	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	-	-20	70	$^{\circ}\mathbb{C}$
Storage Temperature	T _{ST}	-	-30	80	$^{\circ}\mathbb{C}$
Storage Humidity	H _D	Ta<60 ℃	-	90	%RH

1.4 DC Electrical Characteristics

Ta = 25°C

					. •	
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V_{DD}	-	4.5	5.0	5.5	V
"H" Input Voltage	VIH	A - 1	0.7 V _{DD}	-	V _{DD}	V
"L" Input Voltage	VIL	, - l -	-0.3	-	0.6	V
"H" Output Voltage	Vон	IOH=-0.1mA	3.9	-	VDD	V
"L" Output Voltage	Vol	IOL=0.1mA	-	-	0.4	V
Supply Current	I _{DD}	V _{DD} = 5.0 V ,Vop= 4.5V *1	-	2.0	3.0	mA
	Vop	-20 ℃	4.4	4.6	4.8	
LCM Driver Voltage		25℃	4.3	4.5	4.7	V
	*2	70℃	4.1	4.3	4.5	

NOTE: *1 The Maximum current display

*2 The VOP test point is (VDD –V0)



1.5 Optical Characteristics

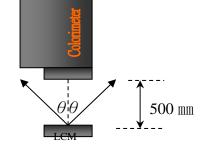
LCD Panel : 1/16 Duty , 1/5 Bias , V_{LCD} = 4.5V , Ta =25°C

Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Posponso Timo	Rise	tr		-	80	125		Note 2
Response Time	Fall	tf	-	-	220	330	ms	Note 2
	Тор	θ+		-	40	-		
Viewing angle	Bottom	θ-	C>2.0	-	40	-	Deg	Note 1
range	Left	θL	C <u>~</u> 2.0	-	45	-		
	Right	θR		-	45	-		
Contrast Ra	tio	С	-	-	10	-	-	Note 3
	Average Brightness (with LCD) *2		IF=100 mA	30	35	1	cd/m ²	
Wavelength (with LCD)		λр	IF-100 IIIA	568	571	574	nm	Note 4
Uniformity *	*1	∆B	IF=100 mA	70	-	_	%	

Note 4

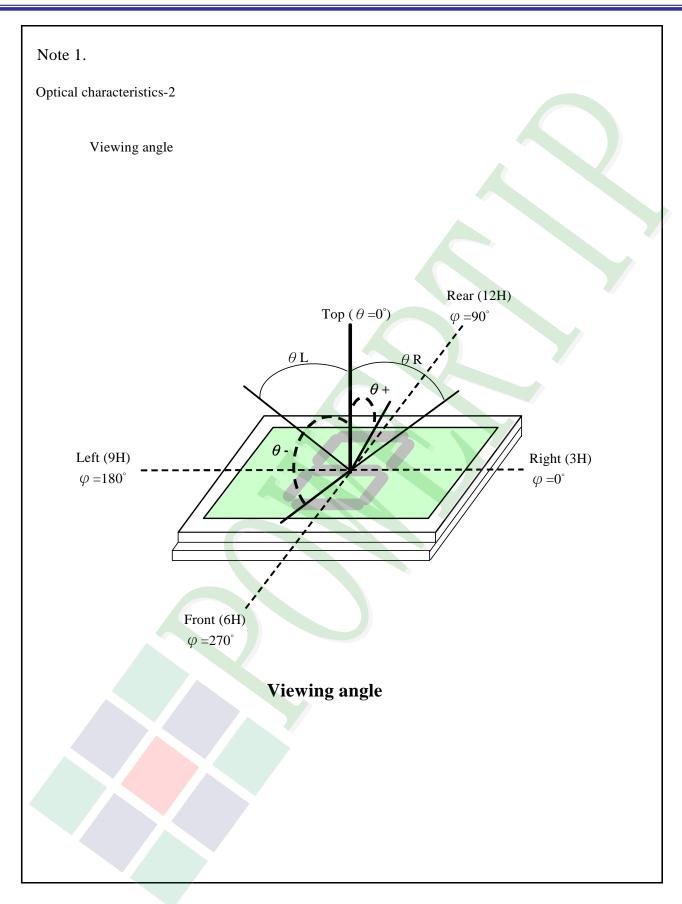
- 1 : △B=B(min) / B(max) * 100%
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H → no wind → dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: $500 \pm 50 \text{ mm}$, $(\theta = 0^{\circ})$
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%



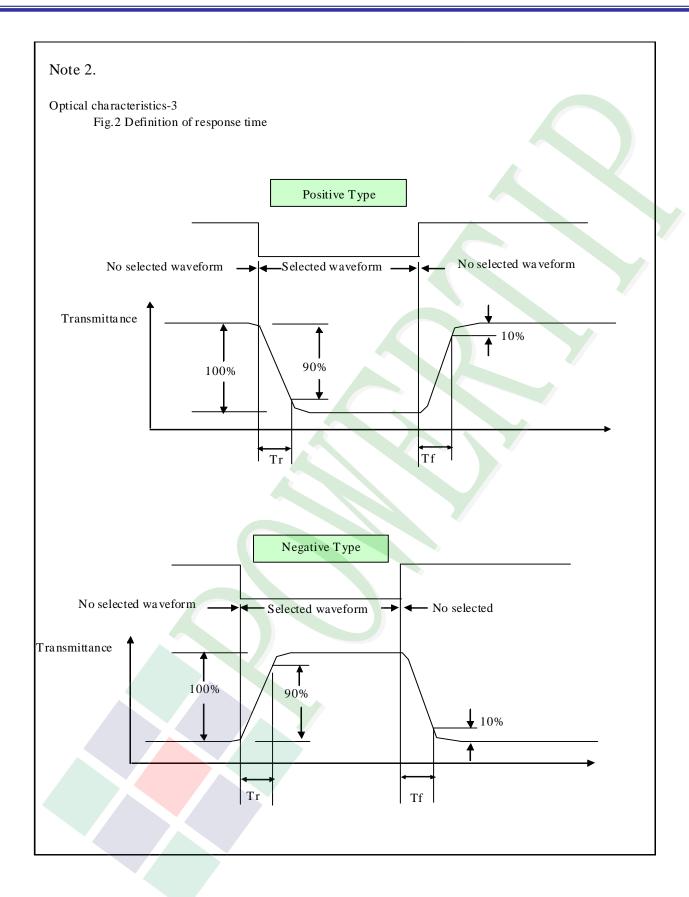


Colorimeter=BM-7 fast











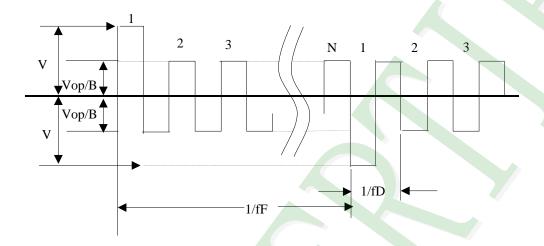
Electrical characteristics-2

[™] 2 Drive waveform

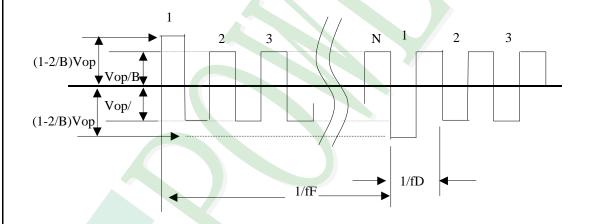
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



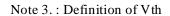
(2) Non- Selected wave form



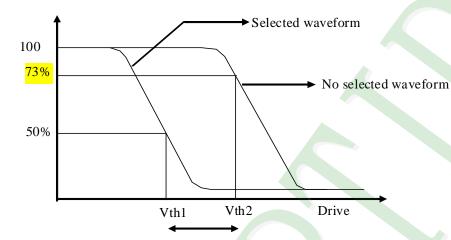
Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period





Transmittance



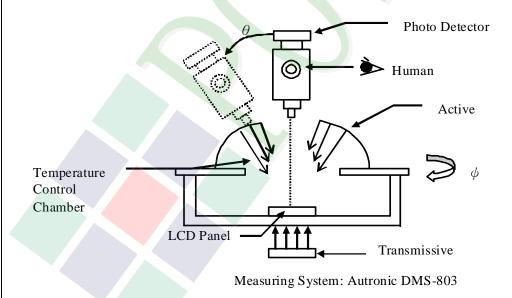
Active voltage range

	Vth1	Vth2
View direction	10°	$40\degree$
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio

= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System





1.6 Backlight Characteristics

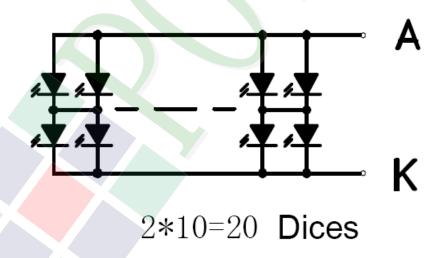
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°ℂ	-	150	mA
Reverse Voltage	VR	Ta =25°ℂ	-	8	V
Power Dissipation	PD	Ta =25°ℂ	- (660	mW

Electrical / Optical Characteristics

	Electrical / e prical characteristics					
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		4.0	4.2	4.4	V
Reverse Current	IR		-	-	100	uA
Average Brightness (without LCD)	IV	IF= 100 mA	165	190	-	cd/m ²
Wavelength (Without LCD)	λр		569	572	575	nm
Color	Yellow/Green					

Internal Circuit Diagram:





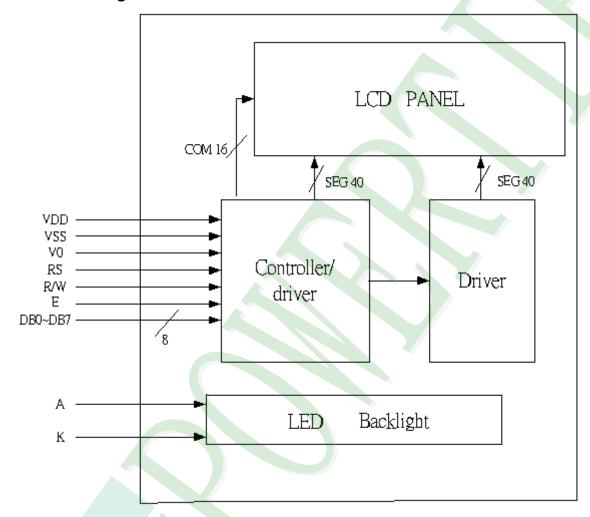
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



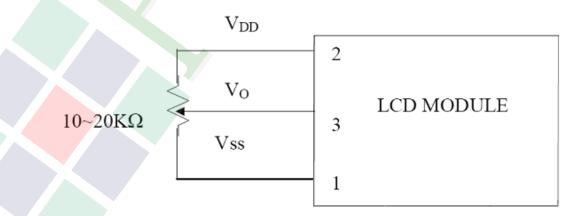


2.2 Interface Pin Description

Pin No.	Symbol	Signal Description			
1	Vss	Power Supply (Vss=0)			
2	V _{DD}	Power Supply (5V)			
3	Vo	Operating voltage for LCD			
		Register Selection input			
4	RS	High = Data register			
4	K5	Low = Instruction register (for write)			
		Busy flag address counter (for read)			
5	D///	Read/Write signal input is used to select the read/write mode			
5	R/W	High = Read mode, Low = Write mode			
6	E	Start enable signal to read or write the data			
7	DB0				
8	DB1	Four low order bi-directional three-state data bus lines. Use for			
9	DB2	data transfer between the MPU and the LCD module.			
10	DB3	These four are not used during 4-bit operation.			
11	DB4				
12	DB5	Four high order bi-directional three-state data bus lines. Used			
13	DB6	for data transfer between the MPU and the LCD module.			
14	DB7	-DB7 can be used as a busy flag.			

2.2.1 Application Notes

Contrast Adjust





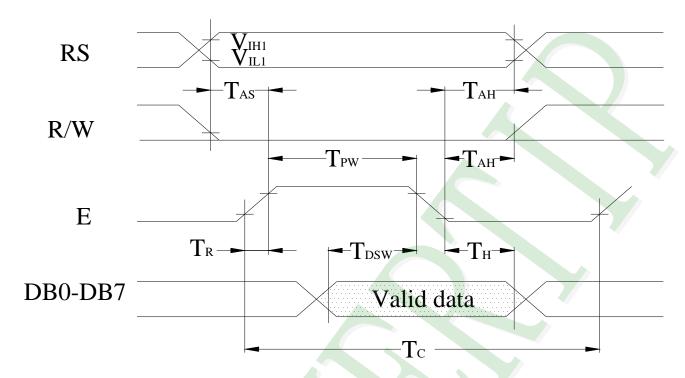
2.2.2 Refer Initial code

```
void initial()
{
    delay(40);
    write_com(0x01);
    delay(5);
    write_com(0x38);
    delay(5);
    write_com(0x0c);
    delay(5);
    write_com(0x06);
    delay(5);
}
```

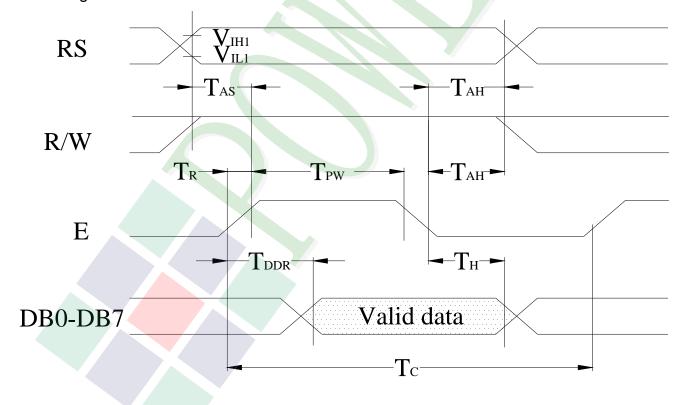


2.3 Timing Characteristics

• Writing data from MPU to ST7066U



• Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

 $(VDD = 5V,Ta=25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	ı	-	ns
Tpw	Enable Pulse Width	Pin E	140	-		ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
Tas	Address Setup Time	Pins: RS , RW,E	0	- (-	ns
Тан	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DSW}	Data Setup Time	Pins:DB0~DB7	40		-	ns
Тн	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

• Read Mode (Reading data from ST7066U to MPU)

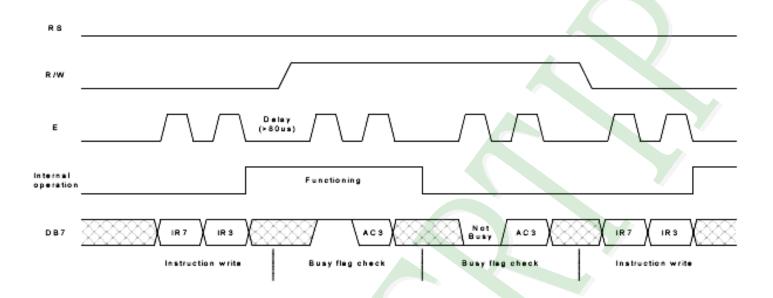
 $(VDD = 5V,Ta=25^{\circ}C)$

						•
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	1	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	_	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
Tas	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
Тан	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
T _H	Data Hold Time	Pins:DB0~DB7	10	-	-	ns



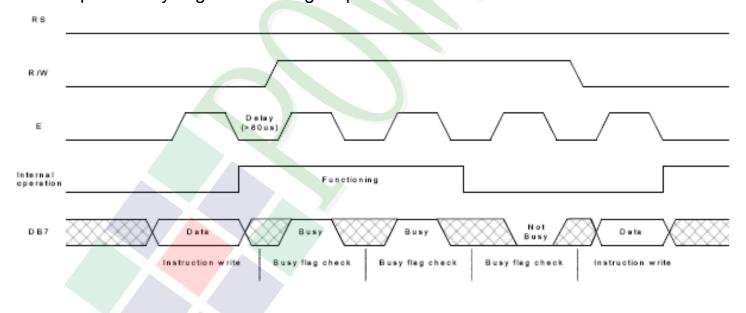
For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used

Example of busy flag check timing sequence





2.4 Display Command

				ı	nstru	ction	Code	9				Description
Instructions		R/	DB	DB	DB	DB	DB	DB	DB	DB	Description	Time
	RS	W	7	6	5	4	3	2	1	0		(270KHz)
						-		_	-		Write "20H" to DDRAM, and set	
Clear	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms
Display										-	AC.	
											Set DDRAM address to "00H"	
											from AC and return cursor to it's	
Return	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms
Home											The contents of DDRAM	
											are not changed.	
											Sets cursor move direction and	
Entry Mode	_		_								specifies display shift. These	
Set	0	0	0	0	0	0	0	1	I/D	S	operations are performed	37μs
											during data write and read .	
Display											D=1 : entire display on	
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1 : cursor on	3 7 μs
											B=1 : cursor position on	
C:::::::::::::::::::::::::::::::::::::											Set cursor moving and display	
Cursor or	_	0	_	0	0		0.10	D/I			shift control bit, and the	27 0
Display	0	0	0	0	0	1	S/C	R/L	×	×	the direction, without changing	37μ s
Shift											of DDRAM data.	
Function											DL: interface data is 8/4 bits	
Function	0	0	0	0	1	DL	N	F	×	×	NL: number of line is 2/1	37 μ s
Set											F: font size is 5×11/5×8	
Set					AC	AC	AC	AC	AC	۸۵	Set CGRAM address	
CGRAM	0	0	0	1	5	4	3	2	1	0	in address counter.	3 7 µs
Address					3	4	3	_		U	in address counter.	
Set				AC	AC	AC	AC	AC	AC	AC	Set DDRAM address	
DDRAM	0	0	1	6	5	4	3	2	1	0	in address counter.	37μs
Address				O	3	4	3		I	U	in address counter.	
											Whether during internal	
Read Busy			В	AC	AC	AC	AC	AC	AC	AC	operation or not can be	
Flag and	0	1	F	6	5	4	3	2	1	0	known by reading BF.	0μs
Address			'			"	J	_	'		The contents of address	
											counter can also be read.	



Write Data to RAM	1	0	D 7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D 7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF Refer to Instruction Table for the list of each instruction execution time.



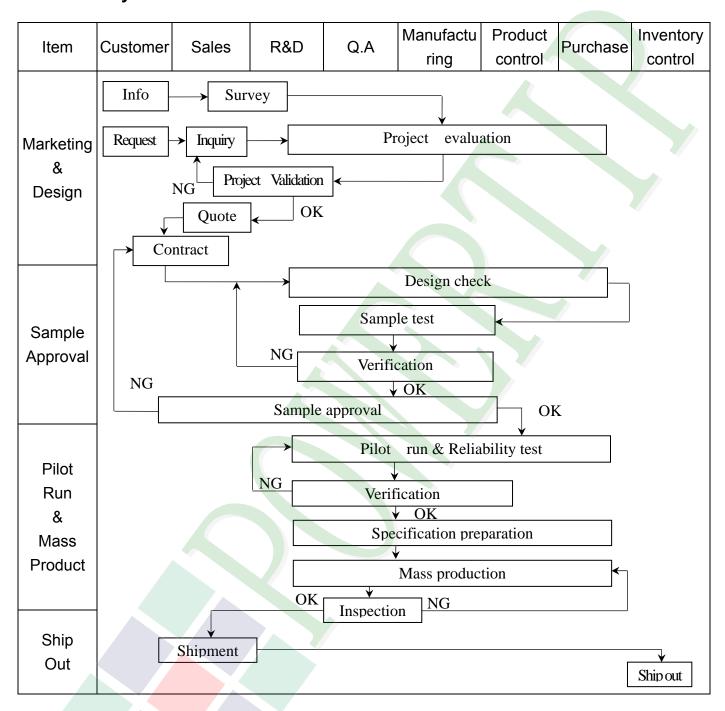
2.5 Character Pattern ST7066-0T

<u>67-64</u> 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															×
0001	(2)															×
0010	(3)															W
0011	(4)															
0100	(5)															
0101	(6)															
0110	(7)															
0111	(8)															i.
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															H
1101	(6)															
1110	(7)															
1111	(8)															

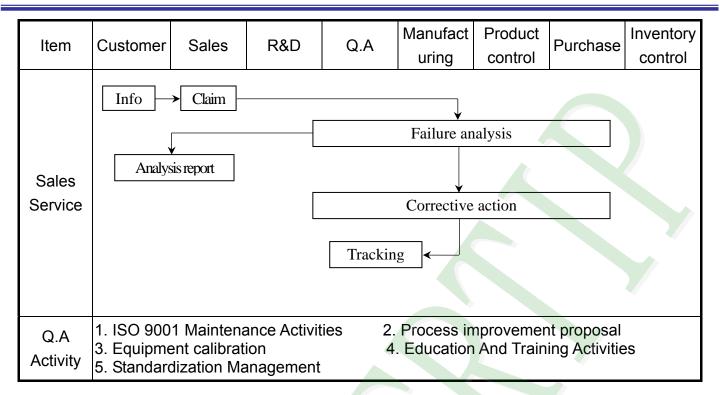


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

- ◆Scope: The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge · MIL-STD · Powertip Tester · Sample
- ♦ Defect Level: Major Defect AQL: 0.4; Minor Defect: AQL: 1.5.
- **♦**OUT Going Defect Level : Sampling .
- ◆Manner of appearance test :
 - (1). The test be under 20W×2 fluorescent light 'and distance of view must be at 30 cm.
 - (2). Standard of inspection: (Unit: mm)
 - (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (4). Definition of area . (Fig. 2)

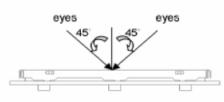


Fig.1

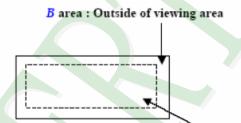


Fig. 2 A area: viewing area

♦ Specification:

NO	Item	Criterion	Level
		1. 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1. 3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3. 1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
04	Electrical Testing	4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major



◆Specification For Monotype and Color STN:

		type and Color STN:					Ver.B01)	
NO	Item	C	riteri	on			Level	
	Black or white dot \ scratch \ contamination	 5. 1 Round type: 5. 1. 1 display only: • White and black spots on 4 white or black spots pr • Densely spaced: NO more 	esent.				\	
		5. 1. 2 Non-display:						
	Round type	Dimension (diameter : Φ)		Acceptance (Q'ty)				
	round type					area		
	→ <u>x</u>	$\Phi \le 0.10$	Acce	ept no dense				
05	<u> </u>	$0.10 < \Phi \le 0.20$		3]	[gnore	Minor	
	.	$0.20 < \Phi \leq 0.30$		2				
	$\Phi = (x+y)/2$	Total quantity	4					
		5. 1. 3 Line type:						
	Linetrne	Dimension		Accep	otano	ce (Q'ty)		
	Line type	Length (L) Width (W)		A area		B area		
	✓ [▼] w	W ≦ (Accept no de	nse			
	→ı _L	$L \le 3.0$ $0.03 < W \le 0$	4			Ignore		
		$L \le 2.5$ $0.05 < W \le 0.$	075					
		W >0	. 075	As	roun	d type		
		5 .		A	(0	N4-3		
		Dimension (diameter : Φ)		Acceptano A area	ce (Q	B area		
		$\Phi \leq 0.20$	Ac	cept no dense				
06	Polarizer	$0.20 < \Phi \leq 0.50$		3				
00	Bubble	$0.50 < \Phi \le 1.00$		2	\dashv	Ignore	Minor	
		$\Phi > 1.00$	0		\dashv	-		
		Total quantity		4	\dashv			



◆Specification For Monotype and Color STN:

NO	Item	Criterion		Level
		Z: The thickness of crack W:	The width of crack. terminal length LCD side length	
		7. 1 General glass chip: 7. 1. 1 Chip on panel surface and crack	between panels:	
		Y Z	Y	
07	The crack of glass	SP Y	SP [NG]	Minor
		Seal width	Y	
		X Y	z	
		≤ a Crack can't enter viewing area	≦1/2 t	
		≤ a Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	



♦Specification For Monotype and Color STN:

NO	Item	Criterion					
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 7. 1. 2 Corner crack:					
		X Y Z					
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t					
0.7	The crack of	$\leq 1/5$ a Crack can't exceed the half of SP width. $1/2$ t $<$ Z ≤ 2 t					
07	glass	7.2 Protrusion over terminal:	Minor				
		7.2.1 Chip on electrode pad:					
		W W W W W W W W W W W W W W W W W W W					
		X Y Z					
		Front \leq a \leq 1/2 W \leq t					
		Back Neglect					



◆Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass X: The width of crack W: terminal length a: LCD side length	
		7.2.2 Non-conductive portion:	
07	The crack of glass	X Y Z ≤1/3 a ≤W ≤t ∴ If the chipped area touches the ITO terminal, over 2/3 of	Minor
		the ITO must remain and be inspected according to electrode	
		terminal specifications. 7. 2. 3 Glass remain :	
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \end{array}$	



♦Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
		9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

			(1011201)				
NO.	TEST ITEM	TEST CO	ONDITION				
1	High Temperature	Keep in +80 ±2°C 240hrs					
_	Storage Test	Surrounding temperature, then sto	orage at normal condition 4hrs.				
2	Low Temperature	Keep in −30 ±2°C 240hrs					
	Storage Test	Surrounding temperature, then sto					
,	High Temperature /	Keep in +60 °C / 90% R.H duration for 240hrs Surrounding temperature, then storage at normal condition 4hrs.					
3	High Humidity Storage Test	(Excluding the polarizer)	orage at normal condition 4hrs.				
	Storage Test		→ +80°C → +25°C				
4	Temperature Cycling Storage Test	(30mins) (5mins)					
	Storage Test	20 Cycle					
		Surrounding temperature, then sto					
		Air Discharge:	Contact Discharge:				
		Apply 2 KV with 5 times	Apply 250 V with 5 times				
	ESD Test	Discharge for each polarity +/-	discharge for each polarity +/-				
		 Temperature ambiance : 15°C Humidity relative : 30% ~ 60% 					
5		3. Energy Storage Capacitance(Cs+Cd): 150pF±10%					
		4. Discharge Resistance(Rd): 330Ω±10%					
		5. Discharge, mode of operation :					
		Single Discharge (time between successive discharges at least 1 sec)					
		(Tolerance if the output voltage indication: ±5%)					
		1. Sine wave 10~55 Hz frequence	y (1 min/sweep)				
6	Vibration Test	2. The amplitude of vibration :1.	• .				
	(Packaged)	3. Each direction (X \ Y \ Z) duration for 2 Hrs					
		Packing Weight (Kg)					
		$0 \sim 45.4$	122				
7	Drop Test	45.4 ~ 90.8	76				
	(Packaged)	90.8 ~ 454	61				
		0ver 454	46				
		Drop Direction: **1 corner / 3 edg	es / 6 sides each 1time				
1							



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

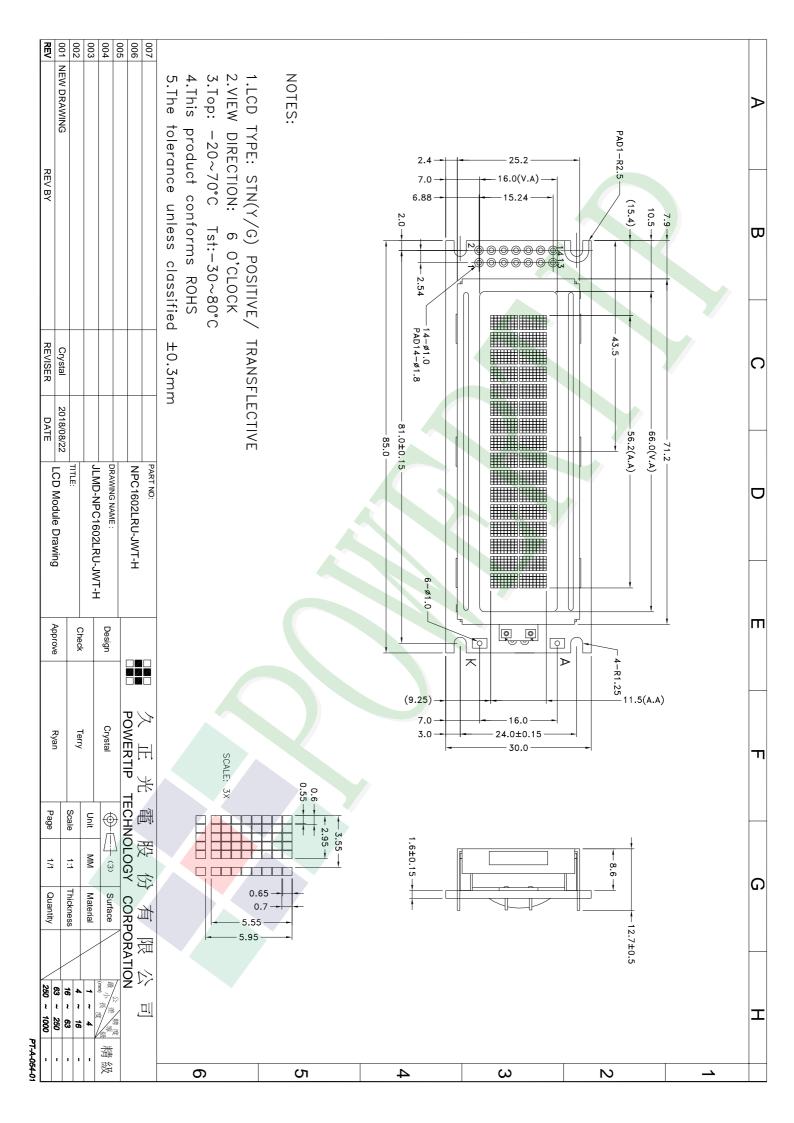
- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So, please handle it very carefully, do not touch, push or rub the exposed polarizing with anything harder than an HB pencil lead (glass, tweezers, etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320 \pm 10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM
- 5.2.10 Caution!(LCM products with Capacitive Touch Panel)
 Strong EMI-sources such as switch-mode power supplies (SMPS) can lead to touch malfunction (e.g. ghost-touches).
 - Therefore, the touch needs to be thoroughly tested inside the target application.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ± 5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
 - This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Ver.001

Documents NO. JPKG-NPC1602LRU-JWT-H

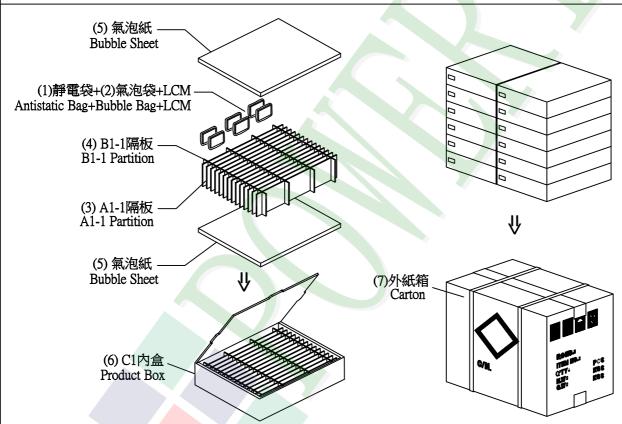
LCM包裝規格書 LCM Packaging Specifications

Approve	Check	Contact		
Ryan	Eddy	Terry		

1.包裝材料規格表 (Packaging Material): (per carton)

No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	NPC1602LRU-JWT-H	85.0 X 30.0 X 12.7	0.0322	468	15.0696
2	靜電袋(1)Antistatic Bag	BAG100100ARABA	100 X 100	0.0011	468	0.5148
3	A1-1隔板(3)A1-1 Partition	BX29500047BZBA	295 X 47 X 3	0.0078	168	1.3104
4	B1-1隔板(4)B1-1 Partition	BX24500047BZBA	245 X 47 X 3	0.0065	48	0.312
5	氣泡紙(5)Bubble Sheet	BAG280240BWABA	280 X 240	0.006	24	0.144
6	C1內盒(6)Product Box	BX31025555AABA	310 X 255 X 55	0.13	12	1.56
7	外紙箱(7)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
8						
9						

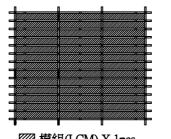
- 2. 整箱總重量 (Total LCD Weight in carton): 19.74 Kg±10%
- 3.單箱數量規格表 (Packaging Specifications and Quantity):
 - (1)Quantity Of Spacer: A1-1隔板 X 14, B1-1隔板 X
- (2) Total LCM quantity in carton: quantity per box 12 468 x no of boxes



特 記 事 項 (REMARK)

4. Label Specifications: 依廠內標準作業

- 5. LCM排放示意圖(前後間隔不放置):
- 5. LCM placed as figure showing: (First and last slot should be empty)



Ø 模組(LCM) X 1pcs.