

SPECIFICATIONS					
CUSTOMER	· PTC				
SAMPLE CODE	NSC1602LRU-FWB-H				
MASS PRODUCTION CODE	NPC1602LRU-FWB-H				
SAMPLE VERSION	· 01				
SPECIFICATIONS EDITION	· 001				
DRAWING NO. (Ver.)	jLMD- NPC1602LRU-FWB-H _001				
PACKAGING NO. (Ver.)	jPKG- NPC1602LRU-FWB-H _001				

Customer Approved

Date:

Approved	Checked	Designer
閆偉	劉進	徐明菲

 \Box Preliminary specification for design input

Specification for sample approval

POWERTIP TECH. CORP.

Headquarters: No.8, 6th Road, Taichung Industrial Park,

Taichung, Taiwan

台中市 407 工業區六路 8號

TEL: 886-4-2355-8168 FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw

Http://www.powertip.com.tw

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RD APPROVED



History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
05/07/2014	01	001	First Sample	-	徐明菲
	1			To	tal : 30 Pages



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Note : For detailed information please refer to IC data sheet : SITRONIX---ST7066U-0B



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Y/G , Positive , Transflective, Extended Temp.
Driver Condition	LCD Module : 1/16 Duty , 1/5 Bias
Viewing Direction	6 O'clock
Weight	40g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U-0B
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web site :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	84.0 (L) * 44.0 (W) *12.7 (H)	mm
Viewing Area	66.0 (L) * 16.0 (W)	mm
Active Area	56.2 (L) * 11.5 (W)	mm
Character Size	2.95(L)* 5.55 (W)	mm
Character Pitch	3.55(L)* 5.95 (W)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V _{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	V _{LCD}	-	Vdd -10.0	VDD +0.3	
Input Voltage	V _{IN}	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C
Storage Humidity	H_{D}	Ta<60 ℃	-	90	%RH

1.4 DC Electrical Characteristics

V_{DD} =5.0±0.5V, V_{SS} =0V, Ta = 25°(
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V_{DD}	-	4.5	5.0	5.5	V
"H" Input Voltage	V _{IH}	-	0.7 Vdd	-	Vdd	V
"L" Input Voltage	V _{IL}	-	-0.3	-	0.6	V
"H" Output Voltage	V _{OH}	IOH=-0.1mA	3.9	-	Vdd	V
"L" Output Voltage	V _{OL}	lo∟=0.1mA		-	0.4	V
Supply Current	I _{DD}	V _{DD} = 5.0 V, Vop= 4.5 V Pattern= Horizontal Line *1	-	2	3	mA
		-20 °℃	4.4	4.6	4.8	
LCM Driver Voltage	V _{OP} *2	25 ℃	4.3	4.5	4.7	V
		70 ℃	4.1	4.3	4.5	

NOTE: *1 The Maximum current display

*2 The V_{OP} test point is (V_DD - V_0)



1.5 Optical Characteristics

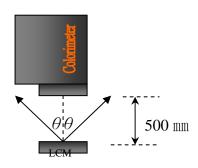
			LCD Panel	:1/16 Dut	y,1/5 Bia	as,V _{LCD} :	= 4.5 V,	Ta =25 ℃
Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Response Time	Rise	tr		-	80	125	me	Note 2
Response nine	Fall	tf	_	-	220	330	ms	Note 2
	Тор	θ+		-	40	-		
Viewing angle	Bottom	θ-	022.0	-	40	-	Deg.	Note 1
range	Left	θL	C <u>≥</u> 2.0	-	45	-		
	Right	θR		-	45	-		
Contrast Ra	tio	С	$\theta = 0^{\circ}$	-	10	-	-	Note 3
Average Bright (with LCD)		IV	IE-100 m A	30	35	-	cd/m ²	
Wavelength (with LCD) *2		λ ρ	IF=100 mA	568	571	574	nm	Noto 4
Uniformity '	ʻ1	ΔB	IF=100 mA	70	-	-	%	Note 4

Note 4:

1 : △B=B(min) / B(max) * 100%

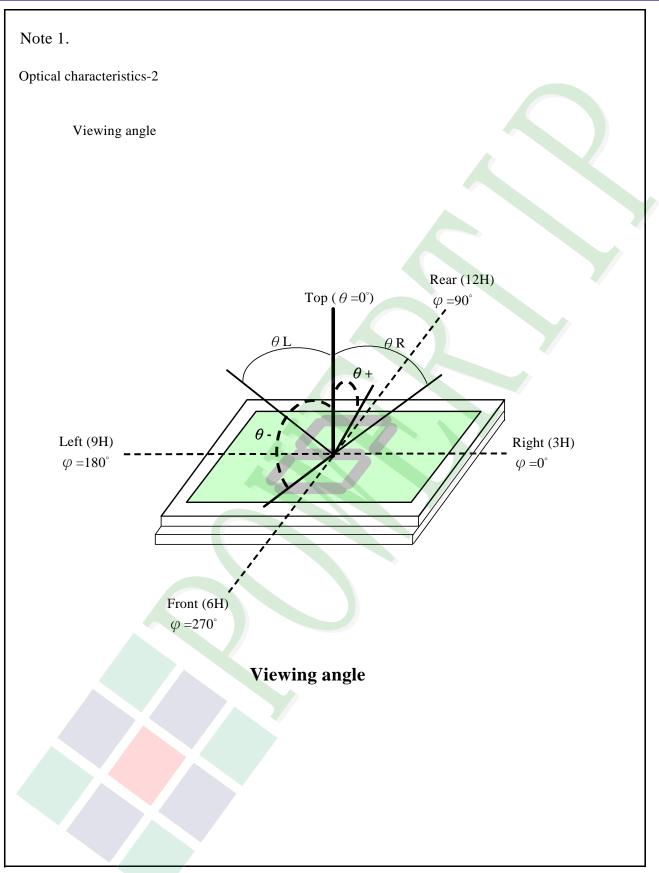
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C ±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 ± 50 mm \rightarrow (θ = 0°)
 - c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01 , Average Brightness ± 4%



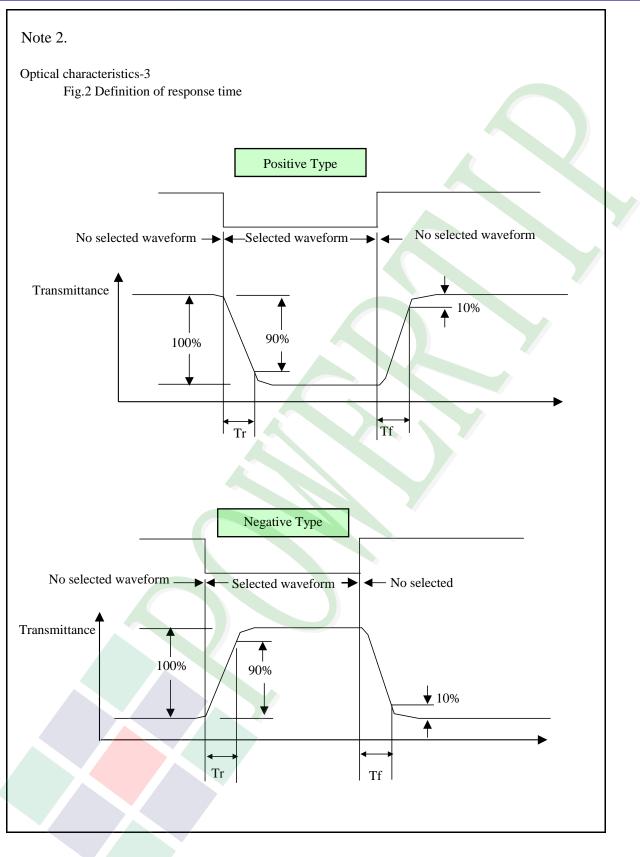


Colorimeter=BM-7 fast

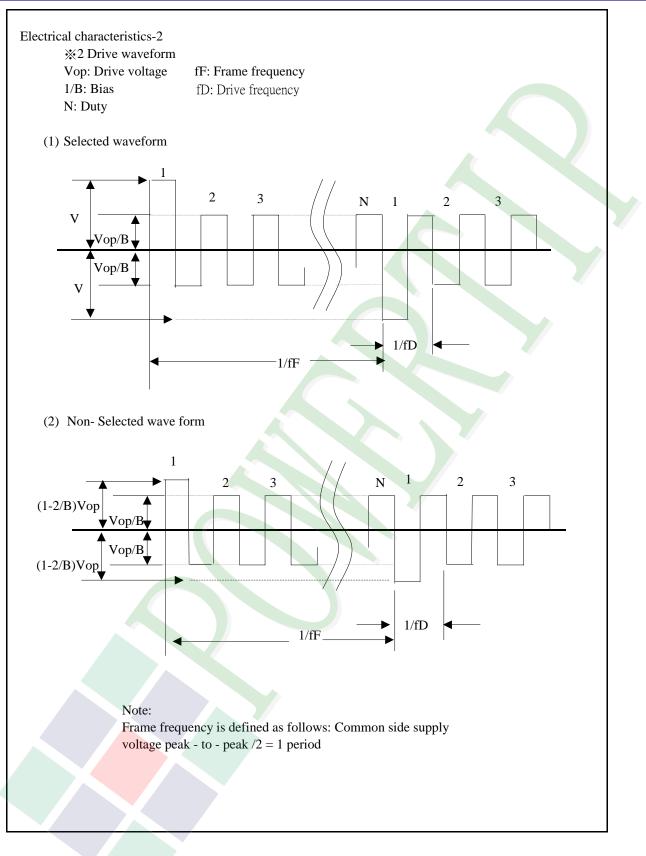




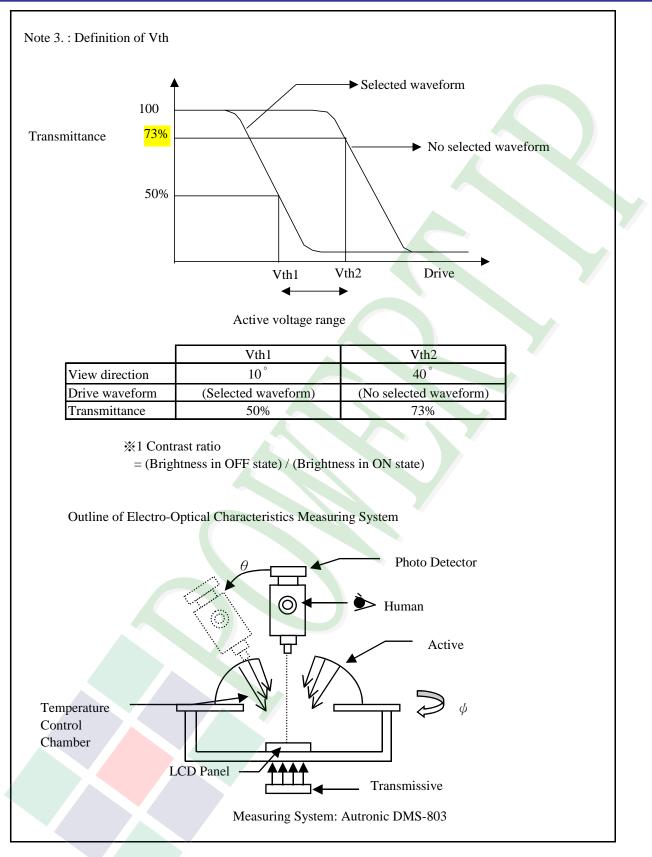














1.6 Backlight Characteristics

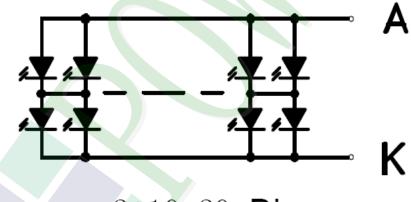
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25 ℃	-	150	mA
Reverse Voltage	VR	Ta =25℃	-	8	V
Power Dissipation	PD	Ta =25℃	-	660	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 100 mA	4.0	4.2	4.4	V
Reverse Current	IR	VR=8V		-	100	uA
Average Brightness	IV	IF= 100 mA	160	190	-	cd/m ²
Wavelength	λρ	IF= 100 mA	569	572	575	nm
Color	Yellow/Green					

Internal Circuit Diagram:



2*10=20 Dices

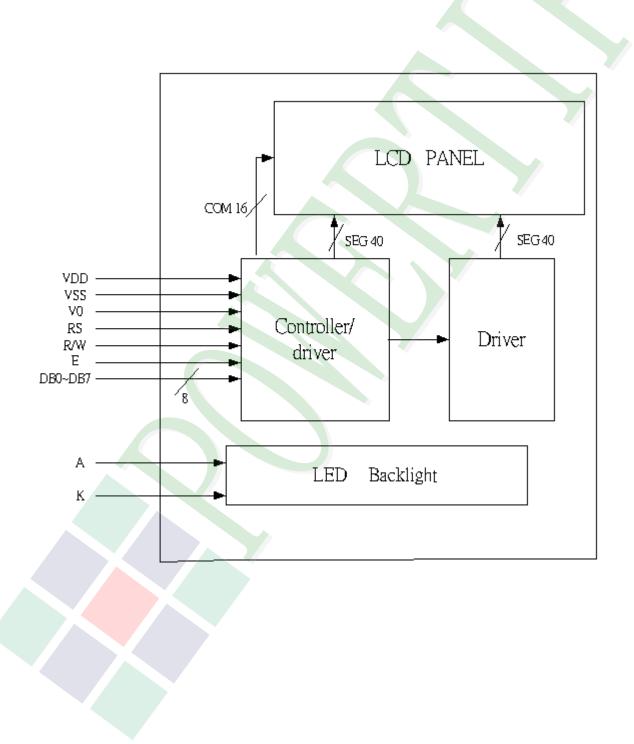


2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

- * See Appendix
- 2.1.2 Block Diagram





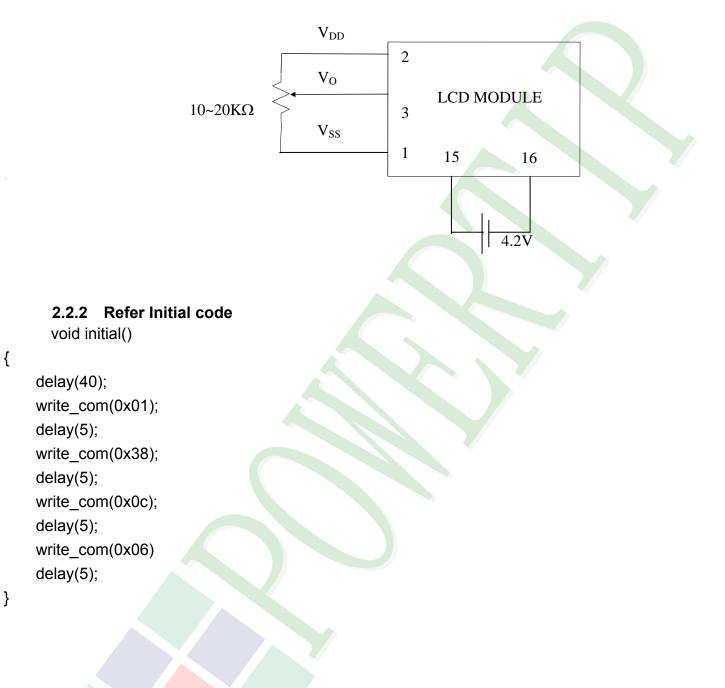
2.2 Interface Pin Description

Pin No.	Symbol	Signal Description			
1	V _{SS}	Power Supply (Vss=0)			
2	V _{DD}	Power Supply (5V)			
3	Vo	Operating voltage for LCD			
		Register Selection input			
4	RS	High = Data register			
4	K5	Low = Instruction register (for write)			
		Busy flag address counter (for read)			
5	R/W	Read/Write signal input is used to select the read/write mode			
5		High = Read mode, Low = Write mode			
6	E	Start enable signal to read or write the data			
7	DB0	Four low order his direction of three state date has lines. Lies for			
8	DB1	Four low order bi-directional three-state data bus lines. Use for			
9	DB2	data transfer between the MPU and the LCD module.			
10	DB3	These four are not used during 4-bit operation.			
11	DB4				
12	DB5	Four high order bi-directional three-state data bus lines. Used			
13	DB6	for data transfer between the MPU and the LCD module.			
14	DB7	DB7 can be used as a busy flag.			
15	А	LED+			
16	К	LED-			



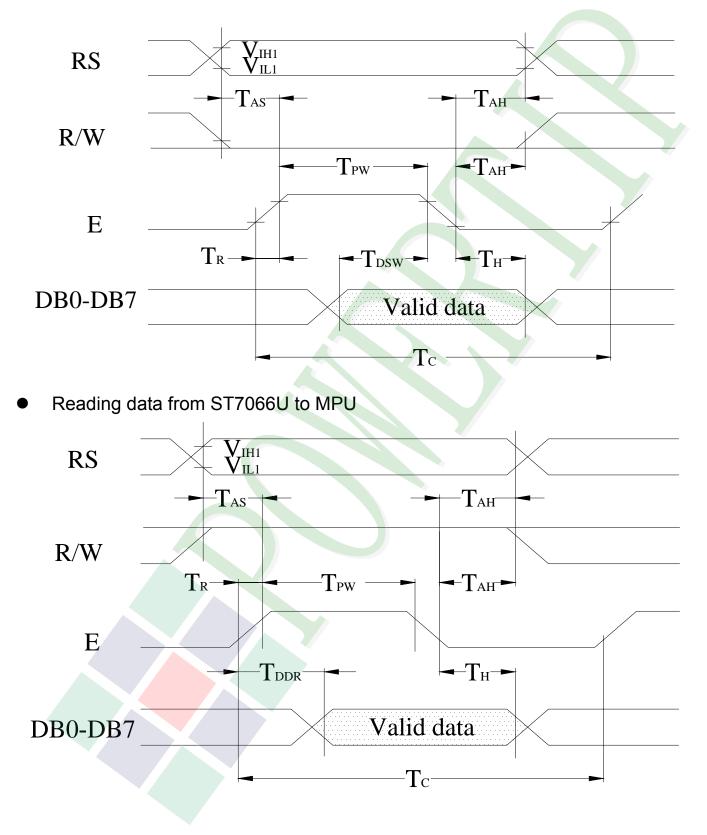
2.2.1 Application Notes

Contrast Adjust



2.3 Timing Characteristics

• Writing data from MPU to ST7066U





• Write Mode (Writing data from MPU to ST7066U)

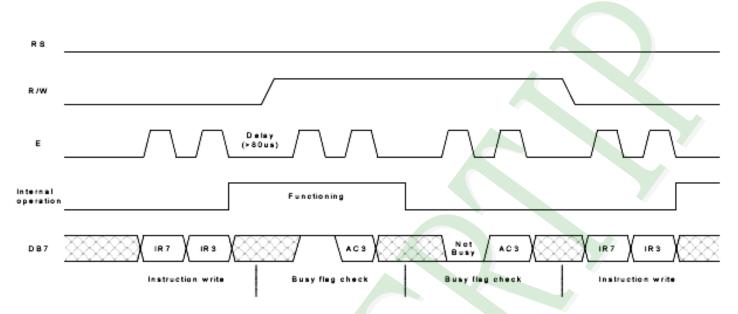
_					(Vdd = -	+5V,Ta=25°	°C)
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit	
T _C	Enable Cycle Time	Pin E	1200	-	-	ns	
T _{PW}	Enable Pulse Width	Pin E	140	-	4	ns	
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns	
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns	
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns	
T _{DSW}	Data Setup Time	Pins:DB0~DB7	40	-	-	ns	
Т _Н	Data Hold Time	Pins:DB0~DB7	10	-	-	ns	

• Read Mode (Reading data from ST7066U to MPU)

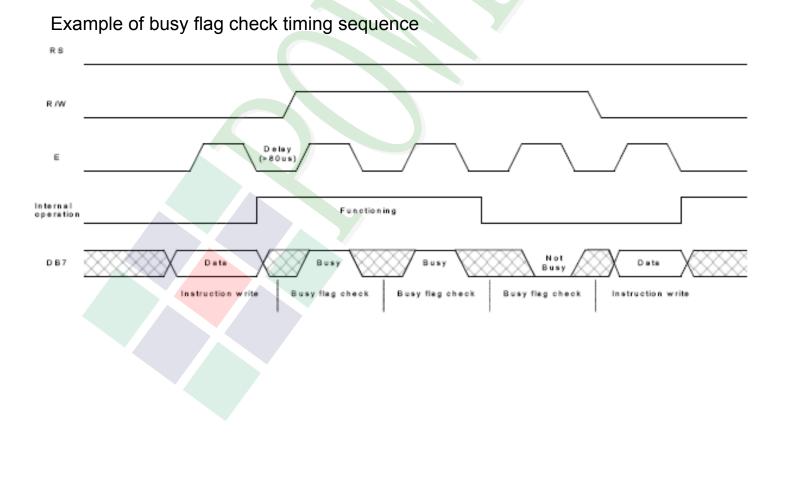
					VDD = +5	V, Ia=25°C
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	1		ns
T_{PW}	Enable Pulse Width	Pin E	140	1	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	1	25	ns
T _{AS}	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
Τ _Η	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .





2.4 Display Command

				I	nstru	ction	Code	9				Description
Instructions		R/	DB	DB	DB	DB	DB	DB	DB	DB	Description	Time
	RS	W	7	6	5	4	3	2	1	0		(270KHz)
01											Write "20H" to DDRAM. and set	
Clear	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms
Display											AC.	
											Set DDRAM address to "00H"	
Deture											from AC and return cursor to it's	
Return	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms
Home											The contents of DDRAM	
											are not changed.	
											Sets cursor move direction and	
Entry Mode	~	~	~	0	0	•				0	specifies display shift. These	07.
Set	0	0	0	0	0	0	0	1	I/D	S	operations are performed	37µs
											during data write and read .	
Display											D=1 : entire display on	
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1 : cursor on	37 μ s
											B=1 : cursor position on	
Cureer or											Set cursor moving and display	
Cursor or	0	0	0	0	0	1	S/C	R/L	×	×	shift control bit, and the	270
Display Shift	0	U	0	0	0		3/0	R/L			the direction, without changing	37µs
Shint											of DDRAM data.	
Function											DL: interface data is 8/4 bits	
Set	0	0	0	0	1	DL	Ν	F	×	×	NL: number of line is 2/1	37 μ s
Sei											F: font size is 5×11/5×8	
Set					AC	AC	AC	AC	AC	<u>، د</u>	Set CGRAM address	
CGRAM	0	0	0	1	AC 5	4 4	3	2	AC 1		in address counter.	37 μ s
Address					5	4	5	~ 2	1	0		
Set				AC	AC	AC	AC	AC	AC		Set DDRAM address	
DDRAM	0	0	1	6	5	4	3	2	1	0	in address counter.	37 μ s
Address				0	5	-	5	2		0		
											Whether during internal	
Read Busy			в	AC	AC	AC	AC	AC	AC	AC	operation or not can be	
Flag and	0	1	F	6	5	4	3	2	1	0	known by reading BF.	0 μ s
Address				0	5	-		2		U	The contents of address	
											counter can also be read.	



Write Data to RAM	1	0	D 7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37 µs
Read Data from RAM	1	1	D 7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF Refer to Instruction Table for the list of each instruction execution time .

2.5 Character Pattern

NO.7066-0B

	000-	<u> </u>														
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	0															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110 (Ø															
1111	(8)															

2.6 JUMPER(Setting different use)

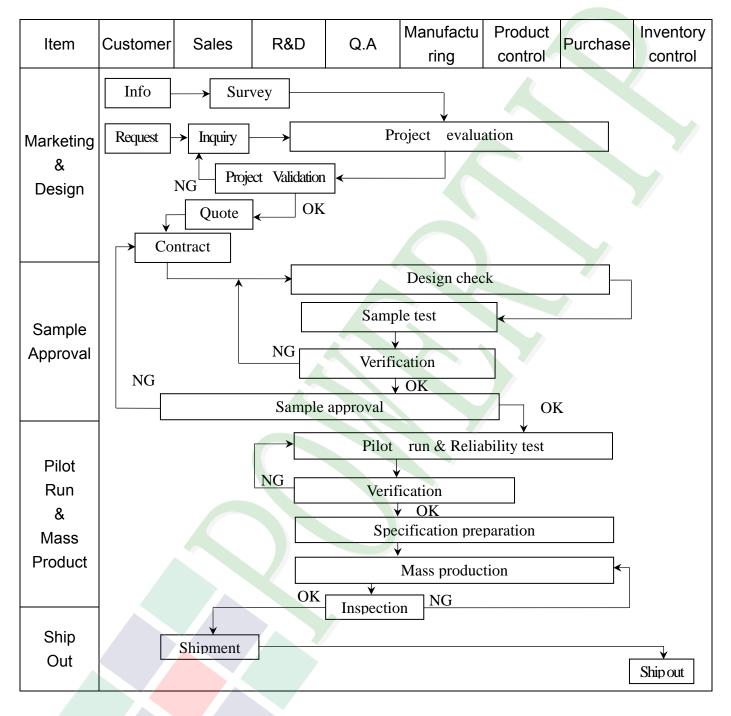
J1/J4:SHORT;

Others :open



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart





Item	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control	
Sales Service	Info Analys	→ Claim -	[Trackin	Failure an Corrective				
Q.A Activity	1. ISO 900 3. Equipme 5. Standare		ion	4. Education And Training Activities					

3.2 **Inspection Specification**

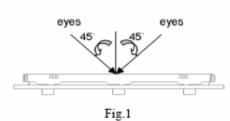
Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).

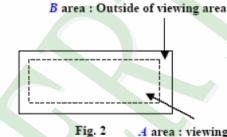
◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

- Equipment : Gauge \ MIL-STD \ Powertip Tester \ Sample
- ◆Defect Level: Major Defect AQL:0.4 ; Minor Defect:AQL: 1.5.
- OUT Going Defect Level : Sampling .

Manner of appearance test :

- (1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.
- (2). Standard of inspection : (Unit : mm)
- (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
- (4). Definition of area . (Fig. 2)





A area : viewing area

Specification:

NO	Item	Criterion	Level
		1. 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
04	Electrical Testing	4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major



♦Spe	cification For Mono	otype and Color	STN :					(Ver.B01)
NO	Item		C	riteri	on				Level
	Black or white dot 、scratch 、 contamination	 5. 1 Round type: 5. 1. 1 display only : White and black spots on display ≤ 0. 30 mm, no more than 4 white or black spots present. Densely spaced : NO more than two spots or lines within 3 mm. 							
		5. 1. 2 Non-6	display : mension			(0)()		T	
	Round type	(diai	Acceptance (Q't						
			Φ≦ 0.10		A area ept no dense	D	area	1	
	≯ <u>x</u> ⊭	0.10	Att	-					
05	● Y ▲	0.10 <	3			Ignore		Minor	
		0.20 <	$0.20 < \Phi \leq 0.30$		2				
	Φ=(x+y)/2	Tota	al quantity		4				
		5. 1. 3 Line t	type:						
	-		Dimension		Accep	tance	(Q'ty)		
	Line type	Length (L)	Width (W)		A area		B area	I	
	∽ / ¥ w		W≦	0.03	Accept no der	nse			
		$L \leq 3.0$	$0.03 < W \leq 0$	0.05			Ignor	e	
	L	L ≤ 2.5	$0.05 < \mathbf{W} \leq 0.$	075	4				
		W >0		. 075 As rour			nd type		
		Dim	nension		Acceptanc	ce (Q't	y)		
		(diam	eter : Φ)		A area		B area		
			$\Phi \leq 0.20$	Ac	cept no dense				
06	Polarizer	0.20 <	$\Phi \leq 0.50$	3					
	Bubble	$0.50 < \Phi \le 1.00$			2		Ignore		Minor
			$\Phi > 1.00$		0				
		Total	quantity		4				
		Total quantity 4							

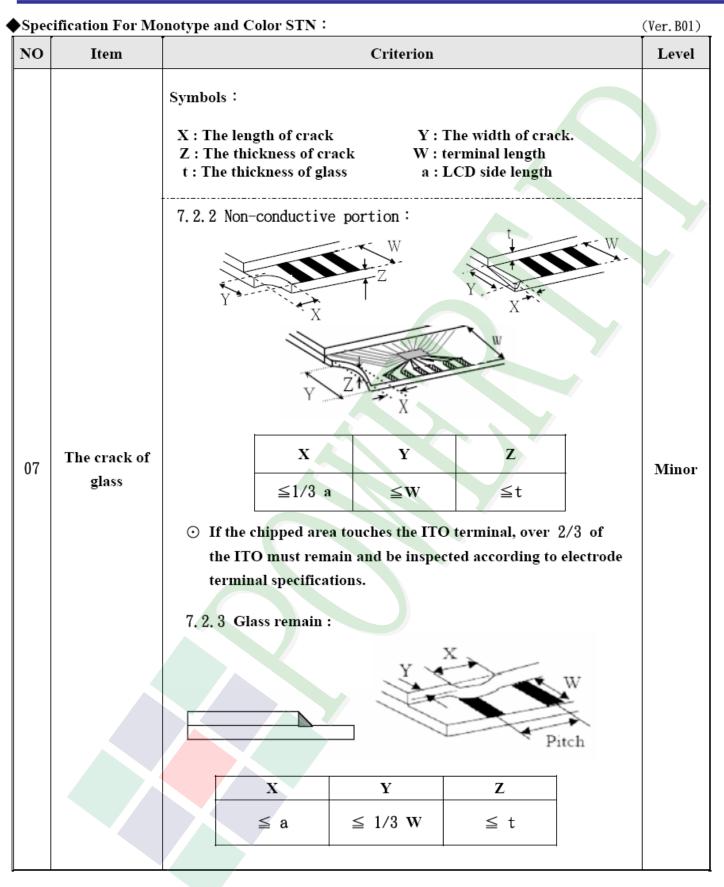


Specification For Monotype and Color STN : (Ver. B0)							
NO	Item	Criterion	1	Level			
		Z : The thickness of crack W	7 : The width of crack. 7 : terminal length 2 : LCD side length				
		7.1 General glass chip: 7.1.1 Chip on panel surface and crac	ck between panels:				
		y z z	Z X Y X				
07	The crack of glass	SP Y	SP [NG]	Minor			
			Y				
		X Y	Z				
		≤ a Crack can't enter viewing area	≦1/2 t				
		≤ a Crack can't exceed th half of SP width.	$e 1/2 t < Z \leq 2 t$				



T			Ver.B01)					
NO	Item	Criterion	Level					
		Symbols : X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length						
		7.1.2 Corner crack :						
		X Y Z						
		$ \leq 1/5 \text{ a} \qquad \begin{array}{c} \text{Crack can't enter} \\ \text{viewing area} \end{array} \qquad \textbf{Z} \qquad \leq 1/2 \text{ t} \end{array} $						
	The crack of	$ \leq 1/5 \text{ a} \begin{array}{c} \text{Crack can't exceed the} \\ \text{half of SP width.} \end{array} 1/2 \text{ t} < \text{Z} \leq 2 \text{ t} \end{array} $						
07	glass		Minor					
	2	7.2 Protrusion over terminal:						
		7.2.1 Chip on electrode pad:						
		X Y Z W Y						
		X						
		X Y Z						
		Front $\leq a \leq 1/2$ W $\leq t$						
		Back Neglect						







Speci	ification For Mo	notype and Color STN :	(Ver.B01)
NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
		9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1. 5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

4.I r	Reliability lest Condition (Ver.B01)							
NO.	TEST ITEM	TEST C	CONDITION					
1	High Temperature Storage Test	Keep in +80℃±2℃ 96 hrs Surrounding temperature, the 4hrs.	n storage at normal condition					
2	Low Temperature Storage Test	Keep in -30℃ ±2℃ 96 hrs Surrounding temperature, the 4hrs.	n storage at normal condition					
3	High Temperature / High Humidity Storage Test	Keep in +60°C / 90% R.H durat Surrounding temperature, th 4hrs. (Excluding the polarizer)	ion for 96 hrs en storage at normal condition					
		-30(min) → +25° C	→+80°C (max) → +25°C					
	Tomporature Cualiza	(30mins) (5mins	;) (30mins) (5mins)					
4	Temperature Cycling Storage Test	10	Cycle					
	Juli g	Surrounding temperature, then storage at normal condition						
		4hrs.						
		Air Discharge: Apply 2 KV with 5 times Discharge for each polarity	Contact Discharge: Apply 250 V with 5 times discharge for each polarity +/-					
		+/- 1. Temperature ambiance : 15° C ~ 35° C						
5	ESD Test	2. Humidity relative : $30\% \sim 60\%$						
		3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%						
		 Discharge Resistance(Rd) : 330Ω±10% Discharge, mode of operation : 						
		Single Discharge (time between successive discharges at least						
		1 sec) (Tolerance if th	e output voltage indication : ±5%)					
	Vibratian Test	1. Sine wave 10~55 Hz frequ	ency (1 min/sweep)					
6	Vibration Test (Packaged)	2. The amplitude of vibration :1.5 mm						
	(3. Each direction (X \ Y \ Z)	duration for 2 Hrs					
		Packing Weight (K	g) Drop Height (cm)					
		0 ~ 45.4	122					
7	Drop T <mark>est</mark>	45.4 ~ 90.8	76					
	(Packaged)	90.8 ~ 454	61					
		Over 454	46					
		Drop Direction : %1 corner / 3	edges / 6 sides each 1time					

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required

