

SPECIFICATION

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OLED SPECIFICATION

Model No:

REG010032ABPP5N00000

CUSTOMER:

APPROVED BY

PCB VERSION

DATE

FOR CUSTOMER USE ONLY

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY	
Release DATE:				



1. Revision History

VERSION	DATE	REVISED PAGE NO.	Note
0	2013/12/10		First release
A	2014/06/16		Add Low Temperature
			storage.
В	2015/05/04		Modify IDD
С	2016/06/01		Modify Static
			electricity test



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1.General Specification

The Features is described as follow:

- Module dimension: 98.0 x 60.0 x 10.0 (max.) mm
- View area: 77.0x25.20mm
- Active area: 58.95 x 19.15mm
- Dot Matrix: 100*32
- Dot size: 0.54 x 0.55 mm
- Dot pitch: 0.59x 0.60mm
- Duty: 1/16
- Emitting Color: OLED , Blue



2.Module Coding System

1	2	3	4	5	6	7	8	9	10	11	12	13	14
R	E	G	010032	А	В	Р	Р	5	N	0	0	0	00

1	Brand : Raystar Opt	ronics Inc.								
2	E : OLED									
3	Display Type : C→C	Character, G→Graphi	c , T→TAB ,X→COG .	$H \rightarrow COG$ (with Frame)						
4	Dot Matrix : 100*32	2								
5	Series			7						
		A : Amber	R : Red	C : Full Color						
6	Emitting Color	B : Blue	W : White							
0		G : Green	Y : Yellow							
		S : Sky Blue	X : Dual Color							
7	Polarizer	P : With Polarizer; I	N: Without Polarizer							
1	Polanzer	A : Anti-glare Polari	izer							
8	Display Mode	P : Passive Matrix ;	P : Passive Matrix ; N : Active Matrix							
9	Driver Voltage	3:3.0~3.3V;5	3:3.0~3.3V; 5:5.0V							
10	Touch Panel	N : Without touch p	N : Without touch panel; T: With touch panel							
		0 : Standard								
		1 : Sunlight Readable								
11	Product type	2 : Transparent OLED (TOLED)								
		3 : Flexible OLED (FOLED)							
		4 : OLED Lighting								
		0 : Standard								
10	Inspection Crede	2 : B grade								
12	Inspection Grade	C : Automotive grad	de							
		Y : Consumer grade	е	100000						
13	Interface	0 : Default ; F : FP0	C;H:Hot bar;D:De	mo Kit						
14	Serial No.	Serial number(00~Z	ZZ)							



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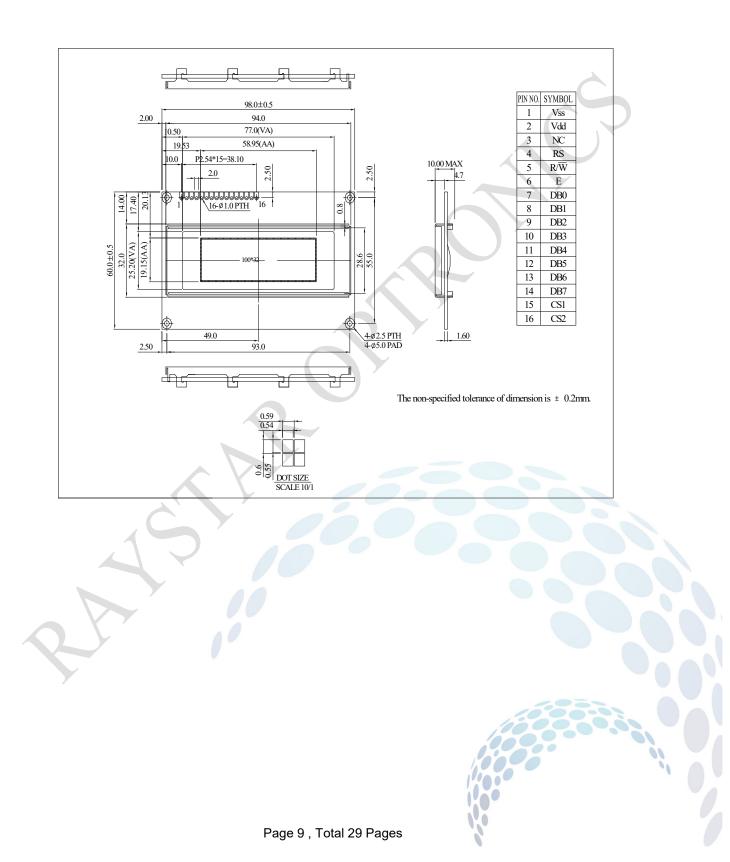
3.Interface Pin Function

Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply Voltage for logic
3	NC	-	
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(Module→MPU) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	CS1	-	Chip1 select input pin
16	CS2	5	Chip2 select input pin

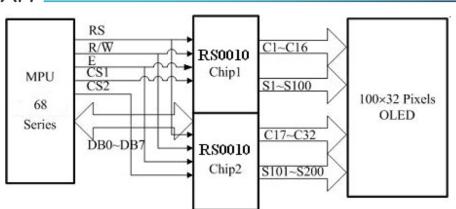
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4.Counter Drawing & Block Diagram







Ad	Idress I	Format			DE	37	D	B6	DB5	DB4	D	B3	D	B2	D	31	DB0	
GXA(Gra	aphic X-	-axis A	ddre	SS	1	1	A	DD6	ADD5	ADD4	ADD3		AD	D2	AD	D1	AD	D0
GYA(Gra	aphic Y	axis A	ddre	SS	0)		1	0	0		0	(0	()	CG	A0
		1		2	3		4	2	23 C 17 C		9	7	9	8	9	9	10	00
CS1=0 CS2=1	CGA=0	GXA=10000000 GXA=10000000 GYA=01000001 GYA=01000000	GXA=10000001	GYA=01000000	GXA=10000010	GYA=01000000	GXA=10000011	GYA=01000000			GXA=11100000	GYA=01000000	GXA=11100001	GYA=01000000	GXA=11100010	GYA=01000000	GXA=11100011	GYA=01000000
	CGA=1	GXA=10000000 GYA=01000001	GXA=10000001 GXA=10000001	GYA=01000001 GYA=01000000		GYA=01000001	GXA=10000011 GXA=10000011	GYA=01000001			GXA=11100000	GYA=01000001	GXA=11100001 GXA=11100001	GYA=01000001 GYA=01000000	GXA=11100010 GXA=11100010	GYA=01000001	GXA=11100011 GXA=11100011	GYA=01000001
		1	1	2	3		4				97 98		99		100			
CS1=1 CS2=0	CGA=0	GXA=10000000 GXA=10000000 GYA=01000001 GYA=01000000	GXA=10000001	GYA=01000000	GXA=10000010 GXA=10000010	GYA=01000000	GXA=10000011	GYA=01000000			GXA=11100000	GYA=01000000	GXA=11100001	GYA=01000000	GXA=11100010	GYA=01000000	GXA=11100011	GYA=01000000
	CGA=1	GXA=10000000 GYA=01000001	GXA=10000001 GXA=10000001	GYA=01000001	GXA=10000010	GYA=01000001	GXA=10000011 GXA=10000011	GYA=01000001			GXA=11100000	GYA=01000001	GXA=11100001	GYA=01000001	GXA=11100010 GXA=11100010	GYA=01000001	GXA=11100011 GXA=11100011	GYA=01000001 GYA=01000000





5.Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Notes
Operating Temperature	T _{OP}	-40	+80	°C	
Storage Temperature	T _{ST}	-40	+80	°C	2
Supply Voltage For Logic	VDD-V _{SS}	-0.3	5.3	V	

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6.Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	VDD-VSS	-	4.8	5.0	5.3	V
Input High Volt.	VIH	-	0.8 VDD	-	VDD	V
Input Low Volt.	VIL	-	GND	-	0.2 VDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8 VDD		VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND	-)	0.2 VDD	V
50% Check Board Operating Current	IDD	VDD=5V	46	50	54	mA

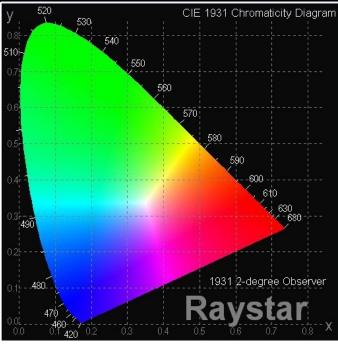
Note: In order to avoid any possible damages, 3V or 3.3V logic I/O for VDD 5V OLED module is not recommended.

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7.Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ	-	160	-	-	deg
View Angle	(Н)ф	-	160	-	-	deg
Contrast Ratio	CR	Dark	2000:1	-		-
Response Time	T rise	-	-	10	-	μs
	T fall	-	-	10	-	μs
Display with 50% che	eck Board Bright	ness	50	60	-	cd/m2
CIEx(Blue)		(CIE1931)	0.12	0.16	0.20	-
CIEy(Blue)		(CIE1931)	0.19	0.23	0.27	-





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8.OLED Lifetime

ITEM	Conditions	Min	Тур	Remark
Operating Life Time	Ta=25℃ / Initial 50% check Board Typical Brightness Value	40,000 Hrs	50,000 Hrs	Note

Note:

- 1. Life time is defined the amount of time when the luminance has decayed to <50% of the initial value.
- 2. This analysis method uses life data obtained under accelerated conditions to extrapolate an estimated probability density function (*pdf*) for the product under normal use conditions.
- 3. Screen saving mode will extend OLED lifetime.



9.Reliability

Content of Reliability Test

Environmenta	l Test		
Test Item	Content of Test	Test Condition	Applicable Standard
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80 □ 240hrs	
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-40⊡ 240hrs	
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80 240hrs	
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-40 □ 240hrs	
High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60□,90%RH 240hrs	
Temperature Cycle	Endurance test applying the low and high temperature cycle. -40 25 80 30min 5min 30min 1 cycle	-40□/80□ 100 cycles	
Mechanical Tes	st		
Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hr	50
Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sin wave 11 ms 3 times of each direction	
Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115mbar 40hrs	
Others			
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=±600V(contact), ±800v(air), RS=330Ω CS=150pF 10 times	

*** Supply voltage for OLED system =Operating voltage at 25°C



Test and measurement conditions

- 1. All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.
- 2. All-pixels-on is used as operation test pattern.
- 3. The degradation of Polarizer are ignored for High Temperature storage, High Temperature/ Humidity Storage, Temperature Cycle

Evaluation criteria

- 1. The function test is OK.
- 2. No observable defects.
- 3. Luminance: > 50% of initial value.
- 4. Current consumption: within ± 50% of initial value.

APPENDIX:

RESIDUE IMAGE

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.



10.Inspection specification

NO	Item	Criterion						AQL		
01	Electrical Testing	1.1 Missing verti defect. 1.2 Missing char			-	gme	ent contrast			
		1.3 Display malf	unction.				(
		1.4 No function of 1.5 Current cons			eds product s	peo	cifications.	0.65		
		1.6 OLED viewir	ng angle d		•	P				
		1.7 Mixed produ 1.8 Contrast def								
02	Black or white	2.1 White and bl three white or bla	•			mm	i, no more than			
	spots on	2.2 Densely spa	•	•		s o	r lines within	2.5		
	OLED (display	3mm.						2.0		
ļ	only)									
03	OLED black	3.1 Round type :			SIZE		Acceptable Q			
	spots,	$\Phi = (x + y) / 2$	following drawing $\Phi=(x + y)/2$				TY			
	white spots,				Ф≦0.10		Accept no dense			
	contamina				0.10 < Φ≦0.2	20	2			
	tion (non-displ				0.20 < Φ≦0.2	25	1			
	ay)			0.25 < Φ 0						
		3.2 Line type : (A	As followin	g dr		_				
			Length	Wi	dth		cceptable Q TY			
	A			W	≦0.02	Α	ccept no dense			
			L≦3.0	0.0)2 < W≦0.03	- 2		2.5		
		Y	L≦2.5	0.0)3 < W≦0.05					
				0.0)5 < W	Α	s round type			
04	Polarizer bubbles	If bubbles are vis	sihle	Si	ze Φ		Acceptable Q TY			
		judge using blac	k spot		<u>≤c ¢</u> ≦0.20	_	Accept no dense			
		specifications, no to find, must che	ifications, not easy		20 < Φ≦0.50	3	3	2.5		
		-	specify direction.			2	2	2.5		
						0	0000			
		$1.00 < \Phi \qquad 0$ Total Q TY 3								
			Doco 17	Tete	20 Deces					
			Page 17,	i otal	29 Pages		5			



NO	Item	Criterion			AQL
)5	Scratches	Follow NO.3 OLED black spots, white spots, contamination			
			t: Glass thickness a	Chip thickness : OLED side length	
		6.1 General glass ch 6.1.1 Chip on panel s	ip : surface and crack bet	ween panels:	P
		z: Chip thickness	y: Chip width	x: Chip length	
	<u>Obiene ed</u>	Z≦1/2t	Not over viewing area	x≦1/8a	
)6	Chipped glass	1/2t < z≦2t	Not exceed 1/3k	x≦1/8a	2.5
		 ⊙If there are 2 or more chips, x is total length of each chip. 6.1.2 Corner crack: 			
		z: Chip thickness	y: Chip width	x: Chip length	
		Z≦1/2t	Not over viewing area	x≦1/8a	
	h ('	1/2t < z≦2t	Not exceed 1/3k	x≦1/8a	
		\odot If there are 2 or more chips, x is the total length of each chip.			



NO	Item	Criterion						AQL
		Symbols :	v: Chiny	width 7: (hin	thicknoss		
		x: Chip lengthy: Chip widthz: Chip thicknessk: Seal widtht: Glass thicknessa: OLED side length						
		L: Electrode pad length						
		6.2 Protrusion over te 6.2.1 Chip on electro						
)	
		y: Chip width	x: Chi	o length	z: (Chip thickness		
		y≦0.5mm	x≦1/8a			< z ≦ t		
		6.2.2 Non-conductive	e portior	:				
						,		
					<i>Y</i>			
06	Glass						,	2.5
00	crack						4	2.5
		y: Chip width	X:	Chip length		z: Chip		
						thickness		
		y≦ L	x≦	1/8a		0 < z≦t		
		⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO						
		must remain and be inspected according to electrode terminal						
		 specifications. ⊙If the product will be heat sealed by the customer, the alignment 						
		mark not be damaged.						
		6.2.3 Substrate protuberance and internal crack.						
		<i>y</i>		y: width		x: length		
				y≦1/3L		x≦a		
						150		
						1000		Ś.
					_		_	

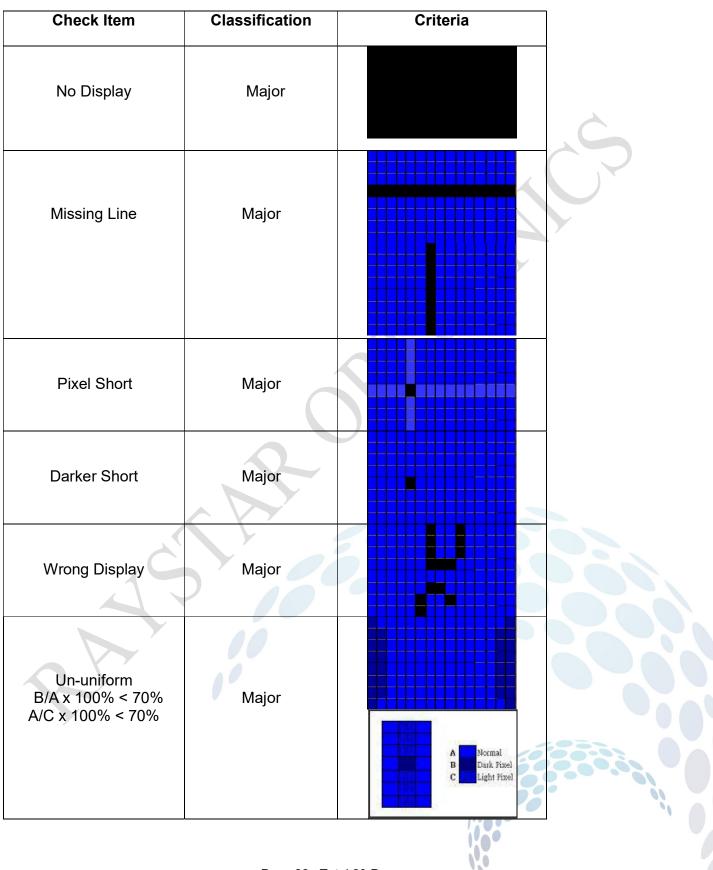


NO	Item	Criterion	AQL		
07	Cracked glass	The OLED with extensive crack is not acceptable.			
08	Backlight elements	 8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using OLED spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong. 			
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.9.2 Bezel must comply with job specifications.	2.5 0.65		
10	PCB、COB	 10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, OLED pad, zebra pad or screw hold pad, make sure it is smoothed down. 	 2.5 2.5 2.5 2.5 0.65 0.65 2.5 		
11	Soldering	 11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB. 	2.5 2.5 2.5 0.65		



NO	Item	Criterion	AQL	
NO 12	Item General appearance	 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP. 12.2 No cracks on interface pin (OLB) of TCP. 12.3 No contamination, solder residue or solder balls on product. 12.4 The IC on the TCP may not be damaged, circuits. 12.5 The uppermost edge of the protective strip on the 		
		interface pin must be present or look as if it cause the interface pin to sever.	2.5	
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5 0.65	
		12.7 Sealant on top of the ITO circuit has not hardened.	0.65	
		12.8 Pin type must match type in specification sheet. 12.9 OLED pin loose or missing pins.	0.65	
		 12.10 Product packaging must the same as specified on packaging specification sheet. 12.11 Product dimension and structure must conform to product specification sheet. 	0.65	





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11.Precautions in use of OLED Modules

- (1) Avoid applying excessive shocks to module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of OLED display module.
- (3) Don't disassemble the OLED display module.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist OLED display module.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) It's pretty common to use "Screen Saver" to extend the lifetime and Don't use fix information for long time in real application.
- (9) Don't use fixed information in OLED panel for long time, that will extend "screen burn" effect time..
- (10) Raystar has the right to change the passive components, including R2and R3 adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)

(11) Raystar have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Raystar have the right to modify the version.)

11.1 Handling Precautions

- (1) Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position.
- (2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
- (3) If pressure is applied to the display surface or its neighborhood of the OLED display module, the cell structure may be damaged and be careful not to apply pressure to these sections.
- (4) The polarizer covering the surface of the OLED display module is soft and easily scratched. Please be careful when handling the OLED display module.
- (5) When the surface of the polarizer of the OLED display module has soil, clean the surface. It takes advantage of by using following adhesion tape.
- * Scotch Mending Tape No. 810 or an equivalent

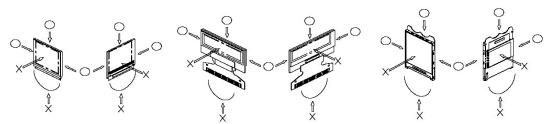
Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent Also, pay attention that the following liquid and solvent may spoil the polarizer:

* Water

* Ketone

- * Aromatic Solvents
- (6) Hold OLED display module very carefully when placing OLED display module into the System housing. Do not apply excessive stress or pressure to OLED display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.

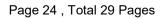




- (7) Do not apply stress to the LSI chips and the surrounding molded sections.
- (8) Do not disassemble nor modify the OLED display module.
- (9) Do not apply input signals while the logic power is off.
- (10) Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity.
- * Be sure to make human body grounding when handling OLED display modules.
- * Be sure to ground tools to use or assembly such as soldering irons.
- * To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
- * Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film.
- (11) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OLED display module has been stored surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5.
- (12) If electric current is applied when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

11.2 Storage Precautions

- (1) When storing OLED display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. And, also, avoiding high temperature and high humidity environment or low temperature (less than 0°C) environments.(We recommend you to store these modules in the packaged state when they were shipped from Raystar Optronics Inc. At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.
- (2) If electric current is applied when water drops are adhering to the surface of the OLED display module, when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.



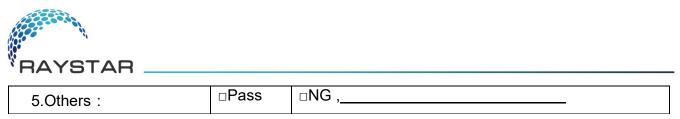


11.3 Designing Precautions

- (1) The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, panel damage may be happen.
- (2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the VIL and VIH specifications and, at the same time, to make the signal line cable as short as possible.
- (3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (VDD). (Recommend value: 0.5A)
- (4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- (5) As for EMI, take necessary measures on the equipment side basically.
- (6) When fastening the OLED display module, fasten the external plastic housing section.
- (7) If power supply to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module. Connection (contact) to any other potential than the above may lead to rupture of the IC.



	Page: 1
le Sample	e Estimate Feedback Sheet
□ Pass	□NG ,
	□NG ,
	□NG ,
	□NG ,
□ Pass	□NG ,
□Pass	□NG ,
Pass	□NG ,
□ Pass	□NG ,
<u>on</u> :	
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
1	
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
	 Pass



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Module Number : ____

Pa	g	e	:	2

4、Electronic Characteristics of Module :

□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
□Pass	□NG ,
	□Pass □Pass □Pass □Pass □Pass □Pass

5、<u>Summary</u>:

Sales signature : _____ Customer Signature : _____



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