RAYSTAR

SPECIFICATION

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OLED SPECIFICATION

Model No:

REG005016AWPP5N00000

CUSTOMER:

APPROVED BY	,		
PCB VERSION			
DATE			
FOR CUSTOMER USE	ONLY	662	
SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

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_APPROVAL FOR SPECIFICATIONS AND SAMPLE



1. Revision History

VERSION	DATE	REVISED PAGE NO.	Note
0	2014/06/30		First release
A	2016/06/01		Modify Static
			electricity test
В	2019/05/31		Drawing Add PIN
			Detail Dimension &
			Modify Reliability test
			Condition



Contents

- 1. General Specification
- 2.Module Classification Information
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- 4.Contour Drawing & Block Diagram
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- 9.Reliability
- 10.Inspection specification
- 11.Precautions in use of OLED Modules



1.General Specification

The Features is described as follow:

- Module dimension: 58.0 x 32.0 x 10.0 (MAX) mm
- View area: 38.0 x 16.0 mm
- Active area: 29.96 x 11.16 mm
- Dot Matrix: 50*16
- Dot size: 0.56 x 0.66 mm
- Dot pitch: 0.60 x 0.70 mm
- Duty: 1/16
- Emitting Color: OLED , White
- IC: RS0010
- Interface: 6800
- Size: 1.26 inch



2.Module Classification information

1	2	3	4	5	6	7	8	9	10	11	12	13	14
R	E	G	005016	А	W	Р	Р	5	Ν	0	0	0	00

1	Brand : Raysta	r Optronics Inc.			\sim
2	E : OLED				
3	Display Type	C : COB Character X : COG P : COG + FR + PCB A : COG + PCB			DB Graphic DG + FR B
4	Dot Matrix : 5				
5	Series				
6	Emitting Color	B : BlueVG : GreenY	R : Red V : White (: Yellow K : Dual Color		C : Full Color
7	Polarizer	P : With Polarizer; N A : Anti-glare Polariz		zer	
8	Display Mode			х	
9	Driver Voltage	3:3.0~3.3V ; 5:	5.0V		
10	Touch Panel	N : Without touch pa	nel; T: With tou	ch pane	
11	Product type	0 : Standard 1 : Daylight Readable 2 : Transparent OLE 3 : Flexible OLED (F 4 : OLED Lighting	D (TOLED)	6	
12	Inspection Gra	Y : Consumer grade			
13	Option	0 : Default ; F : ZIF F	FPC ; H ∶ Hot ba	ar FPC	; D : Demo Kit
14	Serial No.	Serial number(00~ZZ	<u>(</u>)	1	1000000

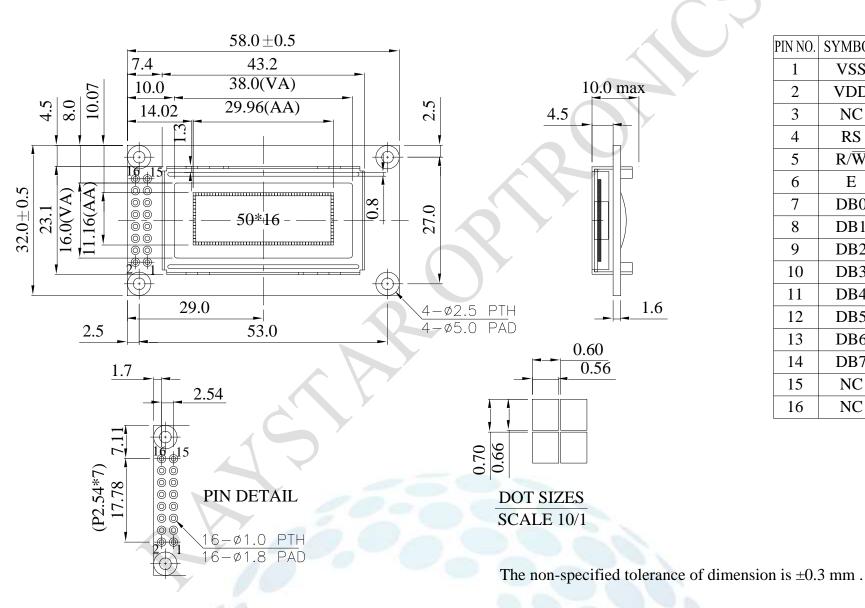


3.Interface Pin Function

Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply Voltage for logic
3	NC	—	No Connection
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(Module→MPU) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	NC	-	No Connection
16	NC	-	No Connection



4.Contour Drawing & Block Diagram



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PIN NO. SYMBOL

1

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VSS

VDD

NC

RS

 R/\overline{W}

Е

DB0

DB1

DB2

DB3

DB4

DB5

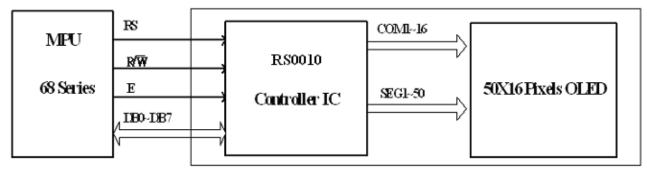
DB6

DB7

NC

NC





Address Format	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
GXA(Graphic X-axis Address)	1	ADD6	ADD5	ADD4	ADD3	ADD2	ADD1	ADD0
GYA(Graphic Y-axis Address)	0	1	0	0	0	0	0	OGA0

	1	2	3	4	··· · ··	··· - ··	47	48	49	50
CGA=0	GXA=10000000 GYA=01000000	GXA=10000001 GYA=01000000	GXA=10000010 GYA=01000000	GXA=10000011 GYA=01000000			GXA=10101110 GYA=01000000	GXA=10101111 GYA=01000000	GXA=10110000 GYA=01000000	GXA=10110001 GYA=01000000
CGA=1	GXA=10000000 GYA=01000001	GXA=10000001 GYA=01000001	GXA=10000010 GYA=01000001	GXA=10000011 GYA=01000001			GXA=10101110 GYA=01000001	GXA=10101111 GYA=01000001	ЗХА=10110000 GYA=01000001	GXA=10110001 GYA=01000001



5.Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Notes
Operating Temperature	TOP	-40	+80	°C	-
Storage Temperature	TST	-40	+85	°C	
Supply Voltage For Logic	VDD-VSS	-0.3	5.3	V	-

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6.Electrical Characteristics

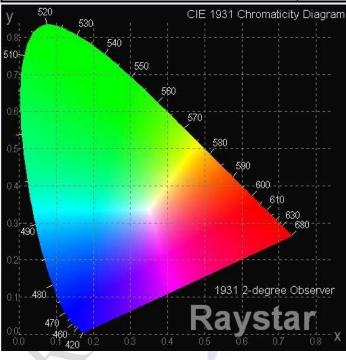
ltem	Symbol	Condition	Min	Тур	Мах	Unit
Supply Voltage For Logic	VDD-VSS	_	4.8	5.0	5.3	<
Input High Volt.	VIH	_	0.8xVDD	_	VDD	V
Input Low Volt.	VIL	_	GND	_	0.2xVDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8xVDD	_	VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND		0.2xVDD	V
50% Check Board Operating Current	IDD	VDD=5V	15	19	24	mA

Note: In order to avoid any possible damages, 3V or 3.3V logic I/O for VDD 5V OLED module is not recommended.



7.Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ	—	160		-	deg
view / ligic	(H)φ	_	160	_	-	deg
Contrast Ratio	CR	Dark	2000:1	-	-	
Response Time	T rise	—	_	10		μs
	T fall	—	-	10		μs
Display with 50% check E	Board Brightness	;	60	80		cd/m2
CIEx(White)		(CIE1931)	0.26	0.28	0.30	_
CIEy(White)		(CIE1931)	0.30	0.32	0.34	—





8.OLED Lifetime

ITEM	Conditions	Min	Тур	Remark
Operating Life Time	Ta=25°C / Initial 50% check Board Typical Brightness Value	40,000 Hrs	50,000 Hrs	Note

Note:

- 1. Life time is defined the amount of time when the luminance has decayed to <50% of the initial value.
- 2. This analysis method uses life data obtained under accelerated conditions to extrapolate an estimated probability density function (*pdf*) for the product under normal use conditions.
- 3. Screen saving mode will extend OLED lifetime.



9.Reliability

Content of Reliability Test

Test Item	Content of Test	Test Condition	Applicable Standard
High Temperature storage	Endurance test applying the high storage temperature for a long time.	85°C 240hrs	- (
_ow Temperature storage	Endurance test applying the low storage temperature for a long time.	-40°C 240hrs	-
ligh Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80°C 240hrs	-
₋ow ſemperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-40°C 240hrs	
High Femperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60°C,90%RH 240hrs	
High Femperature/ Humidity Operation	Endurance test applying the high temperature and high humidity Operation for a long time.	60°C,90%RH 120hrs	
emperature Cycle	Endurance test applying the low and high temperature cycle. -40°C 25°C 80°C 30min <u>5min 30min</u>	-40°C /80°C 30 cycles	
Mechanical Te	st	0	
/ibration test	Endurance test applying the vibration during transportation and using.	Frequency:10~55Hz amplitude:1.5mm Time:0.5hrs/axis Test axis:X,Y,Z	-2
Others	· · · · · · · · · · · · · · · · · · ·		
Static electricity test	Endurance test applying the electric stress to the finished product housing.	Air Discharge model ±4kv,10 times	_

*** Supply voltage for OLED system =Operating voltage at 25°C



Test and measurement conditions

- 1. All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room temperature for 2 hrs prior to conducting the failure test at 23±5°C; 55±15% RH.
- 2. All-pixels-on is used as operation test pattern.
- 3. The degradation of Polarizer are ignored for High Temperature storage, High Temperature/ Humidity Storage, Temperature Cycle

Evaluation criteria

- 1. The function test is OK.
- 2. No observable defects.
- 3. Luminance: > 50% of initial value.
- 4. Current consumption: within ± 50% of initial value.

APPENDIX:

RESIDUE IMAGE

Because the pixels are lighted in different time, the luminance of active pixels may reduce or differ from inactive pixels. Therefore, the residue image will occur. To avoid the residue image, every pixel needs to be lighted up uniformly.



10.Inspection specification

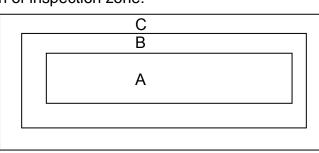
Inspection Standard:

MIL-STD-105E table normal inspection single sample level II.

Definition

1 Major defect : The defect that greatly affect the usability of product.

2 Minor defect : The other defects, such as cosmetic defects, etc. Definition of inspection zone:



Zone A: Active Area

Zone B: Viewing Area except Zone A

Zone C: Outside Viewing Area

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble of quality and assembly to customer`s product.

Inspection Methods

- 1 The general inspection : Under fluorescent light illumination: 750~1500 Lux, about 30cm viewing distance, within 45° viewing angle, under 25±5°C.
- 2 The luminance and color coordinate inspection : By SR-3 or BM-7 or the equal equipments, in the dark room, under 25±5°C.

NO	Item	Criterion	AQL
01	Electrical Testing	 1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 OLED viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect. 	0.65
02	Black or white spots on OLED (display only)	 2.1 White and black spots on display ≦0.25mm, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm. 	2.5



NO	Item	Criterion			AQL		
03	OLED black spots, white spots, contamin ation (non- display)	3.1 Round type : As following drawing $\Phi=(x + y) / 2$	SIZE $\Phi \le 0.10$ $0.10 < \Phi \le 0.20$ $0.20 < \Phi \le 0.25$ $0.25 < \Phi$		Acceptable QTY Accept no dense 2 1 0	Zone A+ B, A+ B A+ B A+ B	2.5
		3.2 Line type : (As following drawing) $\begin{array}{c} \hline W \\ \hline L \\ \hline \\ \hline$					2.5
		 L≦3.0 L≦2.5	0 0.02 <w≦0< td=""><td></td><td>Accept no dense 2 As round type</td><td>A+B A+B A+B</td><td></td></w≦0<>		Accept no dense 2 As round type	A+B A+B A+B	
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.	Size Φ Φ \leq 0.20 0.20<Φ \leq 0.50 0.50<Φ \leq 1.00 1.00<Φ		cceptable Q TY ccept no dense 3 2 0 3	Zone A+B A+B A+B A+B	2.5
05	Scratches	Follow NO.3 OLED black spots, white spots, contamination.					



NO	Item	Criterion			AQL
		L: Electrode pad leng 6.1 General glass chi	r: Chip width z: Cl : Glass thickness a: o th: o : urface and crack betw		2.5
		z: Chip thickness $Z \le 1/2t$	y: Chip width Not over viewing area	x: Chip length a x≦1/8a	
	Chipped	$1/2t < z \leq 2t$	Not exceed 1/3k	x≦1/8a	
	glass	\odot If there are 2 or mo	re chips, x is total leng	of each chip.	
06		6.1.2 Corner crack:			2.5
		z: Chip thickness Z≦1/2t	y: Chip width Not over viewing area	x: Chip lengtha $x \le 1/8a$	
		$\frac{z = 1/2t}{1/2t < z \le 2t}$	Not exceed 1/3k	x≦1/8a	
			nore chips, x is the tota		
	Glass crack		: Glass thickness a: (th rminal :	hip thickness OLED side length	2.5
		y: Chip width	x: Chip length	z: Chip thickness	
		y≦0.5mm	x≦1/8a	$0 < z \leq t$	00
				100000	



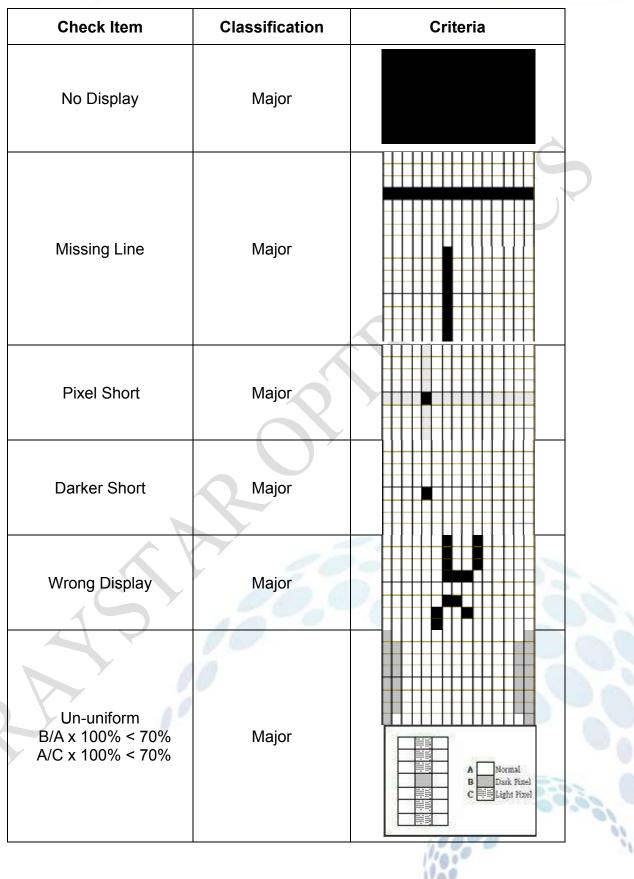
NO	Item	Criterion	AQL
06	Glass crack	 6.2.2 Non-conductive portion: 6.2.2 Non-conductive portion: L y: Chip width x: Chip length z: Chip thickness y≤ L x≤1/8a 0 < z≤ t ○ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙ If the product will be heat sealed by the customer, the alignment mark not be damaged. 6.2.3 Substrate protuberance and internal crack. 	2.5
07	Cracked glass	The OLED with extensive crack is not acceptable.	2.5
08	Backlight elements	 8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using OLED spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong. 	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination.9.2 Bezel must comply with job specifications.	2.5 0.65



NO	Item	Criterion	AQL
		10.1 COB seal may not have pinholes larger than 0.2mm or contamination.	2.5
10	PCB , COB	 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 	2.5 0.65 2.5 2.5 0.65 0.65
		10.8 If solder gets on bezel tab pads, OLED pad, zebra pad or screw hold pad, make sure it is smoothed down.11.1 No un-melted solder paste may be present on the PCB.	2.5
11	Soldering	 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB. 	2.5 2.5 0.65
		 12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP. 12.2 No cracks on interface pin (OLB) of TCP. 	2.5 0.65
12	General appearance	 12.3 No contamination, solder residue or solder balls on product. 12.4 The IC on the TCP may not be damaged, circuits. 12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever. 	2.5 2.5 2.5
		 12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color. 12.7 Sealant on top of the ITO circuit has not hardened. 12.8 Pin type must match type in specification sheet. 12.9 OLED pin loose or missing pins. 12.10 Product packaging must the same as specified on packaging specification sheet. 12.11 Product dimension and structure must conform to product specification sheet. 	2.5 2.5 0.65 0.65 0.65 0.65

0







11.Precautions in use of OLED Modules

- (1) Avoid applying excessive shocks to module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of OLED display module.
- (3) Don't disassemble the OLED display module.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist OLED display module.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) It's pretty common to use "Screen Saver" to extend the lifetime and Don't use fix information for long time in real application.
- (9) Don't use fixed information in OLED panel for long time, that will extend "screen burn" effect time..
- (10) Raystar has the right to change the passive components, including R2and R3 adjust resistors. (Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)

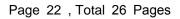
(11) Raystar have the right to change the PCB Rev. (In order to satisfy the supplying stability, management optimization and the best product performance...etc, under the premise of not affecting the electrical characteristics and external dimensions, Raystar have the right to modify the version.)

11.1 Handling Precautions

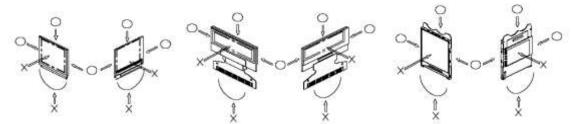
- (1) Since the display panel is being made of glass, do not apply mechanical impacts such us dropping from a high position.
- (2) If the display panel is broken by some accident and the internal organic substance leaks out, be careful not to inhale nor lick the organic substance.
- (3) If pressure is applied to the display surface or its neighborhood of the OLED display module, the cell structure may be damaged and be careful not to apply pressure to these sections.
- (4) The polarizer covering the surface of the OLED display module is soft and easily scratched. Please be careful when handling the OLED display module.
- (5) When the surface of the polarizer of the OLED display module has soil, clean the surface. It takes advantage of by using following adhesion tape.
- * Scotch Mending Tape No. 810 or an equivalent

Never try to breathe upon the soiled surface nor wipe the surface using cloth containing solvent Also, pay attention that the following liquid and solvent may spoil the polarizer:

- * Water
- * Ketone
- * Aromatic Solvents
- (6) Hold OLED display module very carefully when placing OLED display module into the System housing. Do not apply excessive stress or pressure to OLED display module. And, do not over bend the film with electrode pattern layouts. These stresses will influence the display performance. Also, secure sufficient rigidity for the outer cases.







- (7) Do not apply stress to the LSI chips and the surrounding molded sections.
- (8) Do not disassemble nor modify the OLED display module.
- (9) Do not apply input signals while the logic power is off.
- (10) Pay sufficient attention to the working environments when handing OLED display modules to prevent occurrence of element breakage accidents by static electricity.
- * Be sure to make human body grounding when handling OLED display modules.
- * Be sure to ground tools to use or assembly such as soldering irons.
- * To suppress generation of static electricity, avoid carrying out assembly work under dry environments.
- * Protective film is being applied to the surface of the display panel of the OLED display module. Be careful since static electricity may be generated when exfoliating the protective film.
- (11) Protection film is being applied to the surface of the display panel and removes the protection film before assembling it. At this time, if the OLED display module has been stored surface of the display panel after removed of the film. In such case, remove the residue material by the method introduced in the above Section 5.
- (12) If electric current is applied when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful to avoid the above.

11.2 Storage Precautions

- (1) When storing OLED display modules, put them in static electricity preventive bags avoiding exposure to direct sun light nor to lights of fluorescent lamps. And, also, avoiding high temperature and high humidity environment or low temperature (less than 0°C) environments.(We recommend you to store these modules in the packaged state when they were shipped from Raystar Optronics Inc. At that time, be careful not to let water drops adhere to the packages or bags nor let dewing occur with them.
- (2) If electric current is applied when water drops are adhering to the surface of the OLED display module, when the OLED display module is being dewed or when it is placed under high humidity environments, the electrodes may be corroded and be careful about the above.



11.3 Designing Precautions

- (1) The absolute maximum ratings are the ratings which cannot be exceeded for OLED display module, and if these values are exceeded, panel damage may be happen.
- (2) To prevent occurrence of malfunctioning by noise, pay attention to satisfy the VIL and VIH specifications and, at the same time, to make the signal line cable as short as possible.
- (3) We recommend you to install excess current preventive unit (fuses, etc.) to the power circuit (VDD). (Recommend value: 0.5A)
- (4) Pay sufficient attention to avoid occurrence of mutual noise interference with the neighboring devices.
- (5) As for EMI, take necessary measures on the equipment side basically.
- (6) When fastening the OLED display module, fasten the external plastic housing section.
- (7) If power supply to the OLED display module is forcibly shut down by such errors as taking out the main battery while the OLED display panel is in operation, we cannot guarantee the quality of this OLED display module. Connection (contact) to any other potential than the above may lead to rupture of the IC.



R# - 1		Page:
		le Estimate Feedback Sheet
Module Number :		
1 • Panel Specification :	-	
1. Panel Type :	Pass	□NG ,
2. Numbers of Pixel :	Pass	□NG ,
3. View Area :	Pass	□NG ,
4. Active Area :	Pass	□NG ,
5.Emitting Color :	Pass	□NG ,
6.Uniformity :	□Pass	□NG ,
7.Operating Temperature :	Pass	□NG ,
		Y III
8.Storage Temperature :	Pass	□NG ,
9.Others :		
2 . Mechanical Specification	:	
1. PCB Size :	□Pass	□NG ,
2.Frame Size :	□Pass	□NG ,
3.Materal of Frame :	□Pass	□NG ,
4.Connector Position :	□Pass	□NG ,
5.Fix Hole Position :	□Pass	□NG ,
6. Thickness of PCB :	□Pass	□NG ,
7. Height of Frame to PCB :	□Pass	□NG ,
8.Height of Module :	□Pass	□NG ,
9.Others :	□Pass	□NG ,
3 · <u>Relative Hole Size</u> :		
1.Pitch of Connector :	□Pass	□NG ,
2.Hole size of Connector :	□Pass	□NG ,
3.Mounting Hole size :	□Pass	□NG ,
4.Mounting Hole Type :	□Pass	□NG ,
5.Others :	□Pass	□NG ,

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Page: 1



Module Number : 4 · Electronic Characteristics of Module : 1.Input Voltage : □NG , □Pass 2.Supply Current : □NG , □Pass 3.Driving Voltage for □Pass □NG ,___ OLED : 4.Contrast for OLED : □Pass □NG ,__ 5.Negative Voltage Output : □Pass □NG , 6.Interface Function : □Pass □NG , □NG ,_ 7.ESD test : □Pass 8.Others: □Pass □NG , 5 \ Summary : Sales signature : Customer Signature : Date : 1

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