SIEMENS

Data sheet

6ES7215-1HG40-0XB0

SIMATIC S7-1200, CPU 1215C, COMPACT CPU, DC/DC/RELAY, 2 PROFINET PORT, ONBOARD I/O: 14 DI 24V DC; 10 DO RELAY 2A, 2 AI 0-10V DC, 2 AO 0-20MA DC, POWER SUPPLY: DC 20.4 -28.8 V DC, PROGRAM/DATA MEMORY: 125 KB



General information	
Product type designation	CPU 1215C DC/DC/relay
Firmware version	V4.2
Engineering with	
Programming package	STEP 7 V14 or higher
Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
permissible range, lower limit (DC)	20.4 V
permissible range, upper limit (DC)	28.8 V
Reverse polarity protection	Yes
Load voltage L+	
• Rated value (DC)	24 V
 permissible range, lower limit (DC) 	20.4 V
• permissible range, upper limit (DC)	28.8 V
Input current	
Current consumption (rated value)	500 mA; CPU only
Current consumption, max.	1 500 mA; CPU with all expansion modules

Output current for backplane bus (5 V DC), max. 1 600 mA; Max. 5 V DC for SM and CM Encoder supply 24 V encoder supply 24 V encoder supply 24 V encoder supply 26 V encoder supply 12 W Memory Work memory integrated expandable No Load memory integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ. for loading point arithmetic, typ. CPU-blocks Number of blocks (totat) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	Inrush current, max.	12 A; at 28.8 V DC
Output current for backplane bus (5 V DC), max. Incoder supply 24 V encoder supply 24 V encoder supply 24 V encoder supply 25 V encoder supply 26 V encoder supply 27 V L+ minus 4 V DC min. 28 Power loss 29 Power loss, typ. 12 W 12 W 12 W 12 W 12 W 13 W 14 W 15 W 15 W 16 W 16 W 17 W 18 W 18 W 18 W 19 W 19 W 19 W 10 W		
for backplane bus (5 V DC), max. Encoder supply 24 V encoder supply • 24 V L+ minus 4 V DC min. Power loss Power loss, typ. 12 W Memory Work memory • integrated • expandable Load memory • integrated • Plug-in (SIMATIC Memory Card), max. Backup • present • maintenance-free • without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) Backup • No.8 µs; / instruction for word operations, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB • Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag		0.0 A S
Encoder supply 24 V encoder supply 24 V L+ minus 4 V DC min. Power loss Power loss, typ. 12 W Memory Work memory integrated expandable Load memory integrated Plug-in (SIMATIC Memory Card), max. with SIMATIC memory card Backup present present yes with SIMATIC memory card maintenance-free without battery POU processing times for bit operations, typ. 0.08 µs; / instruction for word operations, typ. 1.7 µs; / instruction for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code	Output current	
24 V encoder supply • 24 V L+ minus 4 V DC min. Power loss Power loss, typ. 12 W Memory Work memory • integrated • expandable No Load memory • integrated • Plug-in (SIMATIC Memory Card), max. Backup • present • maintenance-free • without battery CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	for backplane bus (5 V DC), max.	1 600 mA; Max. 5 V DC for SM and CM
24 V encoder supply • 24 V L+ minus 4 V DC min. Power loss Power loss, typ. 12 W Memory Work memory • integrated • expandable No Load memory • integrated • Plug-in (SIMATIC Memory Card), max. Backup • present • maintenance-free • without battery CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	Encoder supply	
Power loss Power loss, typ. Memory Work memory integrated expandable Load memory integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery Power doperations, typ. for bit operations, typ. for word operations, typ. for word operations, typ. To floating point arithmetic, typ. DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used DBata areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag		
Power loss, typ. Memory Work memory integrated expandable Load memory integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery Pas without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used DBata areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	• 24 V	L+ minus 4 V DC min.
Power loss, typ. Memory Work memory integrated expandable Load memory integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery Pas without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used DBata areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag		
Memory Vork memory integrated expandable No Load memory integrated Plug-in (SIMATIC Memory Card), max. With SIMATIC memory card Backup present maintenance-free without battery CPU processing times for bit operations, typ. for loading point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag		40 W
Work memory ● integrated ● expandable No Load memory ● integrated ● Plug-in (SIMATIC Memory Card), max. Backup ● present ● maintenance-free ● without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB ● Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	Power loss, typ.	12 VV
integrated expandable No Load memory integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag HMbyte No No 125 kbyte No No No 14 Mbyte with SIMATIC memory card 4 Mbyte with SIMATIC memory card 9 With SIMATIC memory card 14 Mbyte with SIMATIC memory card 9 Ves 15 Ves 16 Ves 17 yes, / instruction 17 yes, / instruction 18 PS, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code	Memory	
expandable No Load memory integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery CPU processing times for bit operations, typ. for word operations, typ. 1.7 µs; / instruction for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	Work memory	
Load memory integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	• integrated	125 kbyte
integrated Plug-in (SIMATIC Memory Card), max. Backup present maintenance-free without battery Puprocessing times for bit operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag Hyes with SIMATIC memory card Pes with SIMATIC memory card ### Operations ### Operations ##	• expandable	No
Plug-in (SIMATIC Memory Card), max. with SIMATIC memory card Backup Present maintenance-free without battery Pes without battery Pes Processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. Publicks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	Load memory	
Backup ● present ● maintenance-free ● without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB ● Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	• integrated	4 Mbyte
 present maintenance-free without battery Yes without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. 2.3 μs; / instruction CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag 	 Plug-in (SIMATIC Memory Card), max. 	with SIMATIC memory card
 maintenance-free without battery CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag 	Backup	
• without battery Pes CPU processing times for bit operations, typ. 1.7 µs; / instruction for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB • Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	• present	Yes
CPU processing times for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	• maintenance-free	Yes
for bit operations, typ. for word operations, typ. for floating point arithmetic, typ. CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	without battery	Yes
for word operations, typ. 1.7 μs; / instruction 2.3 μs; / instruction CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	CPU processing times	
for floating point arithmetic, typ. 2.3 μs; / instruction DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	for bit operations, typ.	0.08 μs; / instruction
CPU-blocks Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB	for word operations, typ.	1.7 μs; / instruction
Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	for floating point arithmetic, typ.	2.3 μs; / instruction
Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used OB Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	CPU-blocks	
● Number, max. Limited only by RAM for code Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag		_
Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. Flag	ОВ	
Retentive data area (incl. timers, counters, flags), max. Flag	• Number, max.	Limited only by RAM for code
max. Flag	Data areas and their retentivity	
Flag	Retentive data area (incl. timers, counters, flags),	10 kbyte
	Flag	
Number, max. 8 kbyte; Size of bit memory address area		8 kbyte; Size of bit memory address area
Local data	Local data	
 per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class to 26: 6 KB 	• per priority class, max.	16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB
Address area	Address area	
Process image	Process image	

• Inputs, adjustable	1 kbyte
Outputs, adjustable	1 kbyte
Hardware configuration	
Hardware configuration Number of modules per system, max.	3 comm. modules, 1 signal board, 8 signal modules
Time of day	
Clock	V
Hardware clock (real-time)	Yes
Backup time	480 h; Typical
Deviation per day, max.	±60 s/month at 25 °C
Digital inputs	
Number of digital inputs	14; Integrated
 of which inputs usable for technological functions 	6; HSC (High Speed Counting)
Source/sink input	Yes
Number of simultaneously controllable inputs	
all mounting positions	
— up to 40 °C, max.	14
Input voltage	
Rated value (DC)	24 V
• for signal "0"	5 V DC at 1 mA
• for signal "1"	15 V DC at 2.5 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; 0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, selectable in groups of four
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes
for counter/technological functions	
— parameterizable	Single phase: 3 @ 100 kHz & 3 @ 30 kHz, differential: 3 @ 80 kHz & 3 @ 30 kHz
Cable length	
• shielded, max.	500 m; 50 m for technological functions
• unshielded, max.	300 m; For technological functions: No
Digital outputs	
Number of digital outputs	10; Relays
Switching capacity of the outputs	
• with resistive load, max.	2 A
• on lamp load, max.	30 W with DC, 200 W with AC
Output delay with resistive load	

• "0" to "1", max.	10 ms; max.
• "1" to "0", max.	10 ms; max.
Relay outputs	
Number of operating cycles, max.	mechanically 10 million, at rated load voltage 100 000
Cable length	, ,
• shielded, max.	500 m
• unshielded, max.	150 m
anomorada, max.	· · · · · · · · · · · · · · · · · · ·
Analog inputs	
Number of analog inputs	2
Input ranges	
Voltage	Yes
Input ranges (rated values), voltages	
• 0 to +10 V	Yes
Input resistance (0 to 10 V)	≥100k ohms
Cable length	
• shielded, max.	100 m; twisted and shielded
Analog outputs	
Analog outputs Number of analog outputs	2
Output ranges, current	_
• 0 to 20 mA	Yes
0 to 20 m/t	
Analog value generation for the inputs	
Integration and conversion time/resolution per channel	
 Resolution with overrange (bit including sign), max. 	10 bit
 Integration time, parameterizable 	Yes
 Conversion time (per channel) 	625 µs
Analog value generation for the outputs	
Integration and conversion time/resolution per channel	
 Resolution with overrange (bit including sign), 	10 bit
max.	
Encoder	
Connectable encoders	
• 2-wire sensor	Yes
1. Interface	
Interface type	PROFINET
Physics	Ethernet
Isolated	Yes
automatic detection of transmission rate	Yes
Autonegotiation	Yes
Autocrossing	Yes

Interface types	
Number of ports	2
integrated switch	Yes
Functionality	
PROFINET IO Controller	Yes
PROFINET IO Device	Yes
SIMATIC communication	Yes
Open IE communication	Yes
• Web server	Yes
Media redundancy	Yes; as MRP client
PROFINET IO Controller	
Transmission rate, max.	100 Mbit/s
Services	
— PG/OP communication	Yes
— S7 routing	Yes
Isochronous mode	No
— Open IE communication	Yes
— IRT	No
— MRP	Yes; as MRP client
— MRPD	No
— PROFlenergy	No
 Prioritized startup 	Yes
 Number of IO devices with prioritized startup, max. 	16
— Number of connectable IO Devices, max.	16
 Number of connectable IO Devices for RT, max. 	16
— of which in line, max.	16
 Activation/deactivation of IO Devices 	Yes
 Number of IO Devices that can be simultaneously activated/deactivated, max. 	8
— Updating time	The minimum value of the update time also depends on the communication component set for PROFINET IO, on the number of IO devices and the quantity of configured user data.
PROFINET IO Device	
Services	
— PG/OP communication	Yes
— S7 routing	Yes
— Isochronous mode	No
— Open IE communication	Yes
— IRT	No
— MRP	Yes; as MRP client

— MRPD	No
— PROFlenergy	Yes
— Shared device	Yes
 Number of IO Controllers with shared 	2
device, max.	

Protocols	
Supports protocol for PROFINET IO	Yes
PROFIBUS	Yes; CM 1243-5 required
AS-Interface	Yes; CM 1243-2 required
Protocols (Ethernet)	
• TCP/IP	Yes
• DHCP	No
• SNMP	Yes
• DCP	Yes
• LLDP	Yes
Open IE communication	
• TCP/IP	Yes
— Data length, max.	8 kbyte
• ISO-on-TCP (RFC1006)	Yes
— Data length, max.	8 kbyte
• UDP	Yes
— Data length, max.	1 472 byte
Web server	
User-defined websites	Yes
Further protocols	
• MODBUS	Yes

• MODBUS	Yes
Communication functions	
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes
 User data per job, max. 	See online help (S7 communication, user data size)
Web server	
• supported	Yes
Number of connections	
• overall	16; dynamically

Test commissioning functions	
Status/control	
Status/control variable	Yes
Variables	Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters

Forcing	
• Forcing	Yes
Diagnostic buffer	1.60
• present	Yes
Traces	
	2
Number of configurable Traces	
 Memory size per trace, max. 	512 kbyte
Interrupts/diagnostics/status information	
Diagnostics indication LED	
RUN/STOP LED	Yes
• ERROR LED	Yes
• MAINT LED	Yes
Integrated Functions	
Number of counters	6
Counting frequency (counter) max.	100 kHz
Frequency measurement	Yes
controlled positioning	Yes
Number of position-controlled positioning axes, max.	8
Number of positioning axes via pulse-direction	Up to 4 with SB 1222
interface	
PID controller	Yes
Number of alarm inputs	4
Potential separation	
Potential separation digital inputs	
 Potential separation digital inputs 	500V AC for 1 minute
 between the channels, in groups of 	1
Potential separation digital outputs	
Potential separation digital outputs	Relays
• between the channels	No
 between the channels, in groups of 	2
EMC	
Interference immunity against discharge of static electri	city
Interference immunity against discharge of	Yes
static electricity acc. to IEC 61000-4-2	
— Test voltage at air discharge	8 kV
 Test voltage at contact discharge 	6 kV
Interference immunity to cable-borne interference	
 Interference immunity on supply lines acc. to IEC 61000-4-4 	Yes
 Interference immunity on signal cables acc. to IEC 61000-4-4 	Yes

Interference immunity against voltage surge	
• on the supply lines acc. to IEC 61000-4-5	Yes
Interference immunity against conducted variable distu	
Interference immunity against beindeted variable distance Interference immunity against high-frequency	Yes
radiation acc. to IEC 61000-4-6	
Emission of radio interference acc. to EN 55 011	
 Limit class A, for use in industrial areas 	Yes; Group 1
• Limit class B, for use in residential areas	Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011
egree and class of protection	
Degree of protection acc. to EN 60529	
• IP20	Yes
tandards, approvals, certificates	
CE mark	Yes
UL approval	Yes
cULus	Yes
FM approval	Yes
RCM (formerly C-TICK)	Yes
KC approval	Yes
Marine approval	Yes
mbient conditions	
Free fall	
● Fall height, max.	0.3 m; five times, in product package
Ambient temperature during operation	
● min.	-20 °C
• max.	60 °C; Number of simultaneously activated inputs or outputs 7 or 5 (no adjacent points) at 60 °C horizontal or 50 °C vertical, 14 or
	10 at 55 °C horizontal or 45 °C vertical
horizontal installation, min.	-20 °C
 horizontal installation, max. 	60 °C
vertical installation, min.	-20 °C
• vertical installation, max.	50 °C
Ambient temperature during storage/transportation	
• min.	-40 °C
• max.	70 °C
Air pressure acc. to IEC 60068-2-13	
Operation, min.	795 hPa
Operation, max.	1 080 hPa
Operation, max.Storage/transport, min.	1 080 hPa 660 hPa
•	
• Storage/transport, min.	660 hPa

Relative humidity	
Operation, max.	95 %; no condensation
Vibrations	
 Vibration resistance during operation acc. to 	2 g (m/s²) wall mounting, 1 g (m/s²) DIN rail
IEC 60068-2-6	
Operation, tested according to IEC 60068-2-6	Yes
Shock testing	
 tested according to IEC 60068-2-27 	Yes; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms
Pollutant concentrations	
 SO2 at RH < 60% without condensation 	S02: < 0.5 ppm; H2S: < 0.1 ppm; RH < 60% condensation-free
Configuration	
Programming	
Programming language	
— LAD	Yes
— FBD	Yes
— SCL	Yes
Know-how protection	
 User program protection/password protection 	Yes
 Copy protection 	Yes
 Block protection 	Yes
Access protection	
 Protection level: Write protection 	Yes
 Protection level: Read/write protection 	Yes
 Protection level: Complete protection 	Yes
Cycle time monitoring	
adjustable	Yes
Dimensions	
Width	130 mm
Height	100 mm
Depth	75 mm
Weights	
Weight, approx.	585 g
last modified:	11/28/2017