## **SIEMENS**

## Data sheet

6ES7212-1AF40-0XB0

SIMATIC S7-1200F, CPU 1212 FC, COMPACT CPU, DC/DC/DC, ONBOARD I/O: 8 DI 24V DC; 6 DO 24 V DC; 2 AI 0 - 10V DC, POWER SUPPLY: DC 20.4 - 28.8 V DC, PROGRAM/DATA MEMORY 100 KB



General information		
Product type designation	CPU 1212FC DC/DC/DC	
Firmware version	V4.2	
Engineering with		
Programming package	STEP 7 V14 or higher	
Supply voltage		
Rated value (DC)		
• 24 V DC	Yes	
permissible range, lower limit (DC)	20.4 V	
permissible range, upper limit (DC)	28.8 V	
Load voltage L+		
Rated value (DC)	24 V	
<ul> <li>permissible range, lower limit (DC)</li> </ul>	20.4 V	
• permissible range, upper limit (DC)	28.8 V	
Input current		
Current consumption (rated value)	375 mA; Typical	
Inrush current, max.	12 A; at 28.8 V DC	
l²t	0.5 A <sup>2</sup> ·s	

for backplane bus (5 V DC), max.  1 000 mA; Max. 5 V DC for SM and CM  Encoder supply  24 V encoder supply  • 24 V Permissible range: 20.4V to 28.8V  Power loss  Power loss, typ.  9 W  Memory  Work memory  • integrated • expandable No  Load memory  • integrated • Plug-in (SIMATIC Memory Card), max.  Backup • present • maintenance-free • mintout battery  CPU processing times  for bit operations, typ.  for bit operations, typ.  for bit operations, typ.  1.7 µs; / instruction  for lot operations, typ.  1.7 µs; / instruction  for lotoesing point arithmetic, typ.  2.5 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 85535. There is no restriction, the entire working memory can be used  OB • Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag • Number, max.  4 kbyte; Size of bit memory address area  10 Address area  • Inputs • Outputs • Outputs  1 024 byte  • Outputs	Output current	
24 V encoder supply  • 24 V Permissible range: 20.4V to 28.8V  Power loss.  Power loss, typ.  9 W  Memory  • Integrated • expandable Load memory • Integrated • Plug-in (SIMATIC Memory Card), max.  Backup • present • maintenance-free • without battery  Poyer bord operations, typ.  for bit operations, typ.  for word operations, typ.  for word operations, typ.  1.7 µs; / instruction  for folioding point arithmetic, typ.  2.5 µs; / instruction  CPU-blocks  Number of blocks (total)  BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB • Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag • Number, max.  4 kbyte; Size of bit memory address area  • Inputs  Address area  • Inputs  1 024 byte		1 000 mA; Max. 5 V DC for SM and CM
24 V encoder supply  • 24 V Permissible range: 20.4V to 28.8V  Power loss.  Power loss, typ.  9 W  Memory  • Integrated • expandable Load memory • Integrated • Plug-in (SIMATIC Memory Card), max.  Backup • present • maintenance-free • without battery  Power loss, typ.  6 ro bit operations, typ.  6 ro tit operations, typ.  1.7 µs; / instruction  for loading point arithmetic, typ.  2.5 µs; / instruction  CPU-blocks  Number of blocks (total)  Busher, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Local data • per priority class, max.  Address area • Inputs  Power loss.  I/O address area  • Inputs		
Power loss Power loss, typ.  Memory  Work memory  • integrated • expandable Load memory • integrated • Plug-in (SIMATIC Memory Card), max.  Backup • present • maintenance-free • without battery  CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ.  CPU-blocks Number of blocks (total)  B Number, max.  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Local data • per priority class, max.  Permissible range: 20.4V to 28.8V  9 W  Memory  9 W  Memory  9 W  Memory  9 W  Memory  9 W  No  100 kbyte  100 kbyte  9 W  Memory  9 W  Memory  9 W  No  100 kbyte		
Power loss Power loss, typ. 9 W  Memory  Work memory  integrated 100 kbyte expandable Load memory  integrated 2 Mbyte ellug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup  integrated 7 Yes ellug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup  integrated 7 Yes ellug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup  integrated 7 Yes  integrated 8 Yes ellug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup  integrated 9 Yes  integrated 9 Yes ellug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup  integrated 9 Yes ellug-in (SIMATIC Memory Card), max. Pessent 9 Yes ellug-in (SIMATIC Memory Card)  integrated 9 Yes ellug-in (SIMATIC Memory Card)  integrated 9 Yes ellug-in (SIMATIC Memory card)  Backup  integrated 9 Yes ellug-in (SIMATIC Memory card)  Backup  integrated 9 Yes ellug-in (SIMATIC Memory card)  integrated 9 Yes ellug-in (SIM		Deveries ible versus 20 4)/45 20 0)/
Power loss, typ. 9 W  Memory  Work memory  Integrated 100 kbyte expandable No  Load memory  Integrated 2 Mbyte with SIMATIC memory card Backup  Plug-in (SIMATIC Memory Card), max. With SIMATIC memory card Backup  Present Yes  maintenance-free Yes  without battery Yes  Port of bit operations, typ. 0.08 µs; / instruction for floating point arithmetic, typ. 2.5 µs; / instruction  CPU-blocks  Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max. 4 kbyte; Size of bit memory address area  Local data  Per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area	• 24 V	Permissible range: 20.4V to 28.8V
Memory  Work memory  integrated expandable  No  Laad memory  integrated Plug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup  present maintenance-free without battery  CPU processing times  for bit operations, typ. for bit operations, typ. for bit operations, typ. for bit operations, typ.  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max. 4 kbyte; Size of bit memory address area  Local data per priority class, max. 16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area  I/O address area  I/O address area	Power loss	
	Power loss, typ.	9 W
	Memory	
expandable     No Load memory      integrated     Plug-in (SIMATIC Memory Card), max.  Backup     present     Yes     without battery  CPU processing times for bit operations, typ. for word operations, typ.     1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  Backup  Persont  Mumber of blocks (total)  By Part of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Butter of blocks (total)  By Part of blocks (total)  Aby Part of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Butter only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  10 kbyte  Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area		
Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  CPU processing times for bit operations, typ. for bit operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Bas, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  DB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area  I/O address area  I/O address area	• integrated	100 kbyte
integrated Plug-in (SIMATIC Memory Card), max.  Backup  present present present present problem of the present problem of the	• expandable	No
Plug-in (SIMATIC Memory Card), max.  Backup	Load memory	
Pilug-in (SIMATIC Memory Card), max.  Backup  present present maintenance-free without battery  Pes  for bit operations, typ. for word operations, typ. for floating point arithmetic, typ.  PBs, FCs, FBs, counters and timers. The maximum number of addressable blocks (total)  BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Pata areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max.  Limited only by RAM for code  10 kbyte  Number, max.  4 kbyte; Size of bit memory address area  Local data per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area  I/O address area	• integrated	2 Mbyte
present maintenance-free without battery  Pes  CPU processing times for bit operations, typ. for lot operations, typ.  1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  BBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max.  4 kbyte; Size of bit memory address area  Local data per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area  I/O address area  I/O address area	• Plug-in (SIMATIC Memory Card), max.	with SIMATIC memory card
maintenance-free     without battery  Pes  without battery  Yes  CPU processing times  for bit operations, typ.     for word operations, typ.     for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Bas, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area  I/O address area  I/O address area	Backup	
without battery     Yes  CPU processing times  for bit operations, typ.     0.08 µs; / instruction  for word operations, typ.     1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area  I/O address area	• present	Yes
For bit operations, typ.  for word operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  Local data  Per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area	• maintenance-free	Yes
for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  // O address area  I 0 24 byte	• without battery	Yes
for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  // O address area  I 0 24 byte	CPU processing times	
for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  No address area  I/O address area  I/O address area  I/O address area		0.08 μs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  NO address area  I/O address area  I/O address area	for word operations, typ.	1.7 μs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  No address area  I/O address area	for floating point arithmetic, typ.	2.5 µs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  No address area  I/O address area	CPI I-blocks	
addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  I/O address area  1 024 byte		DBs, FCs, FBs, counters and timers. The maximum number of
OB  • Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  • Number, max.  4 kbyte; Size of bit memory address area  Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  • Inputs  1 024 byte	,	
● Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  ● Number, max.  4 kbyte; Size of bit memory address area  Local data  ● per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  ● Inputs  1 024 byte		restriction, the entire working memory can be used
Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  • Number, max.  Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  • Inputs  1 024 byte	ОВ	
Retentive data area (incl. timers, counters, flags), max.  Flag  • Number, max.  Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  • Inputs  1 024 byte	• Number, max.	Limited only by RAM for code
Retentive data area (incl. timers, counters, flags), max.  Flag  • Number, max.  Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  • Inputs  1 024 byte	Data areas and their retentivity	
Flag  ● Number, max.  Local data  ● per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  ● Inputs  1 024 byte		10 kbyte
● Number, max.  Local data  ● per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  ● Inputs  1 024 byte	max.	
Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  I/O address area  • Inputs  1 024 byte	Flag	
<ul> <li>per priority class, max.</li> <li>16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB</li> <li>Address area</li> <li>I/O address area</li> <li>Inputs</li> <li>1 024 byte</li> </ul>	Number, max.	4 kbyte; Size of bit memory address area
Address area  I/O address area  Inputs  1 024 byte	Local data	
I/O address area  ● Inputs 1 024 byte	• per priority class, max.	
• Inputs 1 024 byte	Address area	
	I/O address area	
• Outputs 1 024 byte	• Inputs	1 024 byte
	Outputs	1 024 byte

Draces image	
Process image	1 kbyte
• Inputs, adjustable	
Outputs, adjustable	1 kbyte
Hardware configuration	
Number of modules per system, max.	3 comm. modules, 1 signal board, 2 signal modules
Time of day	
Clock	
Hardware clock (real-time)	Yes
Backup time	480 h; Typical
<ul> <li>Deviation per day, max.</li> </ul>	60 s/month at 25 °C
Digital inputs	
Number of digital inputs	8; Integrated
<ul> <li>of which inputs usable for technological</li> </ul>	4; HSC (High Speed Counting)
functions	
Source/sink input	Yes
Number of simultaneously controllable inputs	
all mounting positions	
— up to 40 °C, max.	8
Input voltage	
<ul><li>Rated value (DC)</li></ul>	24 V
● for signal "0"	5 V DC at 1 mA
● for signal "1"	15 V DC at 2.5 mA
Input current	
● for signal "1", typ.	1 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, selectable in groups of four
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes
for counter/technological functions	
— parameterizable	Single phase: 3 @ 100 kHz & 1 @ 30 kHz, differential: 3 @ 80 kHz & 1 @ 30 kHz
Cable length	
• shielded, max.	500 m; 50 m for technological functions
• unshielded, max.	300 m; For technological functions: No
Digital outputs	
Number of digital outputs	6
<ul><li>of which high-speed outputs</li></ul>	4; 100 kHz Pulse Train Output

Short-circuit protection	No; to be provided externally
Limitation of inductive shutdown voltage to	L+ (-48 V)
Switching capacity of the outputs	
• with resistive load, max.	0.5 A
• on lamp load, max.	5 W
Output voltage	
• for signal "0", max.	0.1 V; with 10 kOhm load
• for signal "1", min.	20 V
Output current	
• for signal "1" rated value	0.5 A
• for signal "0" residual current, max.	0.1 mA
Output delay with resistive load	
• "0" to "1", max.	1 µs
• "1" to "0", max.	5 µs
Switching frequency	
• of the pulse outputs, with resistive load, max.	100 kHz
Cable length	
• shielded, max.	500 m
• unshielded, max.	150 m
Analog inputs	
Number of analog inputs	2
Number of analog inputs Input ranges	2
	Yes
Input ranges	
Input ranges  • Voltage	
Input ranges  • Voltage Input ranges (rated values), voltages	Yes
Input ranges  • Voltage Input ranges (rated values), voltages  • 0 to +10 V	Yes
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)	Yes
Input ranges  • Voltage Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs	Yes  Yes ≥100k ohms  100 m; twisted and shielded
Input ranges  • Voltage Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.	Yes  Yes ≥100k ohms
Input ranges  • Voltage Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs	Yes  Yes ≥100k ohms  100 m; twisted and shielded
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs  Number of analog outputs	Yes  Yes ≥100k ohms  100 m; twisted and shielded
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs  Number of analog outputs  Analog value generation for the inputs	Yes  Yes ≥100k ohms  100 m; twisted and shielded
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs  Number of analog outputs  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign),	Yes  Yes  ≥100k ohms  100 m; twisted and shielded  0
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs  Number of analog outputs  Analog value generation for the inputs  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.	Yes  ≥100k ohms  100 m; twisted and shielded  0
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs  Number of analog outputs  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable	Yes  Yes  ≥100k ohms  100 m; twisted and shielded  0  10 bit  Yes
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs  Number of analog outputs  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Conversion time (per channel)	Yes  Yes  ≥100k ohms  100 m; twisted and shielded  0  10 bit  Yes
Input ranges  • Voltage  Input ranges (rated values), voltages  • 0 to +10 V  • Input resistance (0 to 10 V)  Cable length  • shielded, max.  Analog outputs  Number of analog outputs  Integration and conversion time/resolution per channel  • Resolution with overrange (bit including sign), max.  • Integration time, parameterizable  • Conversion time (per channel)  Encoder	Yes  Yes  ≥100k ohms  100 m; twisted and shielded  0  10 bit  Yes

Interface type	PROFINET
Physics	Ethernet
Isolated	Yes
automatic detection of transmission rate	Yes
Autonegotiation	Yes
Autocrossing	Yes
Interface types	
Number of ports	1
• integrated switch	Yes
Functionality	
PROFINET IO Controller	Yes
PROFINET IO Device	Yes
SIMATIC communication	Yes
Open IE communication	Yes
Web server	Yes
PROFINET IO Controller	
Transmission rate, max.	100 Mbit/s
Services	
— PG/OP communication	Yes
— S7 routing	Yes
— Isochronous mode	No
— Open IE communication	Yes
— IRT	No
— MRP	No
— MRPD	No
— PROFlenergy	No
— Prioritized startup	Yes
Number of IO devices with prioritized	16
startup, max.	
— Number of connectable IO Devices, max.	16
<ul> <li>Number of connectable IO Devices for RT,</li> </ul>	16
max.	
— of which in line, max.	16
<ul> <li>Activation/deactivation of IO Devices</li> </ul>	Yes
— Number of IO Devices that can be	8
simultaneously activated/deactivated, max.	
— Updating time	The minimum value of the update time also depends on the communication component set for PROFINET IO, on the number of IO devices and the quantity of configured user data.
PROFINET IO Device	, , , , , , , , , , , , , , , , , , , ,
Services	
— PG/OP communication	Yes
— S7 routing	Yes
<b>U</b>	

— Isochronous mode	No
— Open IE communication	Yes
— IRT	No
— MRP	No
— MRPD	No
— PROFlenergy	Yes
— Shared device	Yes
— Number of IO Controllers with shared	2
device, max.	

Protocols	
Supports protocol for PROFINET IO	Yes
PROFIBUS	Yes; CM 1243-5 required
AS-Interface	Yes; CM 1243-2 required
Protocols (Ethernet)	
• TCP/IP	Yes
• DHCP	No
• SNMP	Yes
• DCP	Yes
• LLDP	Yes
Open IE communication	
• TCP/IP	Yes
— Data length, max.	8 kbyte
• ISO-on-TCP (RFC1006)	Yes
— Data length, max.	8 kbyte
• UDP	Yes
— Data length, max.	1 472 byte
Web server	
User-defined websites	Yes
Further protocols	
• MODBUS	Yes

Communication functions	
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes
<ul> <li>User data per job, max.</li> </ul>	See online help (S7 communication, user data size)
Web server	
• supported	Yes

Test commissioning functions	
Status/control	
Status/control variable	Yes

• Variables	Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
Forcing	
• Forcing	Yes
Diagnostic buffer	
• present	Yes
Traces	
Number of configurable Traces	2
<ul> <li>Memory size per trace, max.</li> </ul>	512 kbyte
Integrated Functions	
Number of counters	4
Counting frequency (counter) max.	100 kHz
Frequency measurement	Yes
controlled positioning	Yes
Number of position-controlled positioning axes, max.	8
Number of positioning axes via pulse-direction interface	Up to 4 with SB 1222
PID controller	Yes
Number of alarm inputs	4
Number of pulse outputs	4
Limit frequency (pulse)	100 kHz
Potential separation	
Potential separation digital inputs	
<ul> <li>Potential separation digital inputs</li> </ul>	500V AC for 1 minute
<ul><li>between the channels, in groups of</li></ul>	1
Potential separation digital outputs	
<ul> <li>Potential separation digital outputs</li> </ul>	Yes
<ul><li>between the channels</li></ul>	No
<ul><li>between the channels, in groups of</li></ul>	1
Permissible potential difference	
between different circuits	500 V DC between 24 V DC and 5 V DC
EMC	
Interference immunity against discharge of static electri	city
<ul> <li>Interference immunity against discharge of static electricity acc. to IEC 61000-4-2</li> </ul>	Yes
<ul> <li>Test voltage at air discharge</li> </ul>	8 kV
<ul> <li>Test voltage at contact discharge</li> </ul>	6 kV
Interference immunity to cable-borne interference	
<ul> <li>Interference immunity on supply lines acc. to IEC 61000-4-4</li> </ul>	Yes
<ul> <li>Interference immunity on signal cables acc. to IEC 61000-4-4</li> </ul>	Yes

Interference immunity against voltage surge	
	Yes
<ul> <li>on the supply lines acc. to IEC 61000-4-5</li> <li>Interference immunity against conducted variable distur</li> </ul>	
	Yes
<ul> <li>Interference immunity against high-frequency radiation acc. to IEC 61000-4-6</li> </ul>	res
Emission of radio interference acc. to EN 55 011	
<ul> <li>Limit class A, for use in industrial areas</li> </ul>	Yes; Group 1
• Limit class B, for use in residential areas	Yes; When appropriate measures are used to ensure compliance with the limits for Class B according to EN 55011
Degree and class of protection	
Degree of protection acc. to EN 60529	
• IP20	Yes
Standards, approvals, certificates	
CE mark	Yes
UL approval	Yes
cULus	Yes
FM approval	Yes
RCM (formerly C-TICK)	Yes
KC approval	Yes
Marine approval	Yes
Highest safety class achievable in safety mode	
<ul> <li>Performance level according to ISO 13849-1</li> </ul>	PLe
• SIL acc. to IEC 61508	SIL 3
Ambient conditions	
Free fall	
• Fall height, max.	0.3 m; five times, in product package
Ambient temperature during operation	
• min.	0 °C
• max.	55 °C
• horizontal installation, min.	0 °C
horizontal installation, max.	55 °C
• vertical installation, min.	0 °C
vertical installation, max.	45 °C
Ambient temperature during storage/transportation	
• min.	-40 °C
• max.	70 °C
Air pressure acc. to IEC 60068-2-13	
Storage/transport, min.	660 hPa
Storage/transport, max.	1 139 hPa
Installation altitude, min.	-1 000 m
	2 000 m
<ul> <li>Installation altitude, max.</li> </ul>	2 000 III

Relative humidity	
Operation, max.	95 %; no condensation
Vibrations	
<ul> <li>Vibration resistance during operation acc. to IEC 60068-2-6</li> </ul>	2 g (m/s²) wall mounting, 1 g (m/s²) DIN rail
<ul> <li>Operation, tested according to IEC 60068-2-6</li> </ul>	Yes
Shock testing	
• tested according to IEC 60068-2-27	Yes; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms
Pollutant concentrations	
• SO2 at RH < 60% without condensation	S02: < 0.5 ppm; H2S: < 0.1 ppm; RH < 60% condensation-free
Configuration	
Programming	
Programming language	
— LAD	Yes; incl. failsafe
— FBD	Yes; incl. failsafe
— SCL	Yes
Know-how protection	
User program protection/password protection	Yes
Copy protection	Yes
Block protection	Yes
Cycle time monitoring	
● adjustable	Yes
Dimensions	
Width	90 mm
Height	100 mm
Depth	75 mm
Weights	
Weight, approx.	370 g
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