# **SIEMENS**

### Data sheet

## 6ES7211-1BE40-0XB0

SIMATIC S7-1200, CPU 1211C, COMPACT CPU, AC/DC/RELAY, ONBOARD I/O: 6 DI 24V DC; 4 DO RELAY 2A; 2 AI 0 - 10V DC, POWER SUPPLY: AC 85 - 264 V AC AT 47 - 63 HZ,

PROGRAM/DATA MEMORY: 50 KB



General information	
Product type designation	CPU 1211C AC/DC/relay
Firmware version	V4.2
Engineering with	
Programming package	STEP 7 V14 or higher
Supply voltage	
Rated value (AC)	
• 120 V AC	Yes
• 230 V AC	Yes
permissible range, lower limit (AC)	85 V
permissible range, upper limit (AC)	264 V
Line frequency	
• permissible range, lower limit	47 Hz
• permissible range, upper limit	63 Hz
Input current	
Current consumption (rated value)	60 mA at 120 V AC; 30 mA at 240 V AC
Current consumption, max.	180 mA at 120 V AC; 90 mA at 240 V AC
Inrush current, max.	20 A; at 264 V

Power loss, typ. 10 W  Memory  Work memory  Integrated 50 kbyte expandable No  Load memory  Integrated 1 Mbyte expandable No  Integrated Plug-in (SIMATIC Memory Card), max. With SIMATIC memory card  Backup  Present Yes expandable Yes expandable Processing times or bit operations, typ. O.08 µs; / instruction  for bit operations, typ. 1.7 µs; / instruction  for word operations, typ. 2.3 µs; / instruction  CPU-blocks  Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max. Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Limited only by Ram for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Local data  Per priority class, max. 16 kbyte; Pirority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB	l²t	0.8 A <sup>2</sup> ·s
For backplane bus (5 V DC), max.  750 mA; Max. 5 V DC for CM  Encoder supply  24 V encoder supply  20.4 to 28.8V  Power loss  Power loss, typ.  10 W  Memory  Work memory  • integrated • expandable • expandable Load memory  • integrated • Plug-in (SIMATIC Memory Card), max.  Backup  • present • maintenance-free • without battery  Power loss is girling from the pretainus, typ.  17 ys; / instruction  CPU-blocks  Number of blocks (total)  Bas, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 66536. There is no restriction, the entire working memory can be used  OB • Number, max.  Limited only by RAM for code  16 kbyte, Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	Output current	
20.4 to 28.8V  Power loss Power loss, typ. 10 W  Memory  Work memory  • Integrated • expandable No  Load memory  • Integrated So kbyte • Plug-in (SIMATIC Memory Card), max. With SIMATIC memory card  Backup • present Yes • without battery Yes • without battery Yes  * without battery No  CPU processing times  for bit operations, typ. 1.7 µs; / instruction  for word operations, typ. 2.3 µs; / instruction  CPU-blocks  Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65536. There is no restriction, the entire working memory can be used  OB • Number, max. Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag • Number, max. 4 kbyte; Size of bit memory address area  Local data • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB	·	750 mA; Max. 5 V DC for CM
20.4 to 28.8V  Power loss Power loss, typ. 10 W  Memory  Work memory  • Integrated • expandable No  Load memory  • Integrated So kbyte • Plug-in (SIMATIC Memory Card), max. With SIMATIC memory card  Backup • present Yes • without battery Yes • without battery Yes  * without battery No  CPU processing times  for bit operations, typ. 1.7 µs; / instruction  for word operations, typ. 2.3 µs; / instruction  CPU-blocks  Number of blocks (total) DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65536. There is no restriction, the entire working memory can be used  OB • Number, max. Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag • Number, max. 4 kbyte; Size of bit memory address area  Local data • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB	Funndar surah	
Power loss Power loss, typ. 10 W  Memory  Work memory  • integrated		
Power loss Power loss, typ. 10 W  Memory  Work memory  Integrated 50 kbyte 80 kbyte		20.4 to 28.8V
Power loss, typ. 10 W  Memory  Work memory  • integrated • expandable Load memory  • integrated • Plug-in (SIMATIC Memory Card), max. with SIMATIC memory card  Backup • present • present • maintenance-free • without battery  Yes  CPU processing times for bit operations, typ. 0.08 µs; / instruction for floating point arithmetic, typ. 2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  Bask CS, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB • Number, max. Limited only by RAM for code  Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max.  Flag • Number, max. 4 kbyte, Size of bit memory address area  Local data • per priority class, max.  16 kbyte, Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image		201.10 200.
Work memory  integrated expandable No  Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  CPU processing times for bit operations, typ. for word operations, typ. for word operations, typ. 1.7 µs; / instruction for floating point arithmetic, typ.  2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag Number, max.  4 kbyte, Size of bit memory address area  Process image  Address area  Process image	Power loss	
Integrated	Power loss, typ.	10 W
integrated expandable  Load memory  integrated Plug-in (SIMATIC Memory Card), max.  Backup  present maintenance-free without battery  Pres  for bit operations, typ. for word operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Backup  PNumber, max.  Limited only by RAM for code  Load ata Per priority class, max.  Address area  Process image  I Mbyte Number of blocks, max.  Number of blocks, max.  So k byte Number, max.  A k byte; Size of bit memory address area Local data Process image  Process image  Process image	Memory	
expandable     No  Load memory      integrated     Plug-in (SIMATIC Memory Card), max.  Backup      present     Yes     maintenance-free     without battery  CPU processing times  for bit operations, typ.     1.7 µs; / instruction  for word operations, typ.     1.7 µs; / instruction  CPU-blocks  Number of blocks (total)  DBS, FCS, FBS, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB     Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag     Number, max.  Local data     per priority class, max.  16 kbyte; Size of bit memory address area  Local class  Local class  Limited only class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	Work memory	
Load memory  integrated Plug-in (SIMATIC Memory Card), max.  by resent Present	• integrated	50 kbyte
integrated Plug-in (SIMATIC Memory Card), max.  Backup  present prese	• expandable	No
Plug-in (SIMATIC Memory Card), max.  Backup  present	Load memory	
Backup  • present • maintenance-free • without battery  CPU processing times for bit operations, typ. for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  • Number, max.  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag • Number, max.  Limited only by RAM for code  16 kbyte; Size of bit memory address area  Local data • per priority class, max.  Process image	• integrated	1 Mbyte
<ul> <li>present</li> <li>maintenance-free</li> <li>without battery</li> <li>Ves</li> </ul> CPU processing times for bit operations, typ. <ul> <li>0.08 μs; / instruction</li> <li>for word operations, typ.</li> <li>1.7 μs; / instruction</li> </ul> CPU-blocks Number of blocks (total) <ul> <li>DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used</li> </ul> OB <ul> <li>Number, max.</li> <li>Limited only by RAM for code</li> </ul> Data areas and their retentivity Retentive data area (incl. timers, counters, flags), max. <ul> <li>Flag</li> <li>Number, max.</li> <li>4 kbyte; Size of bit memory address area</li> </ul> Local data <ul> <li>per priority class, max.</li> <li>16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB</li> </ul> Address area Process image	<ul> <li>Plug-in (SIMATIC Memory Card), max.</li> </ul>	with SIMATIC memory card
maintenance-free     without battery  Yes  CPU processing times for bit operations, typ.     0.08 µs; / instruction     1.7 µs; / instruction     2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB     Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag     Number, max.  Local data     • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	Backup	
without battery      CPU processing times for bit operations, typ.     for word operations, typ.     for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	• present	Yes
for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  2.3 µs; / instruction  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	• maintenance-free	Yes
for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Number, max.  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  Limited only by RAM for code  10 kbyte; Size of bit memory address area  Local data  Process image  O.08 µs; / instruction  1.7 µs; / instruction  2.3 µs; / instruction  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	without battery	Yes
for bit operations, typ.  for word operations, typ.  for floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  Number, max.  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	CPU processing times	
For floating point arithmetic, typ.  CPU-blocks  Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image		0.08 μs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	for word operations, typ.	1.7 μs; / instruction
Number of blocks (total)  DBs, FCs, FBs, counters and timers. The maximum number of addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	for floating point arithmetic, typ.	2.3 µs; / instruction
addressable blocks ranges from 1 to 65535. There is no restriction, the entire working memory can be used  OB  Number, max.  Limited only by RAM for code  Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	CPU-blocks	
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Data areas and their retentivity  Retentive data area (incl. timers, counters, flags), max.  Flag  • Number, max.  Local data  • per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image		
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Flag  Number, max.  4 kbyte; Size of bit memory address area  Local data  per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	Data areas and their retentivity	
Flag  ● Number, max.  Local data  ● per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	Retentive data area (incl. timers, counters, flags),	10 kbyte
● Number, max.  Local data  ● per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	max.	
Local data	Flag	
● per priority class, max.  16 kbyte; Priority class 1 (program cycle): 16 KB, priority class 2 to 26: 6 KB  Address area  Process image	Number, max.	4 kbyte; Size of bit memory address area
to 26: 6 KB  Address area  Process image	Local data	
Process image	● per priority class, max.	
	Address area	
• Inputs, adjustable 1 kbyte	Process image	
	Inputs, adjustable	1 kbyte

Outputs, adjustable	1 kbyte
Hardware configuration	
Number of modules per system, max.	3 communication modules, 1 signal board
Time of day	
Clock	
Hardware clock (real-time)	Yes
Backup time	480 h; Typical
• Deviation per day, max.	±60 s/month at 25 °C
Digital inputs	
Number of digital inputs	6; Integrated
<ul> <li>of which inputs usable for technological functions</li> </ul>	3; HSC (High Speed Counting)
Source/sink input	Yes
Number of simultaneously controllable inputs	
all mounting positions	
— up to 40 °C, max.	6
Input voltage	
Rated value (DC)	24 V
• for signal "0"	5 V DC at 1 mA
• for signal "1"	15 V DC at 2.5 mA
Input current	
● for signal "1", typ.	4 mA; nominal
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	0.2 ms, 0.4 ms, 0.8 ms, 1.6 ms, 3.2 ms, 6.4 ms and 12.8 ms, selectable in groups of four
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes
for counter/technological functions	
— parameterizable	Single phase : 3 @ 100 kHz, differential: 3 @ 80 kHz
Cable length	
• shielded, max.	500 m; 50 m for technological functions
• unshielded, max.	300 m; For technological functions: No
Digital outputs	
Number of digital outputs	4; Relays
Switching capacity of the outputs	
• with resistive load, max.	2 A
● on lamp load, max.	30 W with DC, 200 W with AC
Output delay with resistive load	

• "0" to "1", max.	10 ms; max.
• "1" to "0", max.	10 ms; max.
Relay outputs	
Number of operating cycles, max.	mechanically 10 million, at rated load voltage 100 000
Cable length	
• shielded, max.	500 m
• unshielded, max.	150 m
Analog inputs  Number of analog inputs	2
Input ranges	2
	Yes
<ul> <li>Voltage</li> <li>Input ranges (rated values), voltages</li> </ul>	165
	Yes
• 0 to +10 V	
• Input resistance (0 to 10 V)	≥100k ohms
Cable length	400 my twisted and skielded
• shielded, max.	100 m; twisted and shielded
Analog outputs	
Number of analog outputs	0
Analog value generation for the inputs	
Integration and conversion time/resolution per channel	
<ul> <li>Resolution with overrange (bit including sign),</li> </ul>	10 bit
max.	
<ul> <li>Integration time, parameterizable</li> </ul>	Yes
<ul> <li>Conversion time (per channel)</li> </ul>	625 µs
Encoder	
Connectable encoders	
• 2-wire sensor	Yes
1. Interface	PROFINET
Interface type	PROFINET
Physics Isolated	Ethernet Yes
automatic detection of transmission rate	Yes
	Yes
Autonegotiation  Autocrossing	Yes
Interface types	163
Number of ports	1
•	No
• integrated switch	110
Functionality  • PROFINET IO Controller	Yes
PROFINET IO Controller	
PROFINET IO Device	Yes

SIMATIC communication	Yes
Open IE communication	Yes
• Web server	Yes
Media redundancy	No
PROFINET IO Controller	
Transmission rate, max.	100 Mbit/s
Services	
— PG/OP communication	Yes
— S7 routing	Yes
— Isochronous mode	No
<ul> <li>Open IE communication</li> </ul>	Yes
— IRT	No
— MRP	No
— MRPD	No
— PROFlenergy	No
<ul> <li>Prioritized startup</li> </ul>	Yes
<ul> <li>Number of IO devices with prioritized</li> </ul>	16
startup, max.	
<ul> <li>Number of connectable IO Devices, max.</li> </ul>	16
<ul> <li>Number of connectable IO Devices for RT,</li> </ul>	16
max.	
— of which in line, max.	16
<ul> <li>Activation/deactivation of IO Devices</li> </ul>	Yes
Number of IO Devices that can be	8
simultaneously activated/deactivated, max.	
— Updating time	The minimum value of the update time also depends on the communication component set for PROFINET IO, on the number
	of IO devices and the quantity of configured user data.
PROFINET IO Device	
Services	
— PG/OP communication	Yes
— S7 routing	Yes
— Isochronous mode	No
— Open IE communication	Yes
— IRT	No
— MRP	No
— MRPD	No
— PROFlenergy	Yes
— Shared device	Yes
<ul> <li>Number of IO Controllers with shared</li> </ul>	2
device, max.	

Supports protocol for PROFINET IO	Yes
PROFIBUS	Yes; CM 1243-5 required
AS-Interface	Yes; CM 1243-2 required
Protocols (Ethernet)	
• TCP/IP	Yes
• DHCP	No
• SNMP	Yes
• DCP	Yes
• LLDP	Yes
Open IE communication	
• TCP/IP	Yes
— Data length, max.	8 kbyte
<ul> <li>several passive connections per port, supported</li> </ul>	Yes
• ISO-on-TCP (RFC1006)	Yes
— Data length, max.	8 kbyte
• UDP	Yes
— Data length, max.	1 472 byte
Web server	
User-defined websites	Yes
Further protocols	
• MODBUS	Yes
Communication functions	
S7 communication	
• supported	Yes
• as server	Yes
• as client	Yes
<ul> <li>User data per job, max.</li> </ul>	See online help (S7 communication, user data size)
Web server	
• supported	Yes
Number of connections	
• overall	16; dynamically
Test commissioning functions	
Status/control	
Status/control variable	Yes
Variables	Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters
Forcing	
• Forcing	Yes
Diagnostic buffer	
• present	Yes
Traces	

Number of configurable Traces
 Memory size per trace, max.
 512 kbyte

# Interrupts/diagnostics/status information Diagnostics indication LED • RUN/STOP LED • ERROR LED • MAINT LED Yes • MAINT LED Yes

Integrated Functions	
Number of counters	3
Counting frequency (counter) max.	100 kHz
Frequency measurement	Yes
controlled positioning	Yes
Number of position-controlled positioning axes, max.	8
Number of positioning axes via pulse-direction interface	Up to 4 with SB 1222
PID controller	Yes
Number of alarm inputs	4

Potential separation	
Potential separation digital inputs	
<ul> <li>Potential separation digital inputs</li> </ul>	500V AC for 1 minute
• between the channels, in groups of	1
Potential separation digital outputs	
<ul> <li>Potential separation digital outputs</li> </ul>	Relays
• between the channels	No
<ul> <li>between the channels, in groups of</li> </ul>	1

EMC	
Interference immunity against discharge of static electric	city
<ul> <li>Interference immunity against discharge of static electricity acc. to IEC 61000-4-2</li> </ul>	Yes
Test voltage at air discharge	8 kV
<ul> <li>Test voltage at contact discharge</li> </ul>	6 kV
Interference immunity to cable-borne interference	
● Interference immunity on supply lines acc. to IEC 61000-4-4	Yes
<ul> <li>Interference immunity on signal cables acc. to IEC 61000-4-4</li> </ul>	Yes
Interference immunity against voltage surge	
• on the supply lines acc. to IEC 61000-4-5	Yes
Interference immunity against conducted variable disturbance induced by high-frequency fields	
<ul> <li>Interference immunity against high-frequency radiation acc. to IEC 61000-4-6</li> </ul>	Yes

### Emission of radio interference acc. to EN 55 011 Yes; Group 1 • Limit class A, for use in industrial areas Yes; When appropriate measures are used to ensure compliance • Limit class B, for use in residential areas with the limits for Class B according to EN 55011 Degree and class of protection Degree of protection acc. to EN 60529 Yes • IP20 Standards, approvals, certificates CE mark Yes **UL** approval Yes cULus Yes FM approval Yes RCM (formerly C-TICK) Yes KC approval Yes Marine approval Yes Ambient conditions Free fall 0.3 m; five times, in product package • Fall height, max. Ambient temperature during operation -20 °C • min. 60 °C • max. -20 °C • horizontal installation, min. 60 °C • horizontal installation, max. -20 °C • vertical installation, min. 50 °C • vertical installation, max. Ambient temperature during storage/transportation -40 °C • min. 70 °C • max. Air pressure acc. to IEC 60068-2-13 795 hPa • Operation, min. 1 080 hPa • Operation, max. 660 hPa • Storage/transport, min. 1 080 hPa Storage/transport, max. -1 000 m • Installation altitude, min. 2 000 m • Installation altitude, max. Relative humidity 95 %; no condensation • Operation, max. Vibrations 2 g (m/s²) wall mounting, 1 g (m/s²) DIN rail • Vibration resistance during operation acc. to IEC 60068-2-6

• Operation, tested according to IEC 60068-2-6

Yes

Shock testing	
• tested according to IEC 60068-2-27	Yes; IEC 68, Part 2-27 half-sine: strength of the shock 15 g (peak value), duration 11 ms
Pollutant concentrations	
<ul> <li>SO2 at RH &lt; 60% without condensation</li> </ul>	S02: < 0.5 ppm; H2S: < 0.1 ppm; RH < 60% condensation-free
Configuration	
Programming	
Programming language	
— LAD	Yes
— FBD	Yes
— SCL	Yes
Know-how protection	
<ul> <li>User program protection/password protection</li> </ul>	Yes
<ul> <li>Copy protection</li> </ul>	Yes
<ul> <li>Block protection</li> </ul>	Yes
Access protection	
Protection level: Write protection	Yes
<ul> <li>Protection level: Read/write protection</li> </ul>	Yes
<ul> <li>Protection level: Complete protection</li> </ul>	Yes
Cycle time monitoring	
● adjustable	Yes
Dimensions	
Width	90 mm
Height	100 mm
Depth	75 mm
Weights	
Weight, approx.	420 g
last modified:	11/28/2017